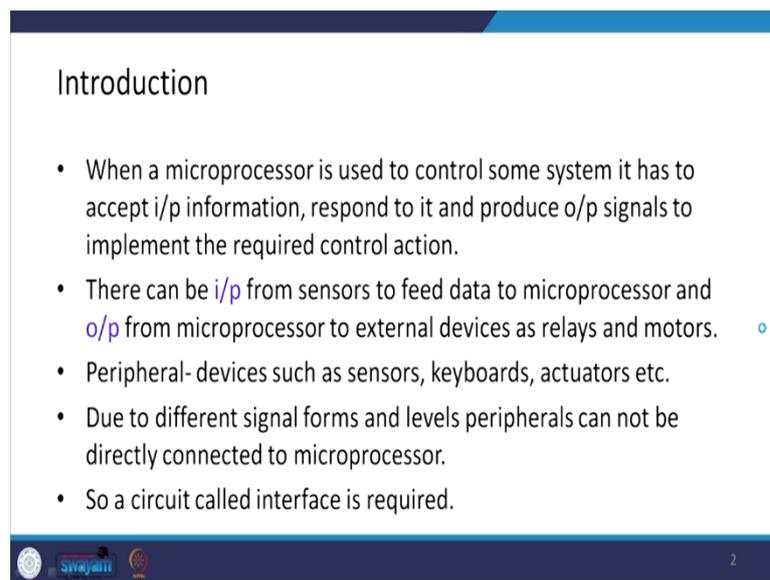


**Mechatronics**  
**Prof. Pushparaj Mani Pathak**  
**Department of Mechanical and Industrial Engineering**  
**Indian Institute of Technology, Roorkee**

**Lecture - 34**  
**Input, Output, and Communication Systems**

I welcome you all to today's NPTEL Online Certification Course lecture on Mechatronics. Today I am going to talk about the Input, Output systems, and Communication Systems. So, here I am going to talk about the two aspects. As I said, a communication system means how the devices communicate through each other. And the input-output means the connection of these input and output devices through to the microcontrollers or microprocessors.

(Refer Slide Time: 01:26)



The slide is titled "Introduction" and contains a bulleted list of five points. The first point states that a microprocessor used for control must accept input, respond, and produce output. The second point notes that input comes from sensors and output goes to devices like relays and motors. The third point lists peripheral devices such as sensors, keyboards, and actuators. The fourth point explains that due to different signal forms and levels, peripherals cannot be directly connected to the microprocessor. The fifth point concludes that an interface circuit is required. The slide footer includes the NPTEL logo, the name "Swajati", and the number "2".

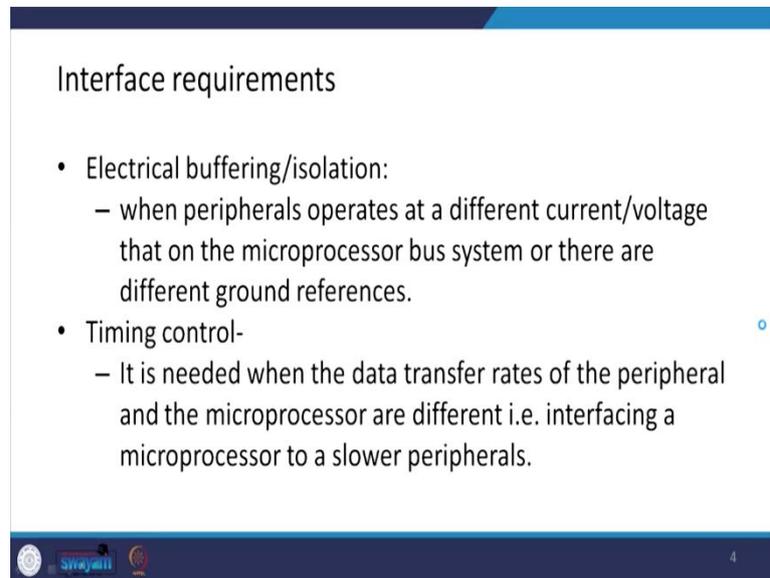
- When a microprocessor is used to control some system it has to accept i/p information, respond to it and produce o/p signals to implement the required control action.
- There can be i/p from sensors to feed data to microprocessor and o/p from microprocessor to external devices as relays and motors.
- Peripheral- devices such as sensors, keyboards, actuators etc.
- Due to different signal forms and levels peripherals can not be directly connected to microprocessor.
- So a circuit called interface is required.

When a microprocessor is used to control some system, it has to accept the input information, respond to it, and produce an output signal to implement the required control action. This is ideally what happens. So, there could be input from sensors to feed data to microprocessor and output from microprocessor to external devices such as relays, motors, and so on.

Now, peripherals such as sensors, keyboard, actuator, etcetera, are what we call we mean by the peripherals. Now, due to different signal forms and levels, peripherals cannot be directly connected to the microprocessor. So, a circuit called an interface is required.

Now, while connecting, we need to address the input and output. So, microprocessors have special input instructions and output instructions. Microprocessors use parallel ports to input or output bytes of data.

(Refer Slide Time: 02:56)

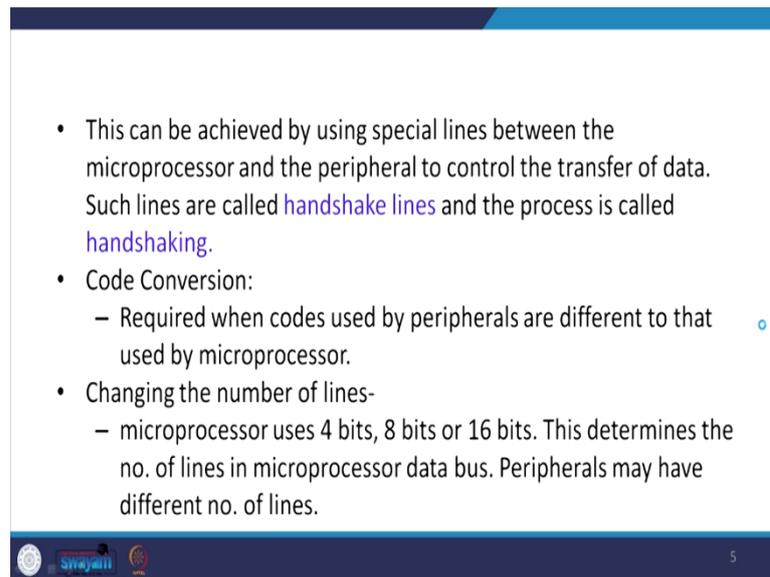


The slide is titled "Interface requirements" and contains two main bullet points. The first bullet point is "Electrical buffering/isolation:" followed by a sub-bullet "– when peripherals operates at a different current/voltage that on the microprocessor bus system or there are different ground references." The second bullet point is "Timing control-" followed by a sub-bullet "– It is needed when the data transfer rates of the peripheral and the microprocessor are different i.e. interfacing a microprocessor to a slower peripherals." The slide has a blue header and footer. The footer contains logos for "swayam" and "4".

- Interface requirements
  - Electrical buffering/isolation:
    - when peripherals operates at a different current/voltage that on the microprocessor bus system or there are different ground references.
  - Timing control-
    - It is needed when the data transfer rates of the peripheral and the microprocessor are different i.e. interfacing a microprocessor to a slower peripherals.

Now, what could be the interface requirements? As I said, we cannot connect the peripheral devices directly to a microprocessor or microcontroller, so it has to be done with the help of a certain interface. So, what could be these interface requirements? So, the first requirement could be electrically buffering and isolation. When peripherals operate at a different current or voltage than on the microprocessor bus system or there are different ground references, then we require electrical buffering or isolation. Next is for the timing control. It is needed when the data transfer rates of the peripheral and the microprocessor are different, so that is interfacing a microprocessor to a slower peripheral.

(Refer Slide Time: 04:01)



- This can be achieved by using special lines between the microprocessor and the peripheral to control the transfer of data. Such lines are called **handshake lines** and the process is called **handshaking**.
- Code Conversion:
  - Required when codes used by peripherals are different to that used by microprocessor.
- Changing the number of lines-
  - microprocessor uses 4 bits, 8 bits or 16 bits. This determines the no. of lines in microprocessor data bus. Peripherals may have different no. of lines.

This can be achieved by using special lines between the microprocessor and the peripherals to control the transfer of data. Such lines are called handshake lines, and the process is called the handshake.

Next is the code conversion. This is required when the codes used by the peripherals are different from those used by the microprocessor.

We could also have the requirement of changing the number of lines. So, the microprocessor uses 4 bits or 8 bits, or 16 bits. So, this determines the number of lines in the microprocessor data bus. Peripherals may have a different number of lines. So, we may require to change the number of lines.

(Refer Slide Time: 05:19)

Slide 6 contains two bullet points. The first bullet point is 'Serial to parallel and vice versa data transfer-' followed by a sub-bullet: '- a 8 bit microprocessor manipulates 8 bits of data at same time. This can be done by parallel data transfer where all data are send simultaneously whereas in serial data transfer signals are send one by one.' The second bullet point is 'Conversion from analogue to digital and vice versa.' The slide footer includes a logo, the text 'Swayam', and the number '6'.

- Serial to parallel and vice versa data transfer-
  - a 8 bit microprocessor manipulates 8 bits of data at same time. This can be done by parallel data transfer where all data are send simultaneously whereas in serial data transfer signals are send one by one.
- Conversion from analogue to digital and vice versa.

Serial to parallel and vice versa data transfer, an 8-bit microprocessor manipulates 8 bits of data at the same time. Now, this can be done by parallel data transfer where all the data are sent simultaneously, whereas, in serial data transfer, signals are sent one by one, and we may have the conversion from analog to digital or digital to analog. We have already seen these devices in my earlier lectures.

(Refer Slide Time: 05:40)

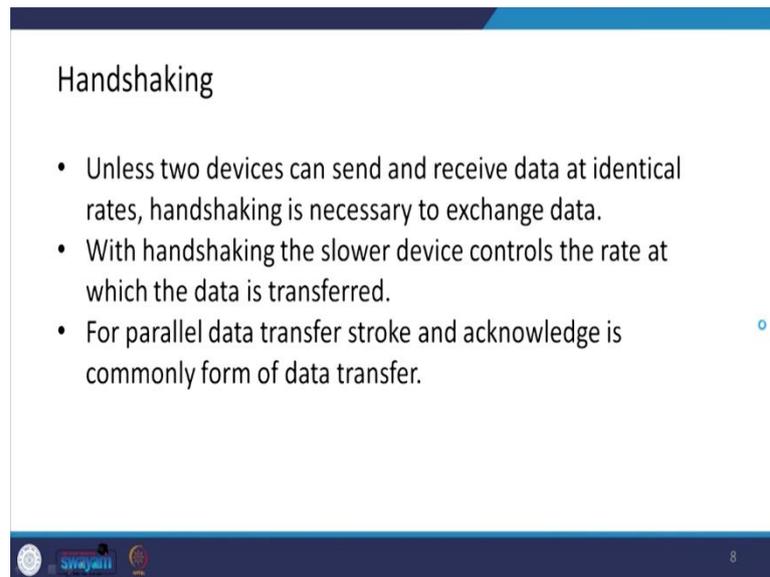
Slide 7 has a title 'Buffers' and one bullet point: 'It does not change the logic of the input but just changes the current or voltage levels. We can use amplifier.' The slide footer includes a logo, the text 'Swayam', and the number '7'.

### Buffers

- It does not change the logic of the input but just changes the current or voltage levels. We can use amplifier.

The buffers do not change the logic of the input but just change the current or voltage levels, and we can use an amplifier for this.

(Refer Slide Time: 06:10)



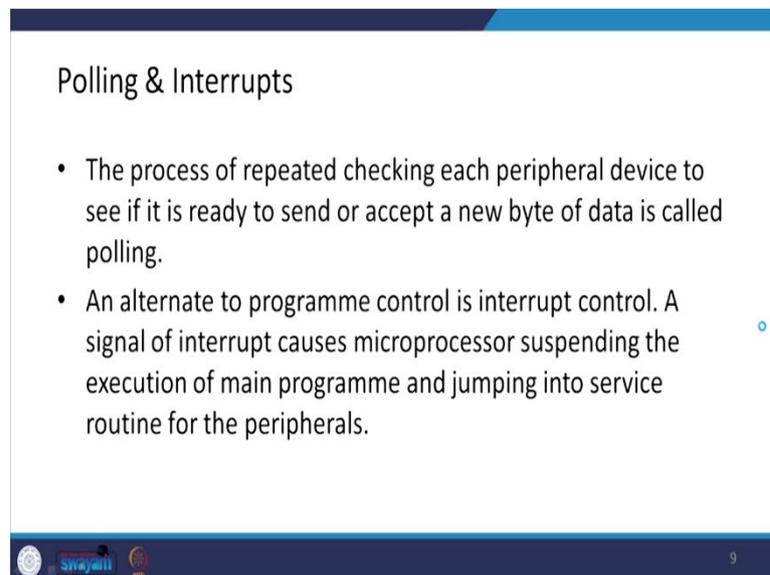
Handshaking

- Unless two devices can send and receive data at identical rates, handshaking is necessary to exchange data.
- With handshaking the slower device controls the rate at which the data is transferred.
- For parallel data transfer strobe and acknowledge is commonly form of data transfer.

8

Handshaking, as I said, unless two devices can send and receive data at identical rates, handshaking is necessary to exchange data. With handshaking, the slower device controls the rate at which the data is to be transferred, and for parallel data transfer, strobe and acknowledge is common form of data transfer.

(Refer Slide Time: 06:28)



Polling & Interrupts

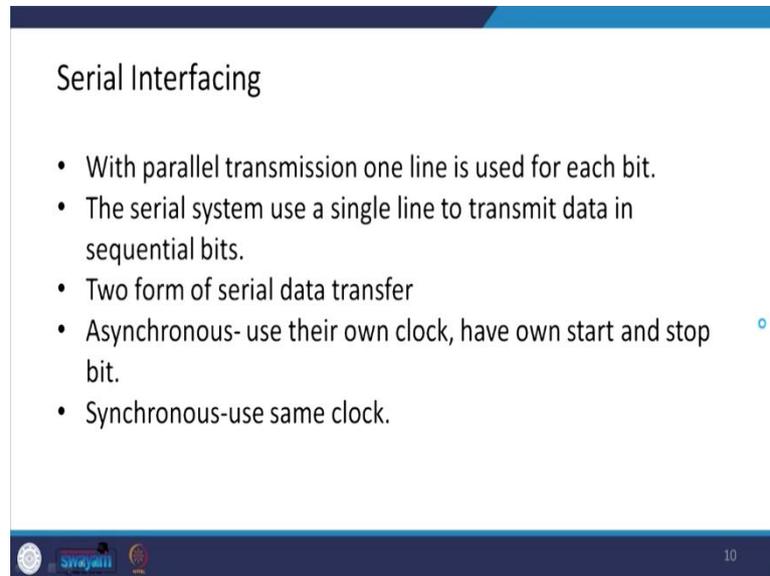
- The process of repeated checking each peripheral device to see if it is ready to send or accept a new byte of data is called polling.
- An alternate to programme control is interrupt control. A signal of interrupt causes microprocessor suspending the execution of main programme and jumping into service routine for the peripherals.

9

Polling and interrupts, the process of repeated checking is a peripheral device to see if it is ready to send or accept a new byte is called polling, and an alternate to program control is

the interrupt controller, and here, a signal of interrupt causes microprocessor is suspending the execution of the main program and jumping into the service routine for the peripherals.

(Refer Slide Time: 07:01)



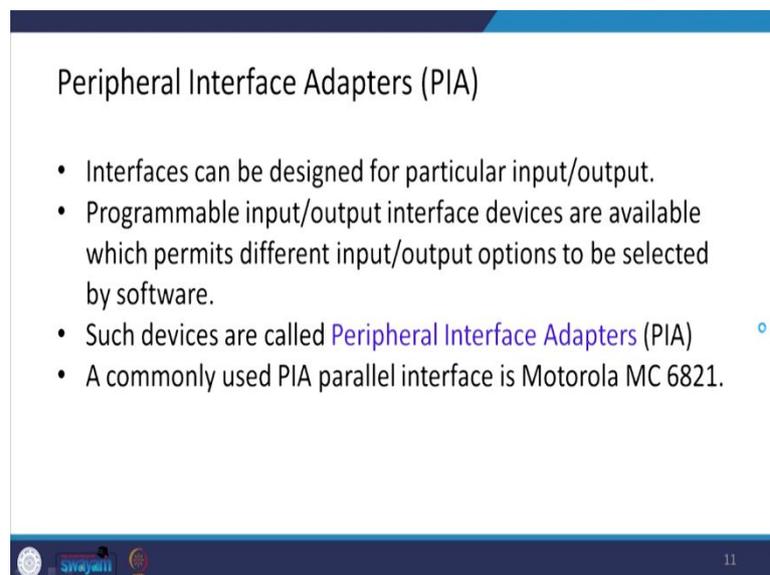
**Serial Interfacing**

- With parallel transmission one line is used for each bit.
- The serial system use a single line to transmit data in sequential bits.
- Two form of serial data transfer
- Asynchronous- use their own clock, have own start and stop bit.
- Synchronous-use same clock.

10

In serial interfacing, with the parallel transmission, one line is used for each bit, and in the serial interface system, they use a single line to transmit data in sequential bits. So, the two forms of serial data transfer are there, asynchronous and synchronous. So, the asynchronous one uses their own clock has its own start and stop bit whereas, the synchronous one, as the name indicates, use the same clock.

(Refer Slide Time: 07:39)



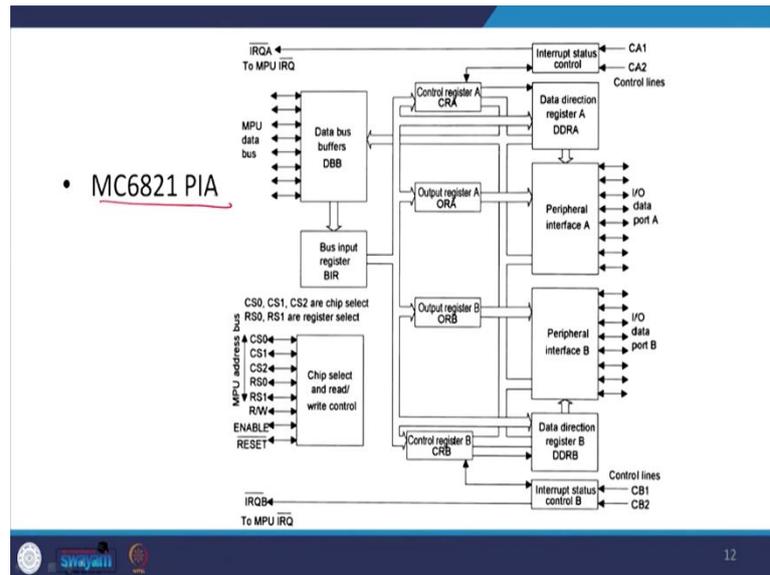
**Peripheral Interface Adapters (PIA)**

- Interfaces can be designed for particular input/output.
- Programmable input/output interface devices are available which permits different input/output options to be selected by software.
- Such devices are called **Peripheral Interface Adapters (PIA)**
- A commonly used PIA parallel interface is Motorola MC 6821.

11

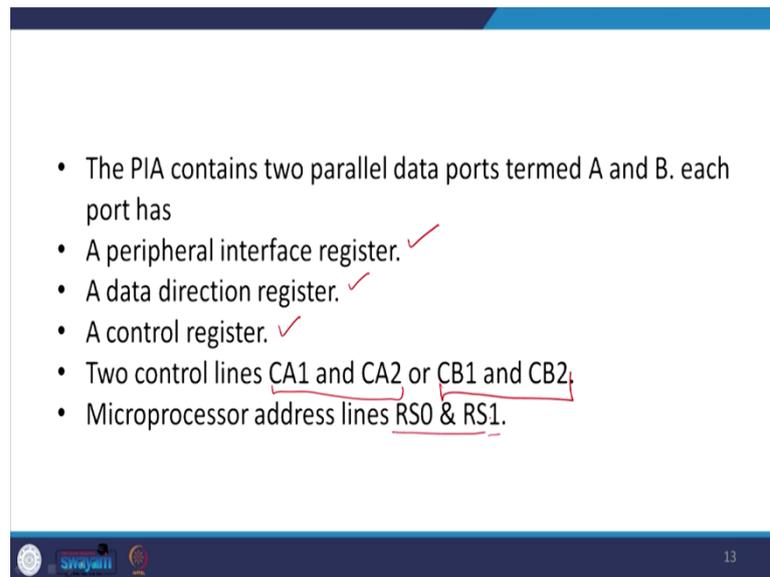
Peripheral interface adapters are, in short, they are also called PIA. So, interfaces can be designed for a particular input, output. Programmable input-output interface devices are available, which permits different input, output options to be selected by the software. Such devices are called PIA's, and a commonly used PIA parallel interface is Motorola MC 6821.

(Refer Slide Time: 08:22)



This is the schematic of the MC 6821 PIA, where you can see that there is various type of register; output register, output register B, A, control register are there, chip select read and write control, bus input register, data bus buffer is there. And here, you have input-output data port A input-output data port B. This is peripheral interface A, and this is peripheral interface B. It has the control lines here CA1, CA2 as well as CB1 and CB2. Here, they have the interrupt status control, interrupt status control over here.

(Refer Slide Time: 09:14)



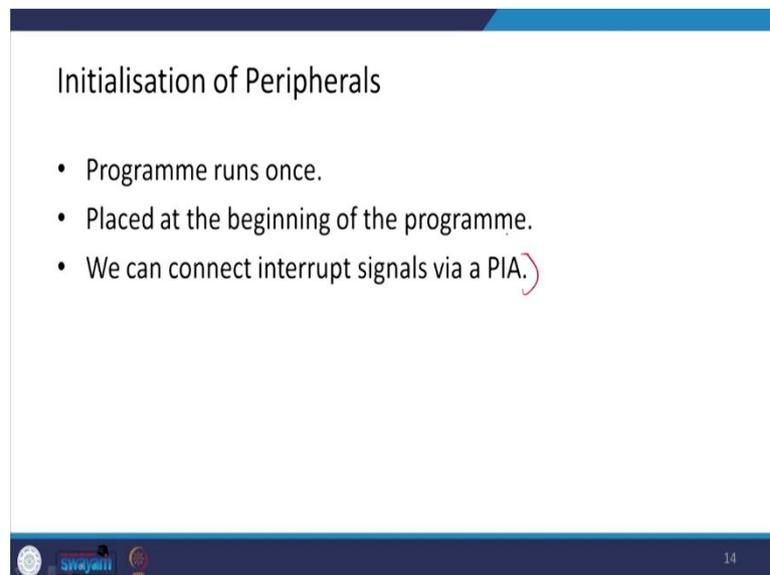
Slide 13 contains a bulleted list of PIA features. The text is as follows:

- The PIA contains two parallel data ports termed A and B. each port has
- A peripheral interface register. ✓
- A data direction register. ✓
- A control register. ✓
- Two control lines CA1 and CA2 or CB1 and CB2.
- Microprocessor address lines RS0 & RS1.

The slide footer includes the Swayam logo and the number 13.

The PIA contains two parallel data ports termed A and B, and each port has a peripheral interface register, a data direction register, and a control register, and the two control lines; CA1 and CA2 and CB1 and CB2, are there. There are microprocessor address lines RS0 and RS1.

(Refer Slide Time: 09:39)



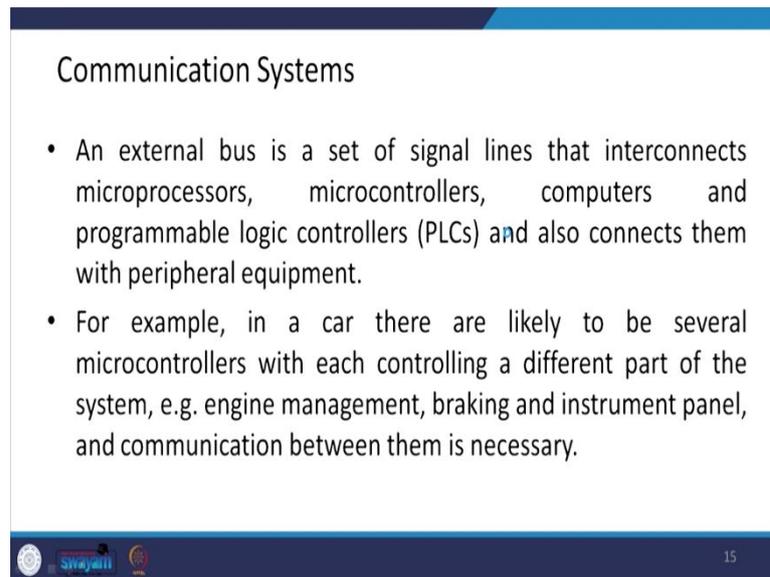
Slide 14 is titled "Initialisation of Peripherals" and contains a bulleted list:

- Programme runs once.
- Placed at the beginning of the programme.
- We can connect interrupt signals via a PIA.)

The slide footer includes the Swayam logo and the number 14.

Initialization of peripherals. So, the program runs once, placed at the beginning of the program, and we can connect interrupt signal via the PIA. Now, let us look at the communication systems.

(Refer Slide Time: 09:58)



### Communication Systems

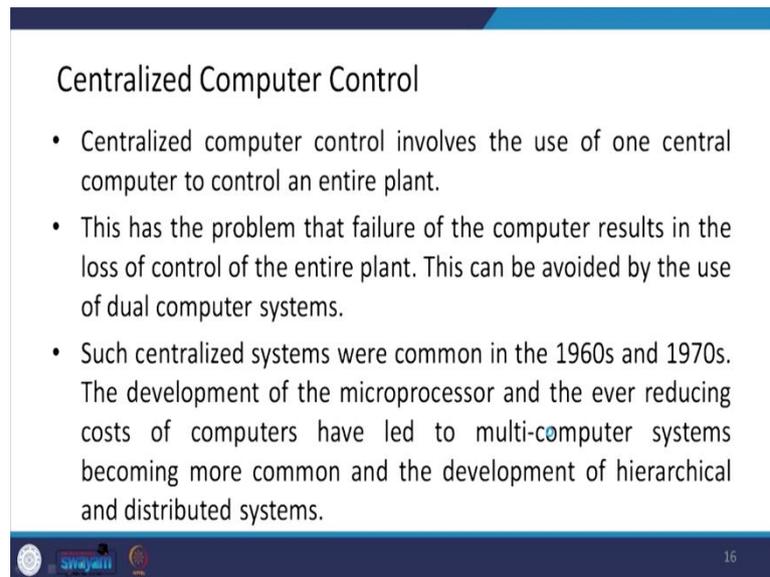
- An external bus is a set of signal lines that interconnects microprocessors, microcontrollers, computers and programmable logic controllers (PLCs) and also connects them with peripheral equipment.
- For example, in a car there are likely to be several microcontrollers with each controlling a different part of the system, e.g. engine management, braking and instrument panel, and communication between them is necessary.

Swajati 15

For those who are interested in further reading, you can refer to Mechatronics by Bolton. So, I have just given a very brief idea about these input-output devices as well as the communication systems. You can have the for the reading from that particular textbook.

Now, coming to the communication system, as I said, the devices have to communicate. So, an external bus is a set of signal lines that interconnects microprocessor, microcontroller computer, and PLCs and also connect them with the peripheral equipment. For example, in a car, there are likely to be several microcontrollers, each controlling a different part of the system, engine management system, braking system, instrument panel, communication between them is necessary.

(Refer Slide Time: 11:06)



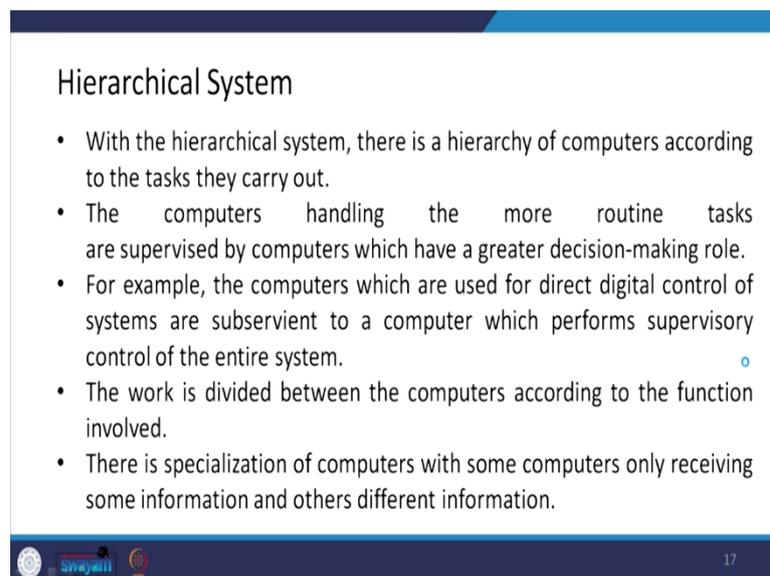
Centralized Computer Control

- Centralized computer control involves the use of one central computer to control an entire plant.
- This has the problem that failure of the computer results in the loss of control of the entire plant. This can be avoided by the use of dual computer systems.
- Such centralized systems were common in the 1960s and 1970s. The development of the microprocessor and the ever reducing costs of computers have led to multi-computer systems becoming more common and the development of hierarchical and distributed systems.

16

Centralized computer control involves the use of one central computer to control an entire plant. This has the problem that the failure of the computer results in the loss of control of the entire plant, and this can be avoided by the use of dual computer systems. Such centralized systems were common in the 1960s and '70s, and the development of the microprocessor and the ever-reducing cost of the computer have led to the multi-computer system becoming more common and the development of hierarchical and the distributed system becoming more common these days. Now, let us look at the hierarchical system.

(Refer Slide Time: 11:59)



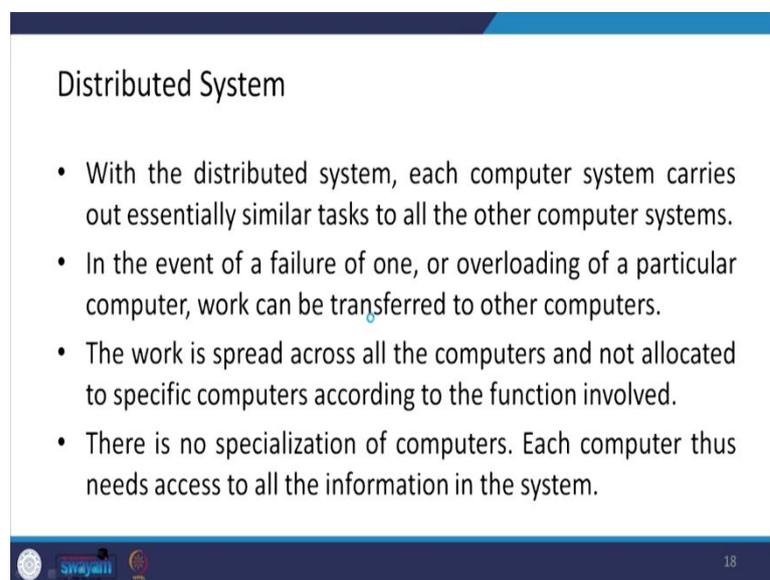
Hierarchical System

- With the hierarchical system, there is a hierarchy of computers according to the tasks they carry out.
- The computers handling the more routine tasks are supervised by computers which have a greater decision-making role.
- For example, the computers which are used for direct digital control of systems are subservient to a computer which performs supervisory control of the entire system.
- The work is divided between the computers according to the function involved.
- There is specialization of computers with some computers only receiving some information and others different information.

17

With the hierarchical system, there is a hierarchy of computers according to the task they carry out. The computer handling the more routine tasks are supervised by computers of which have a greater decision-making role. For example, the computer which is used for direct digital control of systems is subservient to a computer that performs supervisory control of the entire system, and the work is divided between the computers according to the function involved. There is a specialization of computers with some computers only receiving some information and the other the different information. Now, let us look at the distributed system.

(Refer Slide Time: 12:56)



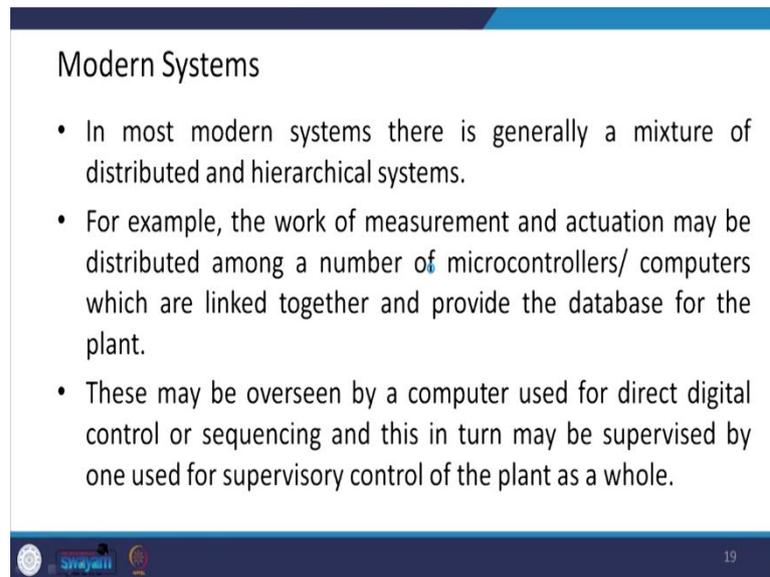
The slide is titled "Distributed System" and contains the following bullet points:

- With the distributed system, each computer system carries out essentially similar tasks to all the other computer systems.
- In the event of a failure of one, or overloading of a particular computer, work can be transferred to other computers.
- The work is spread across all the computers and not allocated to specific computers according to the function involved.
- There is no specialization of computers. Each computer thus needs access to all the information in the system.

The slide footer includes a logo on the left, the text "Swayam" in the center, and the number "18" on the right.

With the distributed system, each computer system carries out essentially similar tasks to all the other computer systems, and in the event of failure of one or overloading of a particular computer, what can be transferred to the other computers. The work is spread across all the computers and not allocated to specific computers according to the functions involved. There is no specialization of computers over here. Each computer thus needs access to all the information of the system.

(Refer Slide Time: 13:43)



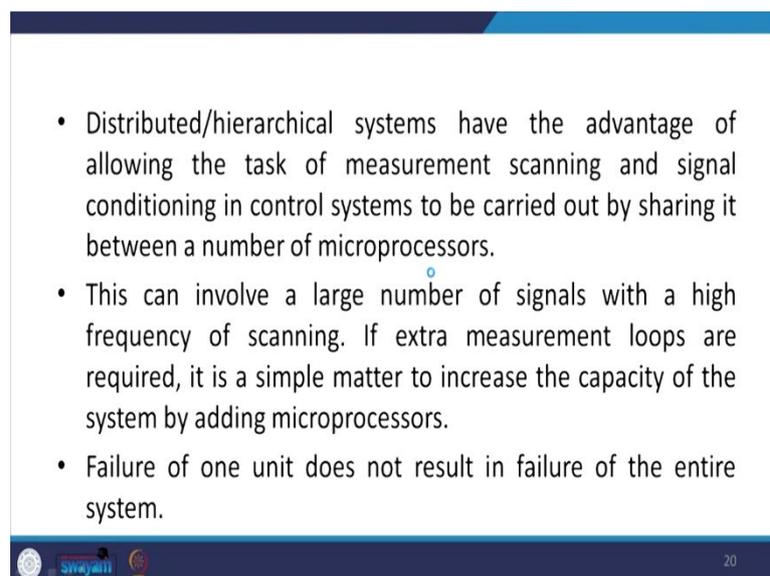
Modern Systems

- In most modern systems there is generally a mixture of distributed and hierarchical systems.
- For example, the work of measurement and actuation may be distributed among a number of microcontrollers/ computers which are linked together and provide the database for the plant.
- These may be overseen by a computer used for direct digital control or sequencing and this in turn may be supervised by one used for supervisory control of the plant as a whole.

19

The modern systems, there is generally a mixture of distributed and hierarchical systems. For example, the work of measurement and actuation may be distributed among a number of microcontrollers computers which are linked together and provide the database for the plant. These may be overseen by a computer used for direct digital control or sequencing, and this, in turn, may be supervised by one used for supervisory control of the plant as a whole.

(Refer Slide Time: 14:37)

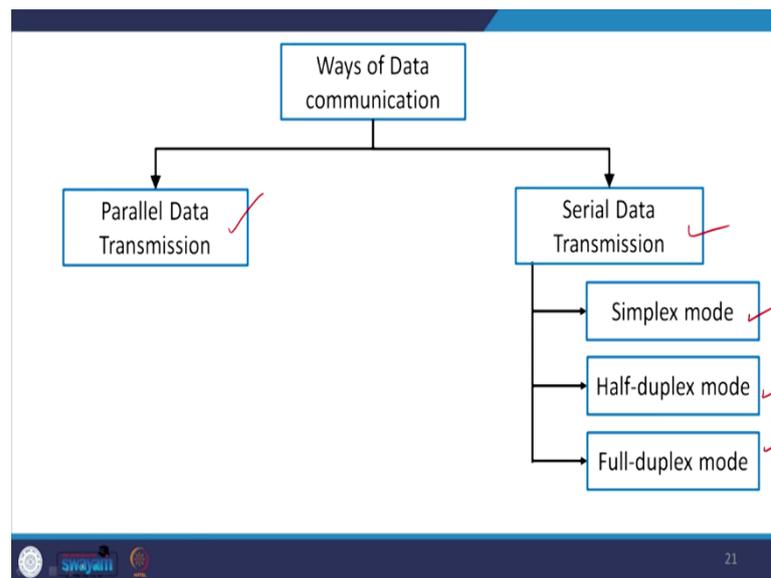


- Distributed/hierarchical systems have the advantage of allowing the task of measurement scanning and signal conditioning in control systems to be carried out by sharing it between a number of microprocessors.
- This can involve a large number of signals with a high frequency of scanning. If extra measurement loops are required, it is a simple matter to increase the capacity of the system by adding microprocessors.
- Failure of one unit does not result in failure of the entire system.

20

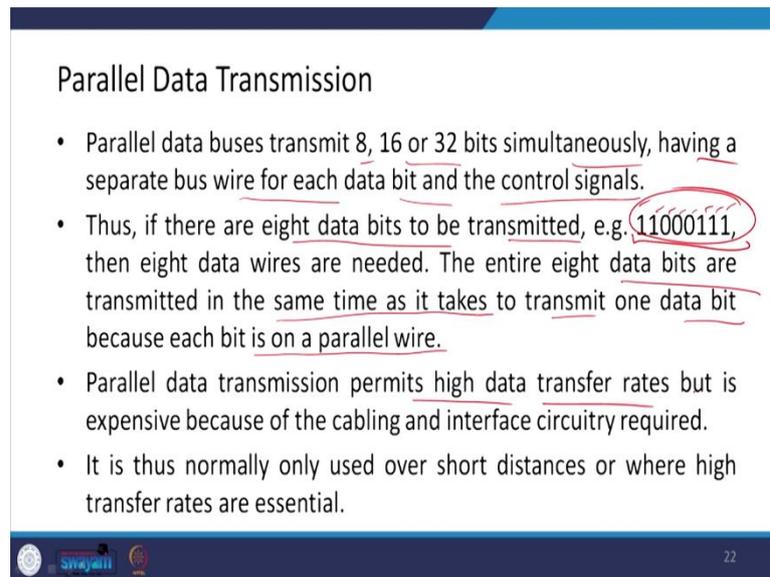
Now, these distributed or hierarchical systems have the advantage of allowing the task of measurement scanning and the signal conditioning in the control system to be carried out by sharing it between the number of microprocessors. This can involve a large number of signals with a high frequency of scanning. If extra measurement loops are required, it is a simple matter to increase the capacity of the system by adding a microprocessor. Failure of one unit does not result in the failure of the entire system.

(Refer Slide Time: 15:02)



Here, If we look at the ways of communication, there are essentially two ways, as I discussed earlier, the parallel data transmission and the serial data transmission. Parallel data transmission, as I said, you have the parallel lines, and in the case of the serial data transmission, we could have the simplex mode or half-duplex mode or the full-duplex mode. I will be discussing all these ways.

(Refer Slide Time: 15:36)



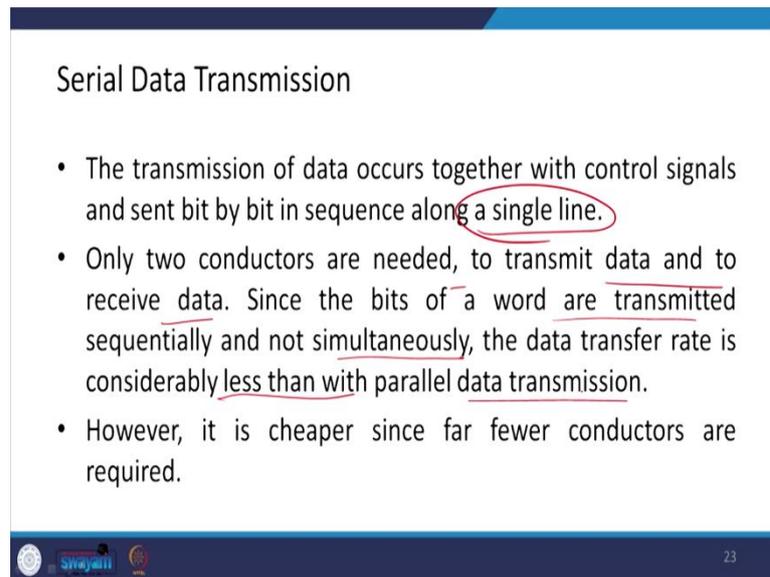
**Parallel Data Transmission**

- Parallel data buses transmit 8, 16 or 32 bits simultaneously, having a separate bus wire for each data bit and the control signals.
- Thus, if there are eight data bits to be transmitted, e.g. 11000111, then eight data wires are needed. The entire eight data bits are transmitted in the same time as it takes to transmit one data bit because each bit is on a parallel wire.
- Parallel data transmission permits high data transfer rates but is expensive because of the cabling and interface circuitry required.
- It is thus normally only used over short distances or where high transfer rates are essential.

22

So, first of all, let us look at the parallel data transmission. So, the parallel data transmission, in this case, 8, 16, or 32 bits, can be transmitted simultaneously, and they have a separate bus for each data bit and the control signal. Suppose, if eight data bit to be transmitted, this is just, for example, so, you have one, two, three, four, five, six, seven, eight, this eight-bit data is to be transmitted then, eight wires are needed, one wire for each bit and the entire eight-bit data are transmitted in the at the same time as it takes to transmit one data bit because each bit is on a parallel wire. Parallel data transmission permits a high data transfer rate because you can transmit each data simultaneously, and it is expensive because of the cabling and interface circuit requirements. It is only used over short distances or where high transfer rates are required.

(Refer Slide Time: 17:11)



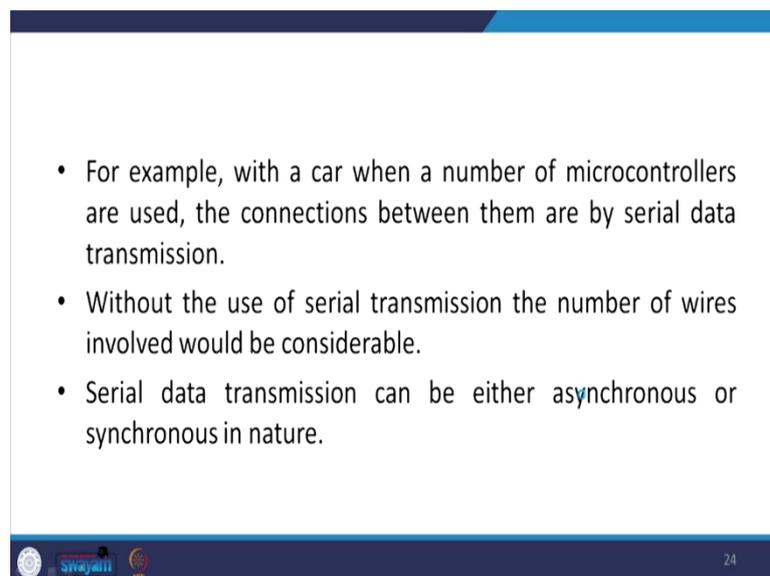
Serial Data Transmission

- The transmission of data occurs together with control signals and sent bit by bit in sequence along a single line.
- Only two conductors are needed, to transmit data and to receive data. Since the bits of a word are transmitted sequentially and not simultaneously, the data transfer rate is considerably less than with parallel data transmission.
- However, it is cheaper since far fewer conductors are required.

23

Next, let us look at the serial data transmission. So, as the name indicates, the transmission of data occurs together with the control signal and is sent bit by bit in sequence through a single line. So, this is the feature of the serial data transmission, and everything has to be transmitted through a single line, and only two conductors are needed to transmit data and to receive data. Since the bits of words are transmitted sequentially and not simultaneously, the data transfer rate is considerably less than with the parallel data transmission. It is quite obvious. However, it is cheaper since far few conductors are required.

(Refer Slide Time: 18:03)

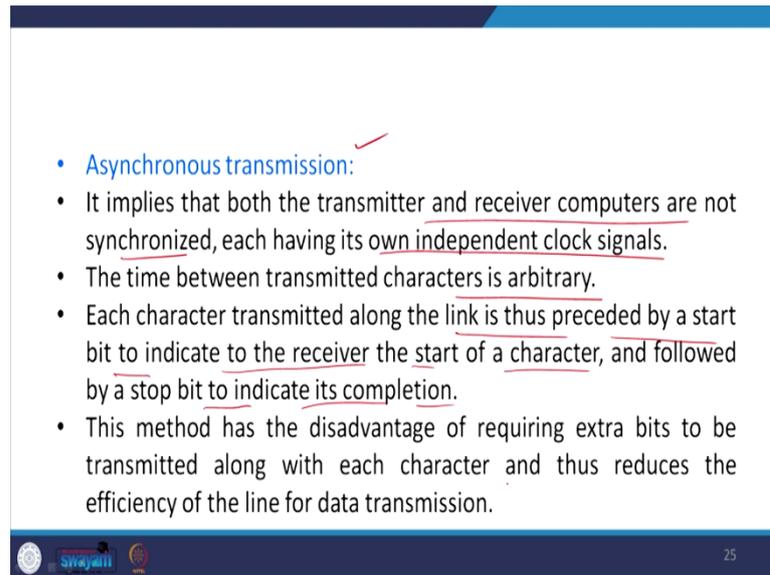


- For example, with a car when a number of microcontrollers are used, the connections between them are by serial data transmission.
- Without the use of serial transmission the number of wires involved would be considerable.
- Serial data transmission can be either asynchronous or synchronous in nature.

24

For example, with a car, when a number of microcontrollers are used, the connection between them or by serial data transmission. Without the use of serial transmission, the number of wires involved would be considerable, and the serial data transmission can be either asynchronous, or it could be synchronous in nature. That is, they could have different clocks, or they could have the same clock.

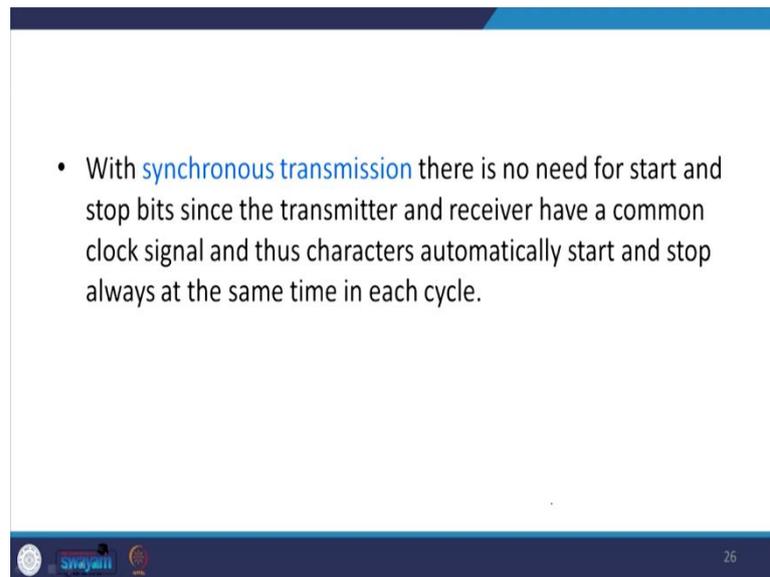
(Refer Slide Time: 18:38)



- **Asynchronous transmission:**
- It implies that both the transmitter and receiver computers are not synchronized, each having its own independent clock signals.
- The time between transmitted characters is arbitrary.
- Each character transmitted along the link is thus preceded by a start bit to indicate to the receiver the start of a character, and followed by a stop bit to indicate its completion.
- This method has the disadvantage of requiring extra bits to be transmitted along with each character and thus reduces the efficiency of the line for data transmission.

Asynchronous transmission implies that both the transmitter and receiver computers are not synchronized, each having its own independent clock signals, and the time between the transmitted characters is arbitrary. Each character transmitted along the link is thus preceded by a start bit to indicate to the receiver the start of a character and followed by a stop bit to indicate its completion. This method has the disadvantage of requiring an extra bit to be transmitted along with each character and thus reduces the efficiency of the line for the data transmission.

(Refer Slide Time: 19:27)

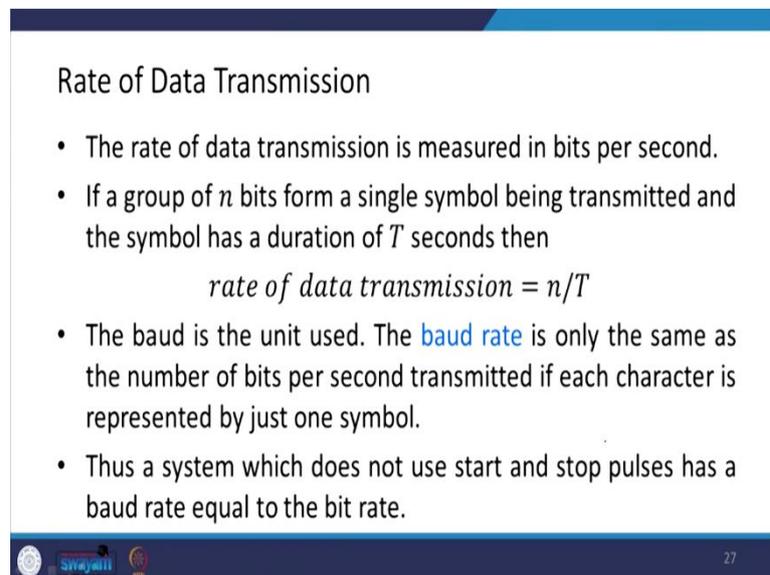


• With **synchronous transmission** there is no need for start and stop bits since the transmitter and receiver have a common clock signal and thus characters automatically start and stop always at the same time in each cycle.

26

With synchronous transmission, there is no need for a start and stop bit since the transmitter and receiver have a common clock signal, and thus characters automatically start and always stop at the same time in each cycle. Now, let us look at the rate of data transmission.

(Refer Slide Time: 19:55)



**Rate of Data Transmission**

- The rate of data transmission is measured in bits per second.
- If a group of  $n$  bits form a single symbol being transmitted and the symbol has a duration of  $T$  seconds then
$$\text{rate of data transmission} = n/T$$
- The baud is the unit used. The **baud rate** is only the same as the number of bits per second transmitted if each character is represented by just one symbol.
- Thus a system which does not use start and stop pulses has a baud rate equal to the bit rate.

27

The rate of data transmission is measured in bits per second. If a group of  $n$  bits forms a single symbol being transmitted and the symbol has a duration of  $T$  second, then the

$$\text{The rate of data transmission} = n/T,$$

and the baud rate is the unit used. The baud rate is only the same as the number of bits per second transmitted if each character is represented by just one symbol. Thus, a system that does not use start and stop pulse has a baud rate equal to the bit rate.

(Refer Slide Time: 20:45)

Serial Data Communication Modes

- 1. Simplex Mode** (one-way transmission): Transmission is only possible in one direction, from device A to device B, where device B is not capable of transmitting back to device A. This method is usually only used for transmission to devices such as printers which never transmit information.

A → Transmit → B

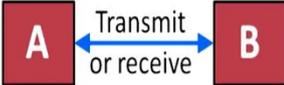
swayam 28

Now, let us look at the serial data communication mode, the three modes which I have talked to you about, the simplex mode. It is the only one-way transmission from A to B. So, the transmission is only possible in one direction from device A to device B, where device B is not capable of transmitting back to device A. This method is usually used for transmission to devices such as printers which never transmit the information.

(Refer Slide Time: 21:24)

**2. Half-Duplex Mode:** ✓

- Data is transmitted in one direction at a time but the direction can be changed.
- Terminals at each end of the link can be switched from transmit to receive.
- Thus device A can transmit to device B and device B to device A but not at the same time.



The diagram shows two red boxes labeled 'A' and 'B' connected by a double-headed blue arrow. The text 'Transmit or receive' is written above the arrow, indicating that data can flow in either direction but not simultaneously.

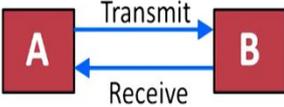
29

Then, we have the half-duplex mode. So, here, the data is transmitted in one direction at a time, but the direction can be changed, and the terminals at each end of the link can be switched from transmit to receive. Device A can transmit to device B and receive device B to device A, but not at the same time. So, it can do either transmit or receive. Both cannot be done simultaneously in the case of half-duplex mode.

(Refer Slide Time: 22:00)

**3. Full-duplex mode**

- Data may be transmitted simultaneously in both directions between devices A and B.
- This is like a two-lane highway in which traffic can occur in both directions simultaneously.
- The telephone system is an example of full-duplex mode in that a person can talk and receive at the same time.



The diagram shows two red boxes labeled 'A' and 'B' connected by two separate blue arrows. The top arrow points from A to B and is labeled 'Transmit'. The bottom arrow points from B to A and is labeled 'Receive', indicating simultaneous two-way communication.

30

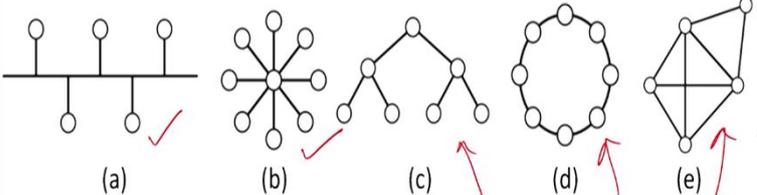
In the case of full-duplex mode, data may be transmitted simultaneously in both directions between A and B. So, here in the full-duplex mode, it could be either A to B, or we can

also have B to A. So, this is like the two-lane highway in which the traffic can occur in both directions simultaneously, and the telephone system is an example of the full-duplex mode in that a person can talk as well as receive at the same time.

(Refer Slide Time: 22:55)

**Networks**

- Network is used for a system which allows two or more computers/ microprocessors to be linked for the interchange of data.
- The following are commonly used forms of network.



Network topologies: (a) data bus, (b) star, (c) hierarchy, (d) ring, (e) mesh

Now, let us look at the networks. The network is used for a system that allows two or more computers or microprocessors to be linked for the interchange of data. So, these are some of the common forms of the network. I will be talking details about is this is the data bus, this is the star configuration, this is hierarchical, this is the ring, and this is the mesh.

(Refer Slide Time: 23:09)

1. **Data bus:** ✓
  - This has a linear bus into which all the stations are plugged. This system is often used for multipoint terminal clusters. It is generally the preferred method for distances between nodes of more than 100 m.
2. **Star:** ✓
  - This has dedicated channels between each station and a central switching hub through which all communications must pass.
  - This is the type of network used in the telephone systems; all the lines passing through a central exchange. This system is also often used to connect remote and local terminals to a central mainframe computer. There is a major problem with this system in that if the central hub fails then the entire system fails.

In the case of the data bus, this has a linear bus into which all the stations are plugged, and this system is often used for multipoint terminal clusters, and it is generally the preferred method for the distance between nodes of more than 100 meters.

Then, the star configuration this is has a dedicated channel between each station and the central switching hub through which all communication must pass. This is the type of network used in the telephone system; all the lines pass through the central exchange. The system is also often used to connect remote and local terminals to a central mainframe computer. There is a major problem with this system that if the central hub fails, the entire system fails in this case of the star form of network.

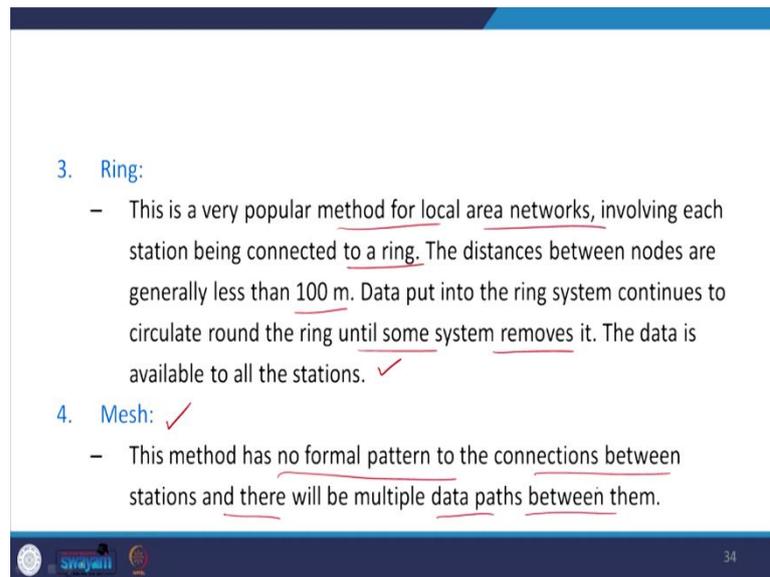
(Refer Slide Time: 24:18)

3. Hierarchy or tree:

- This consists of a series of branches converging indirectly to a point at the head of the tree. With this system there is only one transmission path between any two stations. This arrangement may be formed from a number of linked data bus systems. Like the bus method, it is often used for distances between nodes of more than 100 m.

Then, next is the hierarchy or the tree network. This consists of a series of branches converging directly to a point at the head of the tree. With this system, there is only one transmission path between any two stations, and this arrangement may be formed from a number of link data bus systems. Like the bus method, it is often used for distances between more than 100 meters.

(Refer Slide Time: 24:49)



3. Ring:

- This is a very popular method for local area networks, involving each station being connected to a ring. The distances between nodes are generally less than 100 m. Data put into the ring system continues to circulate round the ring until some system removes it. The data is available to all the stations. ✓

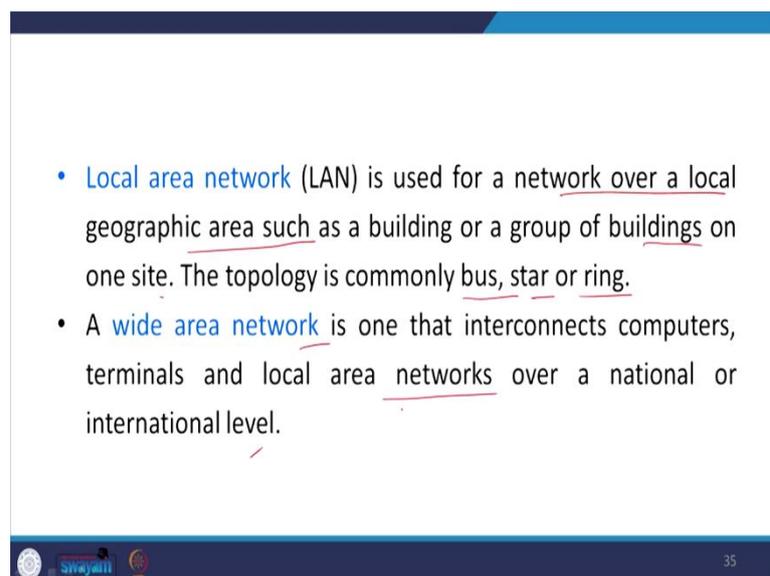
4. Mesh: ✓

- This method has no formal pattern to the connections between stations and there will be multiple data paths between them.

34

The ring system, ring form, is a very popular method for the local area network or the LAN, involving each station being connected to a ring. The distance between nodes is generally less than 100 meters, and data input into the ring system continues to circulate around the ring until some system removes it. The data is available to all the stations, and next is the mesh form. This form has no formal pattern to the connection between the stations, and there will be multiple data paths between them.

(Refer Slide Time: 25:31)

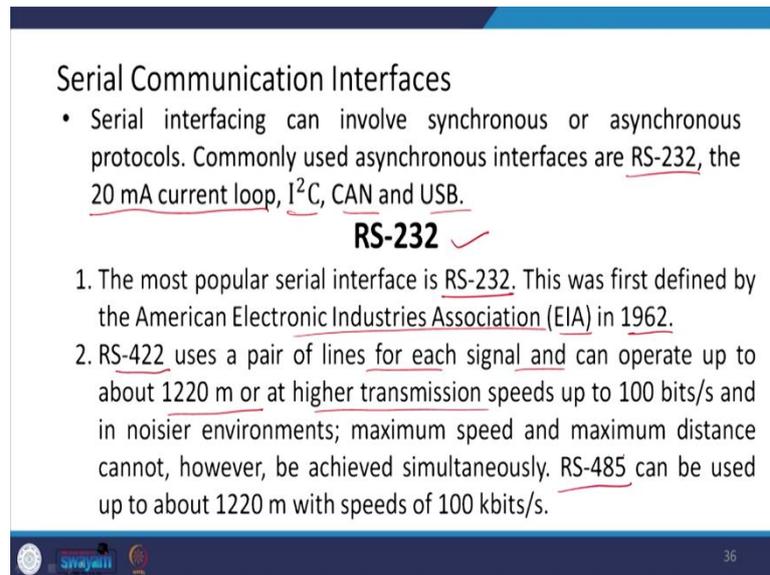


- Local area network (LAN) is used for a network over a local geographic area such as a building or a group of buildings on one site. The topology is commonly bus, star or ring.
- A wide area network is one that interconnects computers, terminals and local area networks over a national or international level.

35

The local area network or the LAN is used for a network over a local geographic area, such as a building or group of building on one site. The topology commonly used is a bus, star, or the ring one, and the wide-area network is one that interconnects computer terminal and local area network over a national or the international level.

(Refer Slide Time: 26:01)



**Serial Communication Interfaces**

- Serial interfacing can involve synchronous or asynchronous protocols. Commonly used asynchronous interfaces are RS-232, the 20 mA current loop, I<sup>2</sup>C, CAN and USB.

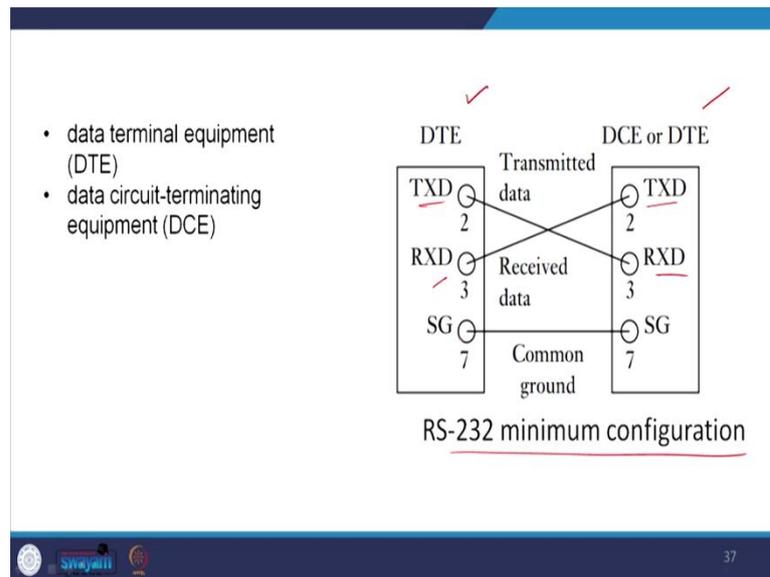
**RS-232 ✓**

1. The most popular serial interface is RS-232. This was first defined by the American Electronic Industries Association (EIA) in 1962.
2. RS-422 uses a pair of lines for each signal and can operate up to about 1220 m or at higher transmission speeds up to 100 bits/s and in noisier environments; maximum speed and maximum distance cannot, however, be achieved simultaneously. RS-485 can be used up to about 1220 m with speeds of 100 kbits/s.

36

Now, let us look at the serial communication interfaces. So, serial interfacing can involve synchronous or asynchronous protocols. Commonly used asynchronous interfaces are RS-232, the 20-milliamp current loop, I square C, CAN, and the USB. Let us look at first the RS-232. The most popular serial interface is RS-232. This was first defined by the American Electronic Industries Association EIA in 1962. The RS-422, similar to RS-232, uses a pair of the line for each signal and can operate up to about 1220 meters or higher transmission speeds up to 100 bits per second and in noisier, noisy environments, maximum speed and maximum distance cannot be achieved simultaneously, similar is the case with RS-485.

(Refer Slide Time: 27:16)



So, this is what the RS-232 minimum configuration looks like. We have the data terminal equipment over here and data circuit terminating equipment here. So, we could have the transmitted data from 2 and 3 could have, 3 have the receive data so. Similarly, we have two transmitted data, and 3 have the received data, and the 7 port is the common ground.

(Refer Slide Time: 27:49)

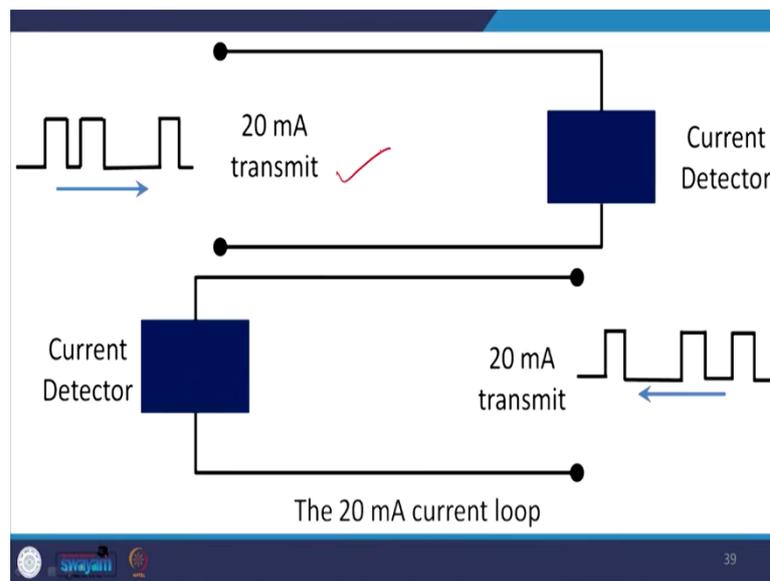
### The 20 mA current loop

- This uses a current signal rather than a voltage signal.
- A pair of separate wires is used for the transmission and the receiver loops with a current level of 20 mA used to indicate a logic 1 and 0 mA for logic 0.
- The serial data is encoded with a start bit, eight data bits and two stop bits.
- Such current signals enable a far greater distance, a few kilometers, between transmitter and receiver than with the standard RS-232 voltage connections

Next, let us look at the 20-milliamp current loop. This uses a current signal rather than a voltage signal. A pair of separate wires are used for the transmission, and the receiver loops with the current level of 20 milliamps are used to indicate a logic 1 and 0 milliamp

for logic 0. So, 20-milliamp for logic 1 and 0 milliamp for logic 0. The serial data is encoded with a start bit, eight-bit, and two stop bits. Such current signals enable a far greater distance, a few kilometers, between transmitter and receiver than with the standard RS-232 voltage connections.

(Refer Slide Time: 28:44)



This is how this 20-milliamp current loop works.

(Refer Slide Time: 28:55)

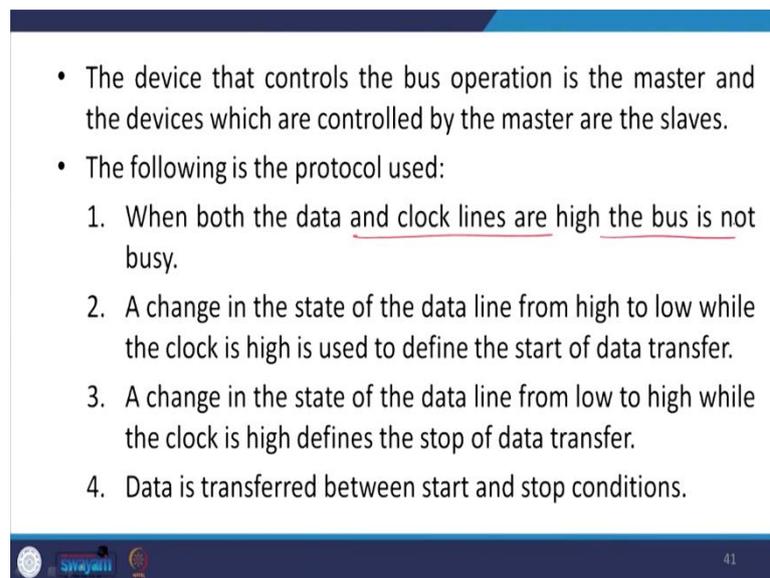
### Inter-IC Communication ( $I^2C$ ) Bus

- Inter-IC Communication bus, is a serial data bus designed by Philips use for communications between integrated circuits or modules.
- The bus allows data and instructions to be exchanged between devices by means of just two wires. This results in a considerable simplification of circuits.
- The two lines are a bidirectional data line (SDA) and a clock line (SCL). Both lines are connected to the positive power supply via resistors.

Next, let us look at the  $I^2C$  bus. The inter IC communication bus is a serial data bus designed by Philips, and it is used for the communication between integrated circuits or

modules. The bus allows data and instruction to be exchanged between devices by means of just two wires, and this results in a considerable simplification of the circuit. The two lines are a bidirectional data line and a clock line. Both lines are connected to the positive power supply via resistors.

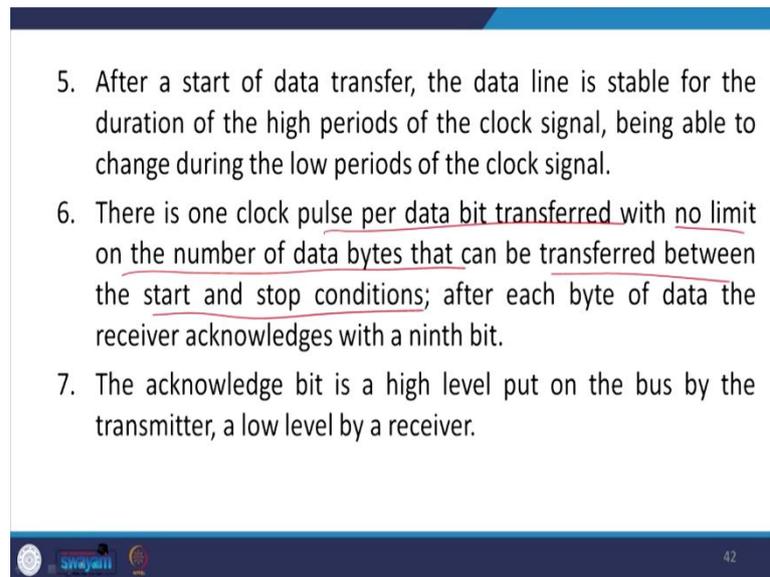
(Refer Slide Time: 29:50)



- The device that controls the bus operation is the master and the devices which are controlled by the master are the slaves.
- The following is the protocol used:
  1. When both the data and clock lines are high the bus is not busy.
  2. A change in the state of the data line from high to low while the clock is high is used to define the start of data transfer.
  3. A change in the state of the data line from low to high while the clock is high defines the stop of data transfer.
  4. Data is transferred between start and stop conditions.

The device that controls the bus operation is the master, and the device which is controlled by the masters are the slaves, and the following is the protocol used. When both the data and clock lines are high, the bus is not busy. A change in the state of the data line from high to low while the clock is high is used to define the start of the data transfer, and a change in the state of the data line from low to high, while the clock is high, defines the stop of the data transfer. Data is transferred between the start and the stop conditions.

(Refer Slide Time: 30:13)



5. After a start of data transfer, the data line is stable for the duration of the high periods of the clock signal, being able to change during the low periods of the clock signal.

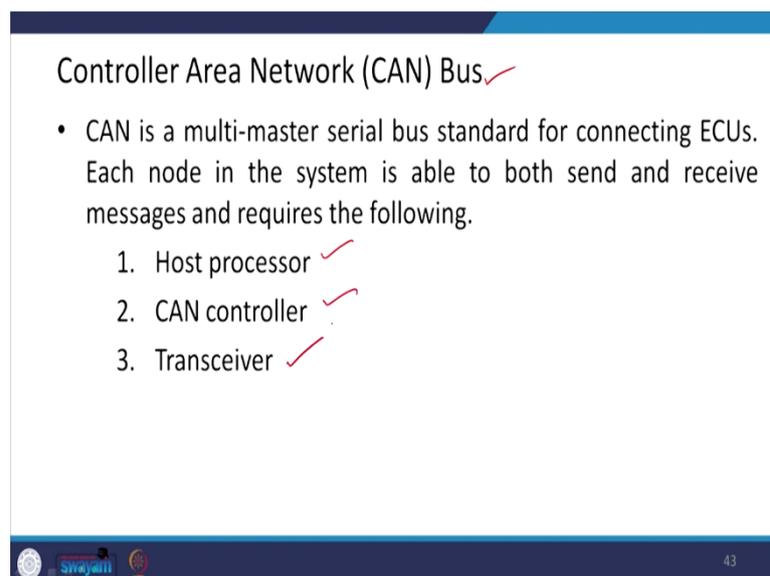
6. There is one clock pulse per data bit transferred with no limit on the number of data bytes that can be transferred between the start and stop conditions; after each byte of data the receiver acknowledges with a ninth bit.

7. The acknowledge bit is a high level put on the bus by the transmitter, a low level by a receiver.

42

After the start of data transfer, the data line is stable for the duration of the high period of the clock signal being able to change during the low periods of the clock signal. There is one clock pulse per data bit transferred with no limit on the number of data bytes that can be transferred between the start and stop conditions after each byte of data the receiver acknowledges with the ninth bit. The acknowledge bit is a high level put on the bus by the transmitter, a low level by a receiver.

(Refer Slide Time: 30:51)



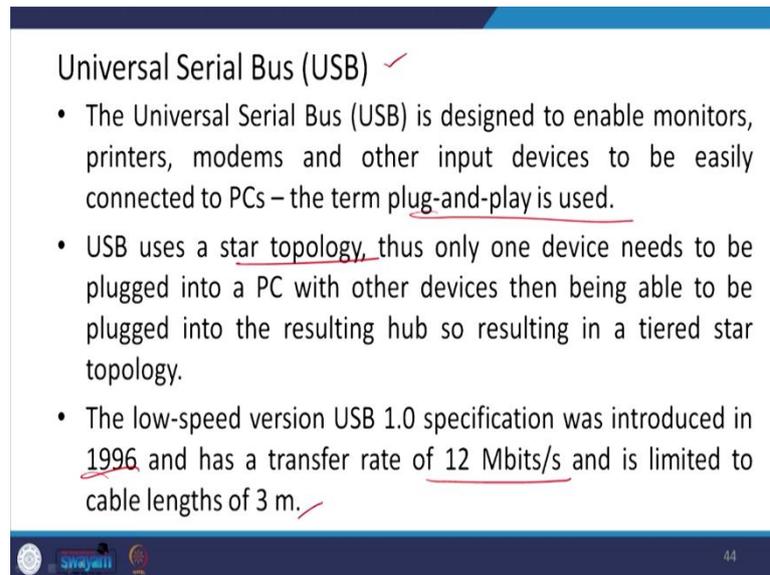
### Controller Area Network (CAN) Bus ✓

- CAN is a multi-master serial bus standard for connecting ECUs. Each node in the system is able to both send and receive messages and requires the following.
  1. Host processor ✓
  2. CAN controller ✓
  3. Transceiver ✓

43

Next, let us look at the CAN bus that is the controller area network bus. CAN is a multi-master serial bus standard for connecting ECU's. Each node in the system is able to both send and receive messages and requires the following; a host computer, a CAN controller, and a transceiver. Next, let us look at the USB.

(Refer Slide Time: 31:21)



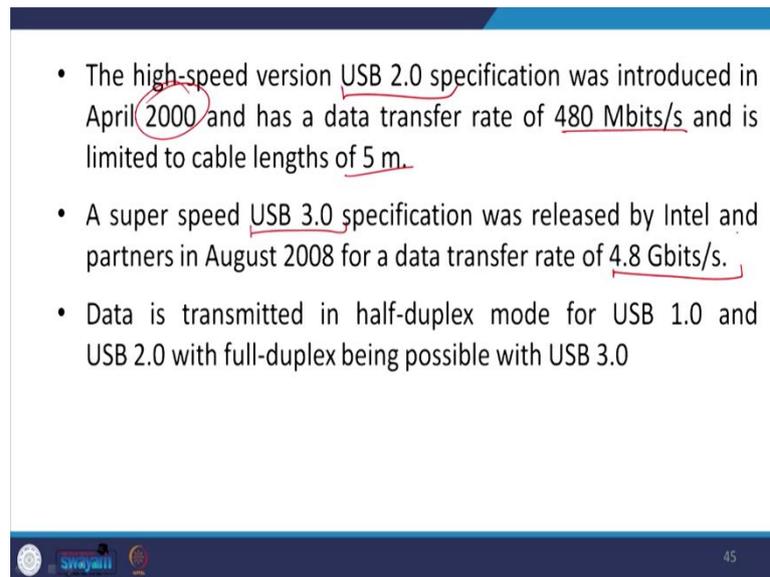
Universal Serial Bus (USB) ✓

- The Universal Serial Bus (USB) is designed to enable monitors, printers, modems and other input devices to be easily connected to PCs – the term plug-and-play is used.
- USB uses a star topology, thus only one device needs to be plugged into a PC with other devices then being able to be plugged into the resulting hub so resulting in a tiered star topology.
- The low-speed version USB 1.0 specification was introduced in 1996 and has a transfer rate of 12 Mbits/s and is limited to cable lengths of 3 m. ✓

44

The universal serial bus is a very popular form. So, the universal serial bus or USB is designed to enable monitors, printers, modems, and other input devices to be easily connected to the PC. The term plug and play is used. USB uses a star topology. Thus, only one device needs to be plugged into a PC with other devices then being able to be plugged into the resulting hub, resulting in a tiered star topology. The low-speed version USB 1.0 specification was introduced in 1996, has a transfer rate of 12 megabits per second and is limited to a cable length of 3 meters.

(Refer Slide Time: 32:16)

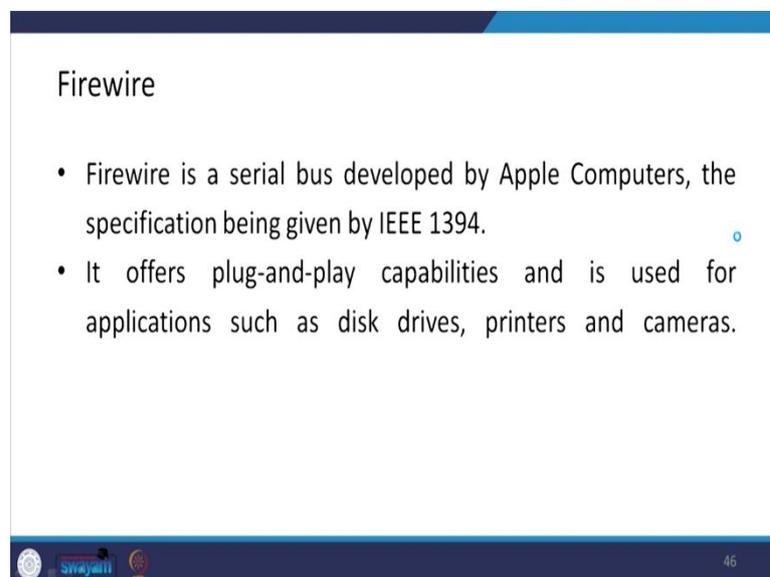


Slide 45 contains three bullet points about USB specifications. The first point states that the high-speed version USB 2.0 specification was introduced in April 2000 and has a data transfer rate of 480 Mbits/s, limited to cable lengths of 5 m. The second point states that a super speed USB 3.0 specification was released by Intel and partners in August 2008 for a data transfer rate of 4.8 Gbits/s. The third point states that data is transmitted in half-duplex mode for USB 1.0 and USB 2.0, with full-duplex being possible with USB 3.0. The slide footer includes logos for Swajathi and a page number of 45.

- The high-speed version USB 2.0 specification was introduced in April 2000 and has a data transfer rate of 480 Mbits/s and is limited to cable lengths of 5 m.
- A super speed USB 3.0 specification was released by Intel and partners in August 2008 for a data transfer rate of 4.8 Gbits/s.
- Data is transmitted in half-duplex mode for USB 1.0 and USB 2.0 with full-duplex being possible with USB 3.0

The next version was USB 2.0, which was introduced in April 2000 and had a data transfer rate of 480 megabits per second and is limited to the cable length of 5 meters and then, we have the USB 3.0 also came with a data transfer rate of 4.8 gigabits per second.

(Refer Slide Time: 32:45)



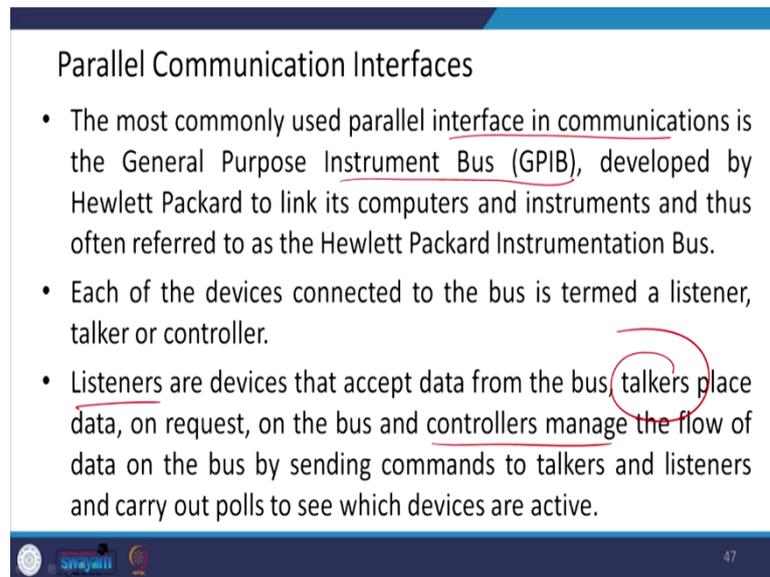
Slide 46 is titled 'Firewire' and contains two bullet points. The first point states that Firewire is a serial bus developed by Apple Computers, with the specification given by IEEE 1394. The second point states that it offers plug-and-play capabilities and is used for applications such as disk drives, printers, and cameras. The slide footer includes logos for Swajathi and a page number of 46.

### Firewire

- Firewire is a serial bus developed by Apple Computers, the specification being given by IEEE 1394.
- It offers plug-and-play capabilities and is used for applications such as disk drives, printers and cameras.

Firewire, firewire is a serial bus developed by Apple Computers, the specifications were given by IEEE 1394, and it offers plug and play capabilities and is used for applications such as disk drives, printers, and cameras.

(Refer Slide Time: 33:06)



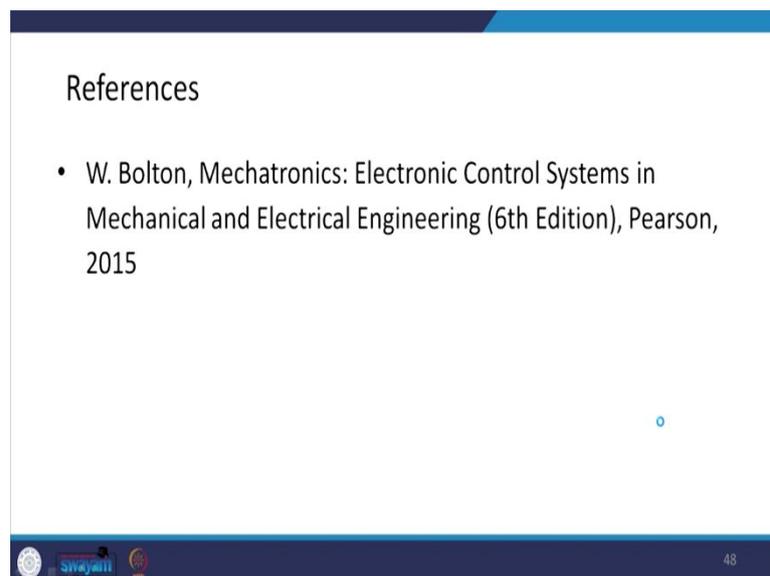
Parallel Communication Interfaces

- The most commonly used parallel interface in communications is the General Purpose Instrument Bus (GPIB), developed by Hewlett Packard to link its computers and instruments and thus often referred to as the Hewlett Packard Instrumentation Bus.
- Each of the devices connected to the bus is termed a listener, talker or controller.
- Listeners are devices that accept data from the bus, talkers place data, on request, on the bus and controllers manage the flow of data on the bus by sending commands to talkers and listeners and carry out polls to see which devices are active.

47

The parallel communication interface, as I said, the most commonly used peripheral, used parallel interface in communication is the general-purpose instrument bus is that is (GPIB), developed by HP to link its computer and instruments and thus often referred to as HP instrumentation bus. Each device connected to the bus is termed listener, talker, or controller. Listeners are devices that accept data from the bus, talkers place data on request on the bus, and controllers manage the flow of data on the bus by sending commands to talkers and listeners and carrying out polls to see which devices are active.

(Refer Slide Time: 34:02)



References

- W. Bolton, Mechatronics: Electronic Control Systems in Mechanical and Electrical Engineering (6th Edition), Pearson, 2015

48

So, this is all about these two lectures, and you can further read if you are interested in this in Bolton, as I indicated earlier.

And thank you and good luck.