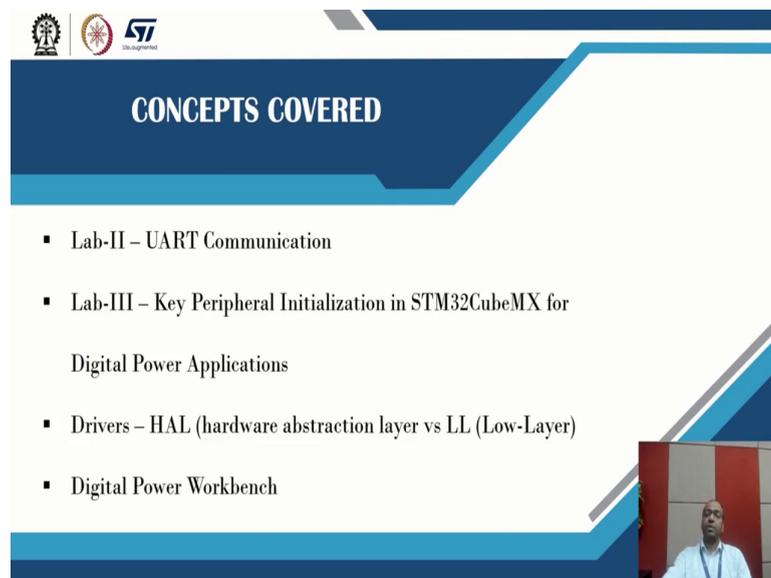


Digital Control in Switched Mode Power Converters and FPGA-based Prototyping
Prof. Santanu Kapat
Department of Electrical Engineering
Indian Institute of Technology, Kharagpur

Module - 09
Digital Control Implementation using Microcontroller
Lecture - 83
Getting started with STM32CubeMX (Part - II)

Welcome, all to the new lecture on Digital Control in Switch Mode Power Converters and FPGA-based Prototyping course. This lecture will be in continuation of the last lecture and it will again focus on STM32CubeMX.

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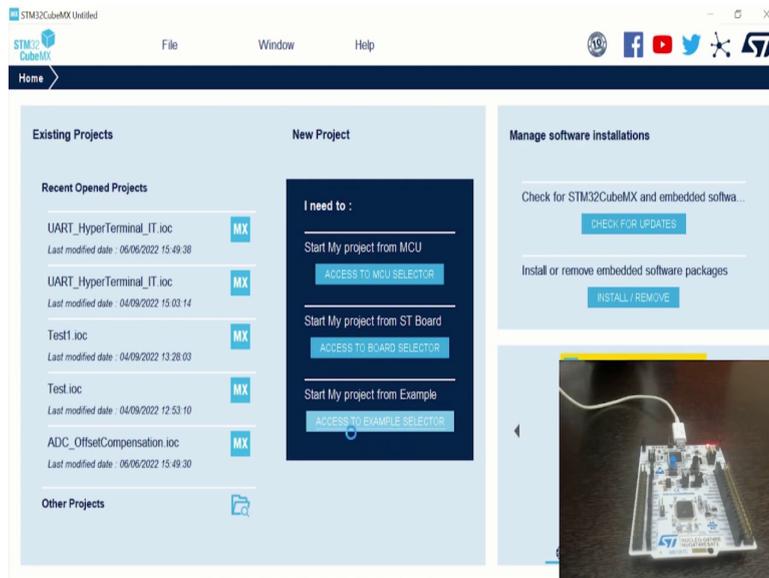


The slide features a blue header with the text 'CONCEPTS COVERED'. Below the header, there is a list of four bullet points. The first bullet point is 'Lab-II - UART Communication'. The second bullet point is 'Lab-III - Key Peripheral Initialization in STM32CubeMX for Digital Power Applications'. The third bullet point is 'Drivers - HAL (hardware abstraction layer vs LL (Low-Layer))'. The fourth bullet point is 'Digital Power Workbench'. In the bottom right corner of the slide, there is a small inset video frame showing a man in a white shirt.

- Lab-II - UART Communication
- Lab-III - Key Peripheral Initialization in STM32CubeMX for Digital Power Applications
- Drivers - HAL (hardware abstraction layer vs LL (Low-Layer))
- Digital Power Workbench

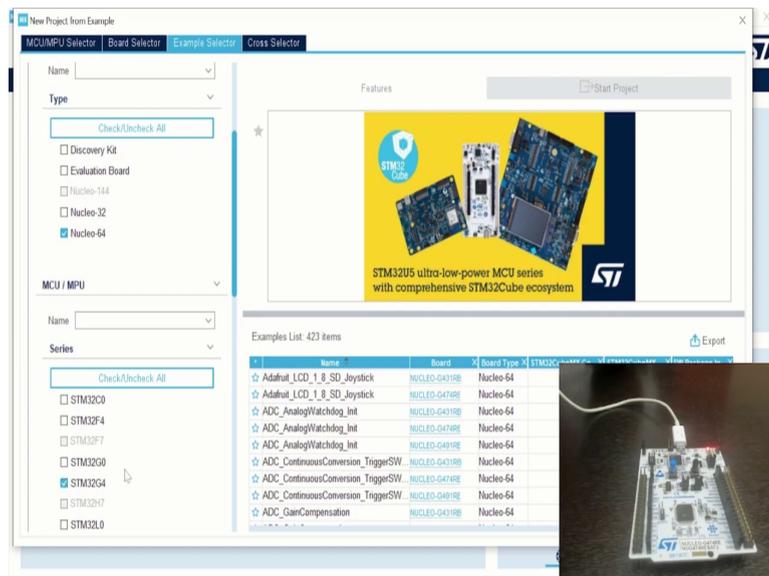
In the last lecture, we discussed the example of GPIO toggling in this lecture we will be starting with the example of UART communication and it will be demonstrated over the STM32 G4 Nucleo board. Then we will be covering the key peripherals initialization required for digital power applications mainly the ADCs and the timers. Then we will be focusing on the advanced setting where we will be covering the difference between the HAL layer and the lower level layer. And in the end, we will be focusing on the digital power workbench.

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Let us start with the UART communication example here we are using a hyper terminal where the Nucleo board will be exchanging the data. We can directly go to the repository and open that particular example, but if we are not aware of the examples available we can go to the access to example selector.

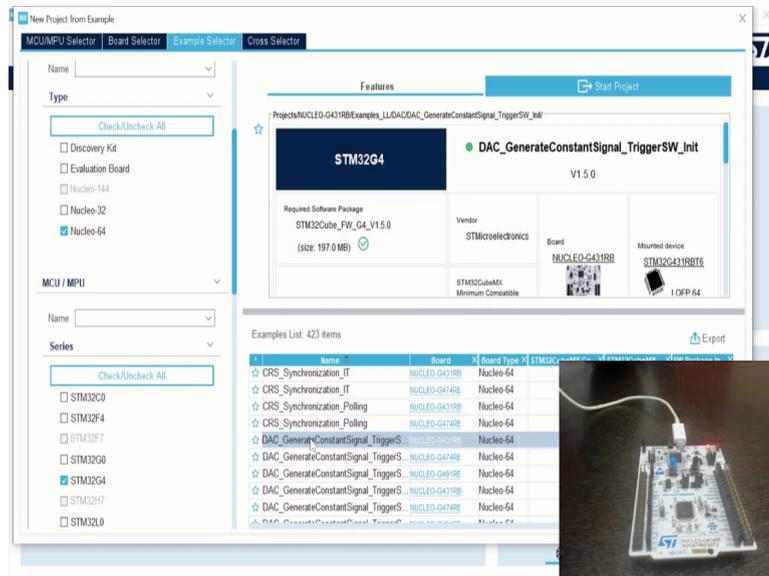
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The beauty of this option is here on a single screen we can see all the firmware examples available for that particular Nucleo board or the discovery board that we are having. So, we can select the STM32 G4 microcontroller. So, all these are the examples particularly for the

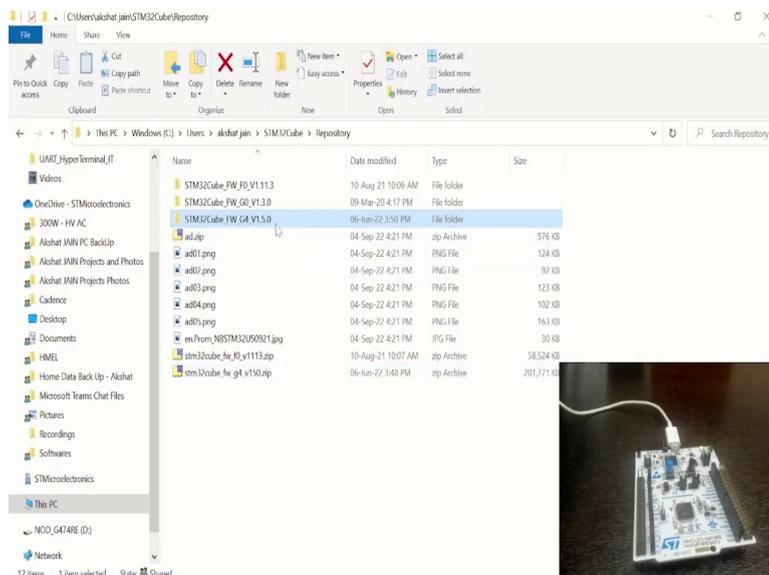
G4 microcontroller these are the ADCs comparator examples. So, we can select any of these examples depending on our requirements.

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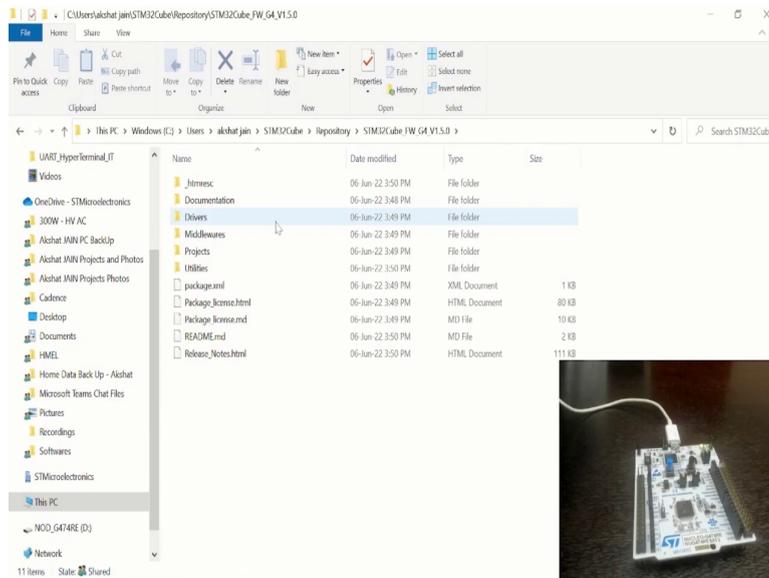
So, corresponding the ioc file will be open also the firmware project is already there. So, we need not generate the code again and again unless we require some specific change. So, let us go to the repository directly.

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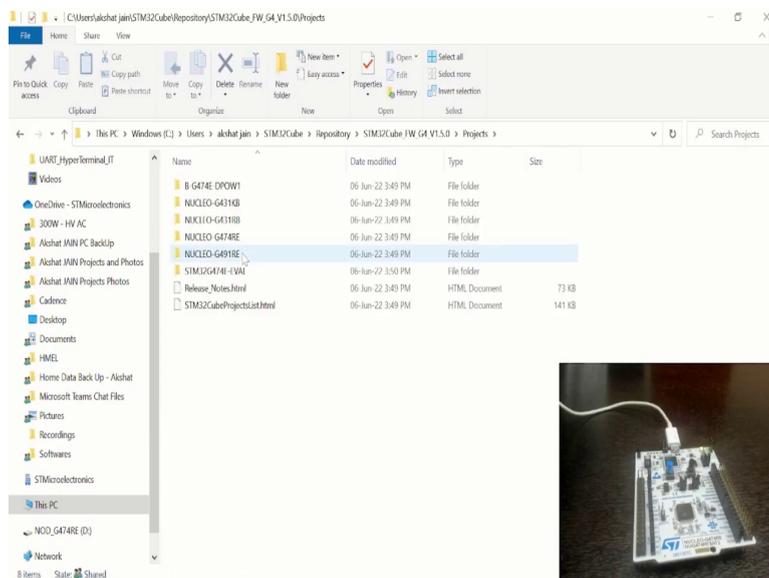
So, this is my repository I will be going into G4 version 1.5.0.

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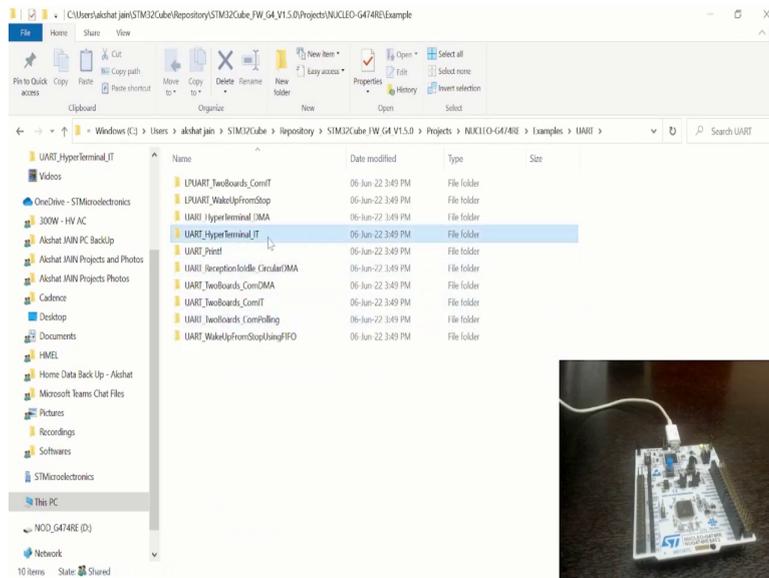
In this, I will be going on to the projects.

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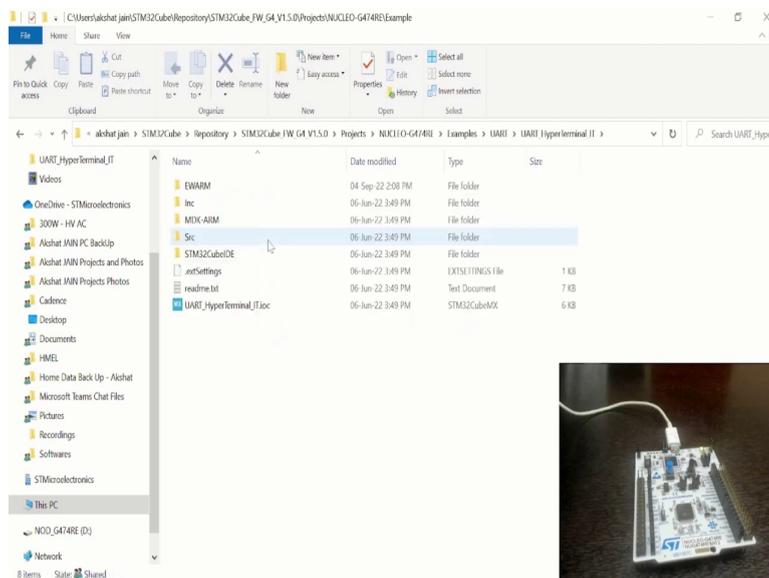
Then I am using NUCLEO G4 74RE86.

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And within that, I will be selecting a hyper terminal interrupt example.

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Before jumping on to the project directly I would request every user to go through the read me dot text file, before exploring any of the examples.

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readme.txt - Notepad

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an HyperTerminal PC application.

Board: NUCLEO-G474RE
Tx Pin: PA.02 (available through VCP)
Rx Pin: PA.03 (available through VCP)

```
graph LR
    subgraph STM32_Board
        USART[USART]
    end
    subgraph HyperTerminal
        HT[HyperTerminal]
    end
    USART -- TX --> HT
    HT -- RX --> USART
    HT -- TX --> USART
    USART -- RX --> HT
    style STM32_Board fill:none,stroke:none
    style HyperTerminal fill:none,stroke:none
```

At the beginning of the main program the HAL_Init() function is called to reset all the peripherals, initialize the Flash interface and the systick. Then the SystemClock_Config() function is used to configure the system clock (SYSCLK) to run at 170 MHz for STM32G4xx Devices.

The UART peripheral configuration is ensured by the HAL_UART_Init() function. This later is calling the HAL_UART_MspInit() function which core is implementing the configuration of the needed UART resources according to the used hardware (CLOCK, GPIO and NVIC). You may update this function to change UART configuration.

The UART/Hyperterminal communication is then initiated. The HAL_UART_Receive_IT() and the HAL_UART_Transmit_IT() functions allow respectively the reception of Data from Hyperterminal and the transmission of a predefined data buffer.

The Asynchronous communication aspect of the UART is clearly highlighted as the data buffers transmission/reception to/from Hyperterminal are done simultaneously.

For this example the TxBuffer (aTxStartMessage) is predefined and the RxBuffer (aRxBuffer) size is limited to 10 data by the mean of the RXBUFFERSIZE define in the main.c file.

In a first step the TxBuffer buffer content will be displayed in the Hyperterminal interface and the received data will be stored in the RxBuffer buffer.
In a second step the received data in the RxBuffer buffer will be sent back to Hyperterminal and displayed.
The end of this two steps are monitored through the HAL_UART_GetState() function result.

NUCLEO-G474RE RevC board LED is used to monitor the transfer status:

- LED2 turns ON if transmission/reception is complete and OK.
- LED2 toggles when when there is an error in transmission/reception process.

The UART is configured as follows:

- BaudRate = 9600 baud
- Word Length = 8 Bits (7 data bit + 1 parity bit)
- One Stop bit
- Odd parity
- Hardware flow control disabled (RTS and CTS signals)
- Reception and transmission are enabled in the time

@note when the parity is enabled, the computed parity is inserted at the MSB position of the transmitted data.

@note Care must be taken when using HAL_Delay(), this function provides accurate delay (in milliseconds) based on variable incremented in SysTick ISR. This implies that if HAL_Delay() is called from a peripheral ISR process, then the SysTick interrupt must have higher priority (numerically lower) than the peripheral interrupt. Otherwise the caller ISR process will be blocked.
To change the SysTick interrupt priority you have to use HAL_NVIC_SetPriority() function.

@note The real-time period to ensure that the SysTick time base is always set to 1 millisecond

In T. Col

It tells what all pins are configured how exactly they are configured and also the basic details that what the end user can expect that basically, the results from that particular example.

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readme.txt - Notepad

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GPID and NVIC). You may update this function to change UART configuration.

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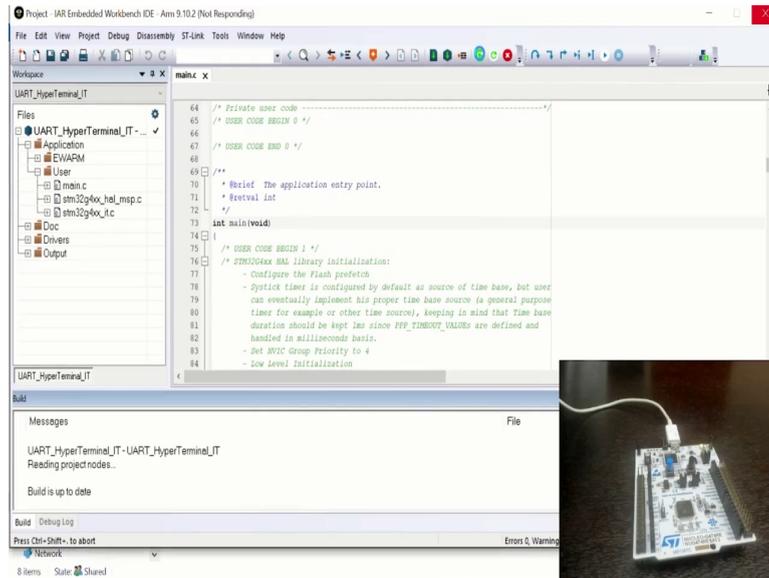
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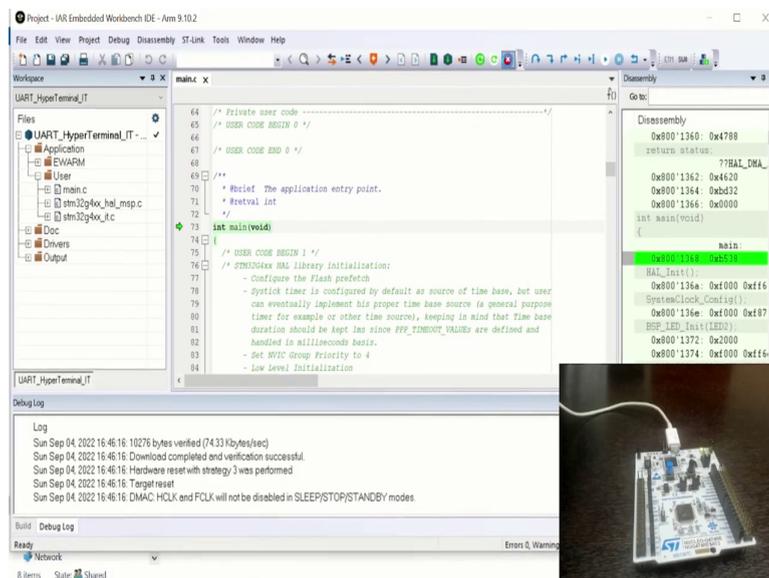
Also in some examples, some settings need to be done like in this the hyper terminal settings that we need to do is given here. So, it is beneficial to go through the read me dot text file for any of the examples used.

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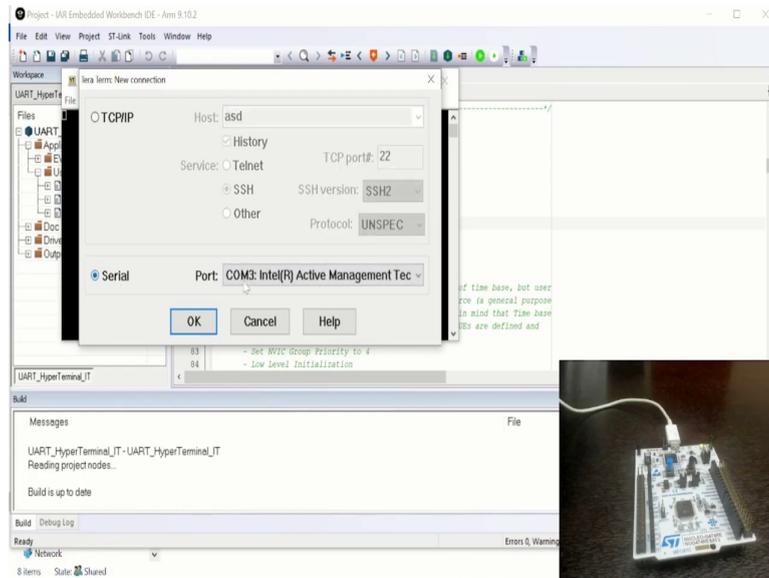


So, let me copy the path of the project and open this in IAR. So, I am fleshing this code into my Nucleo board, it will take a couple of seconds. So, the firmware is less. So, I am stopping the debugging here.

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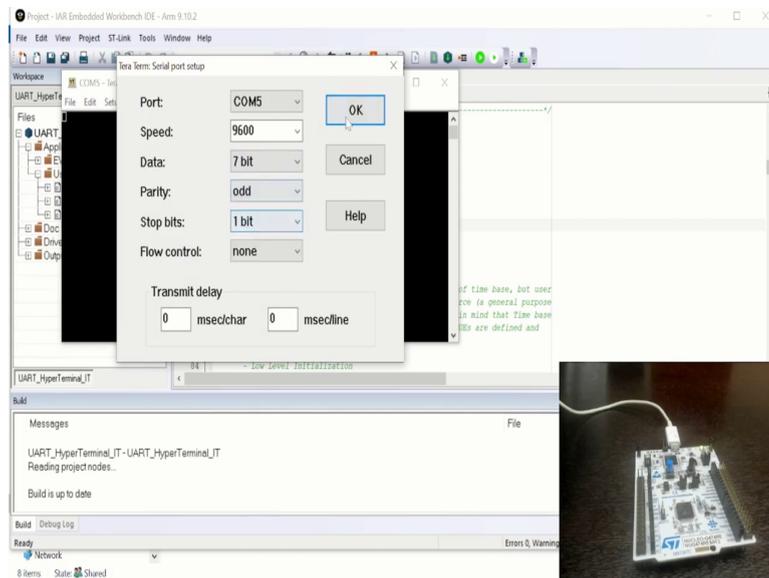


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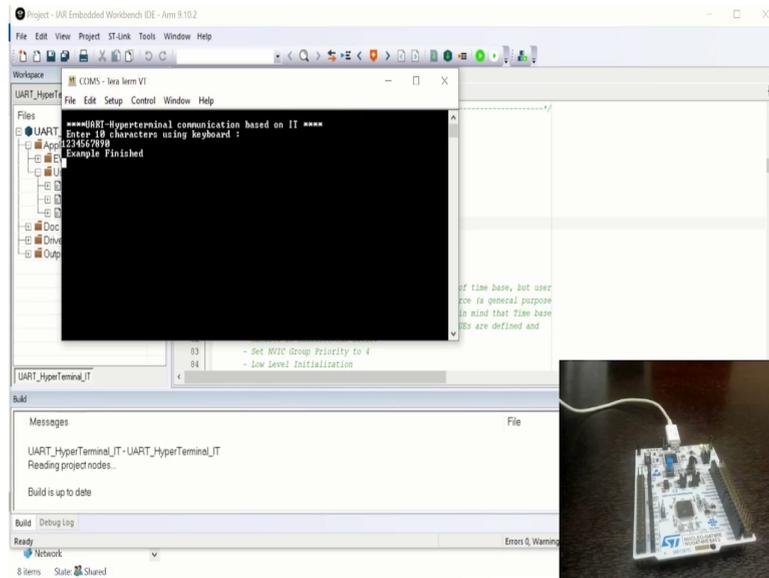


Now, for the hyper terminal, I am using the terraform tool and you can use any of the hyper terminal tools, but we just need to be aware of the settings that we need to do.

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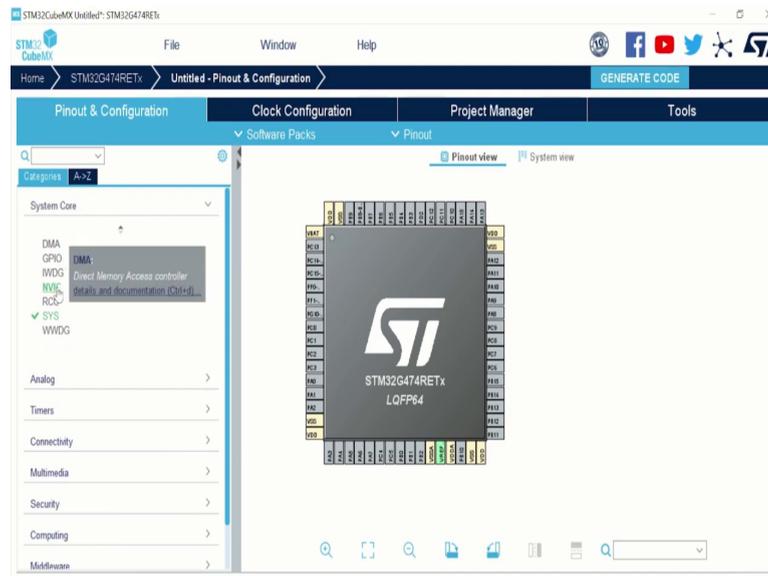
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The settings are present in the read me dot text file and also in the main dot c file of your project, so I have done the setting. So, if you can see the Nucleo board I am just pressing the reset button of this and you can see my hyper terminal window it shows hyper terminal communication based on interrupt entering 10 characters using keyboards

So, I am typing just 1, 2, 3, 4, 5, 6, 7, 8, 9, and 0. So, as soon as I entered it says example finished and whatever data I have entered it has displayed. So, we have completed the hyper-terminal interrupt example. So, the idea behind of showcasing this particular example and similarly there are several other examples that we can use, and on top of it, we can build our application depending on the requirement or the configuration required. Now, we will be switching to the key peripherals that are required within digital power applications.

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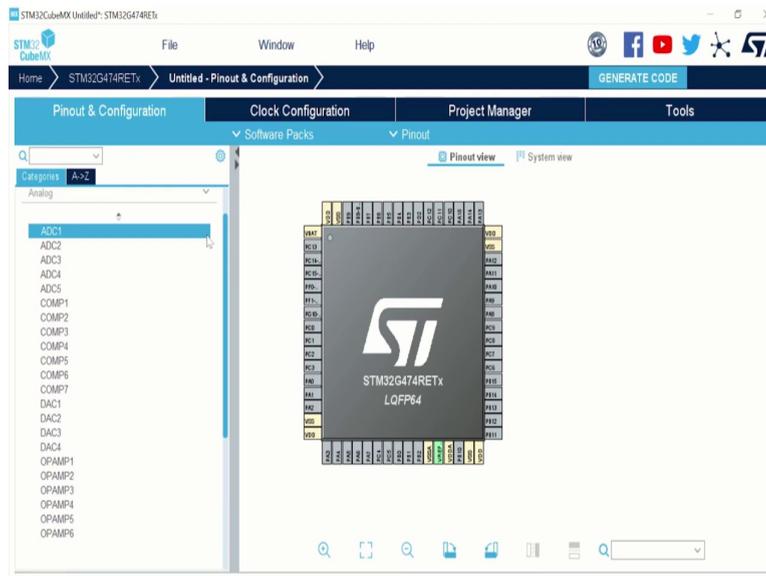


Digital power applications are very much timing critical several analog signals need to be sensed and they will be processed within the microcontroller and accordingly, the right signal or the right PWM needs to be generated to switch on or off our MOSFETs. Along with that in our hardware there are several comparator and op amps to have various comparison sensing with appropriate gain.

For a digital power application, we need to make sure that the MCU clock is fast enough and that the firmware or the code is written inside it should be highly optimized it can be done by using lower-level libraries. So, that every command can be executed in very less clock cycles.

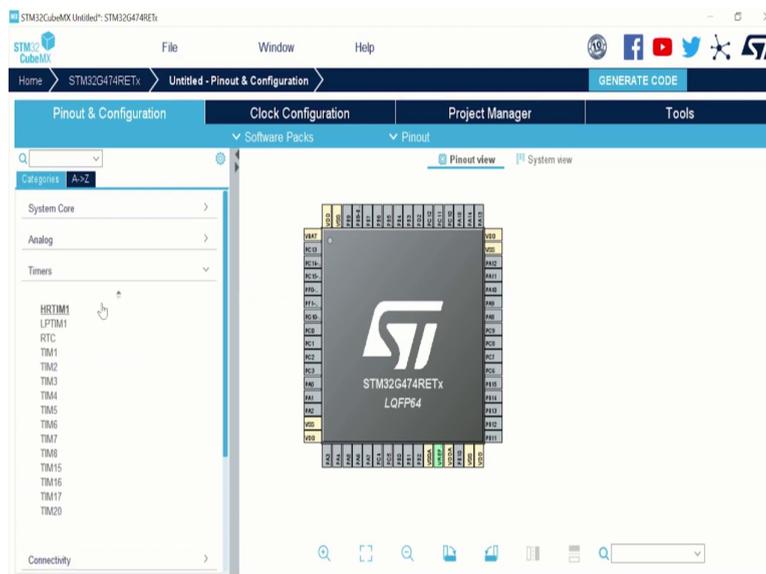
On the screen we can see the STM32 G4 microcontroller which contains all the peripherals required for any digital power applications, on the left side we see all the peripherals it includes; let us text one by one. In the system core, we have already discussed that it includes the GPIO settings, the interrupt settings, and the debugger settings.

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The most important is the analog sections which include the ADC comparator DAC and the OPAMPs the advantages of having the OPAMPs and the comparator inside are not only in terms of cost but also because the space is optimized and we can adjust the various gains inside.

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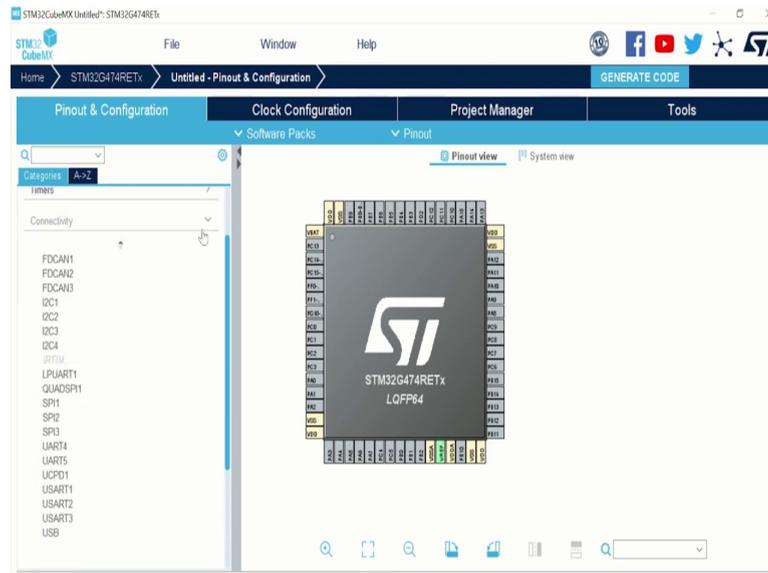


In the timer section, we have high-resolution timers, advanced timers, low-power timers, and several other timers. The high-resolution timer is having a resolution of 184 picoseconds,

where it can be configured and interlinked with several other peripherals and some external events to make it work as an independent state machine.

So, when we say statement that digital power application is very time critical. So, these high-resolution timers and their interlinking with external events are very much helpful.

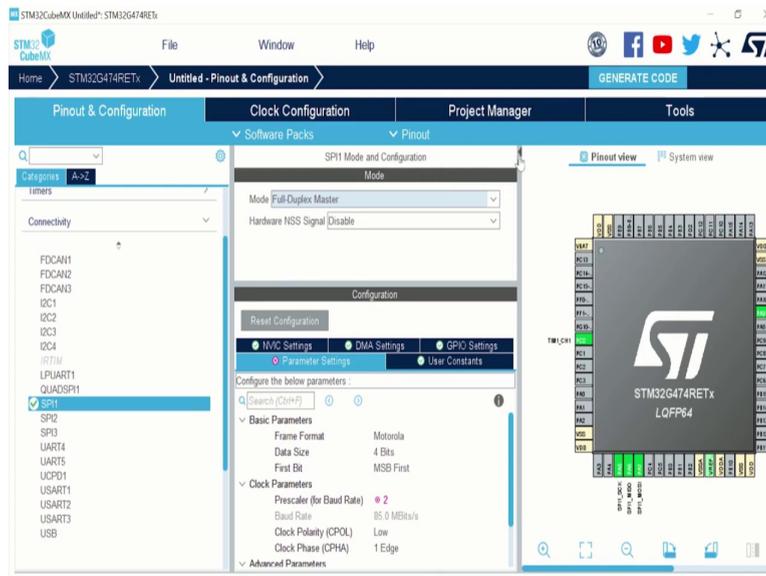
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Apart from that there is the connectivity in which we have the UART SPI I 2C. So, these communication channels can be used as per the requirement, apart from that we can use the middleware computing security as per our user applications though they are not very much required in the digital power applications, but if required we can use it accordingly.

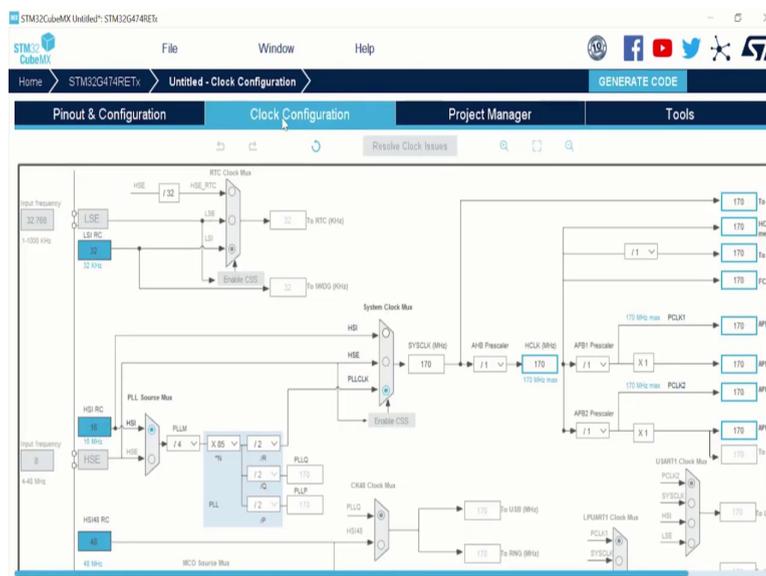
Time and again we highlight that digital power applications are very much time is critical. So, there are some advanced settings in which we can have the best optimizations in terms of code. So, let us go through those settings.

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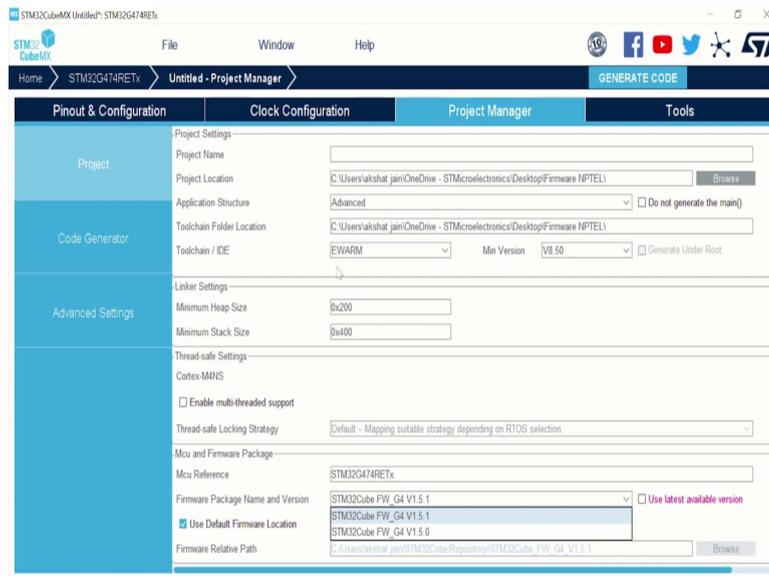
Before we enter into those settings let us configure a few of the pins with different peripherals to explain better. So, just let me configure one of the pins as the timer, and let us take one SPI. So now, we have three peripherals SPI GPIO, and the timer.

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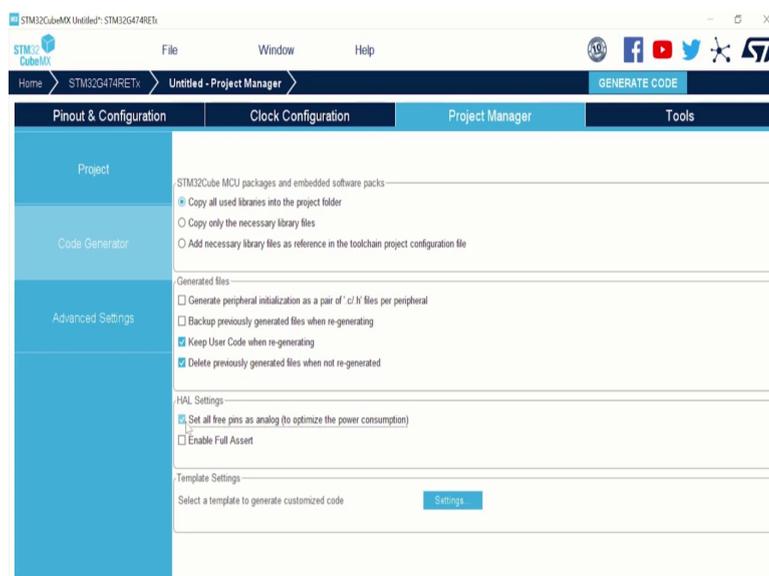
So, on the dark loop, we are on the pin and configuration page. So, this is the second one is the clock and configuration which we have already discussed in the last section.

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The third is the project manager option. So, by default, we see this page where we can enter the project name location, and other things, and in terms of the firmware package that we want to use the package we have already downloaded, we will be able to see here. But just in case we always want to use the latest version, so we can check this option. Also, the stack size that can if you want to change can be updated here, normally it takes a default setting of the microcontroller that we have selected.

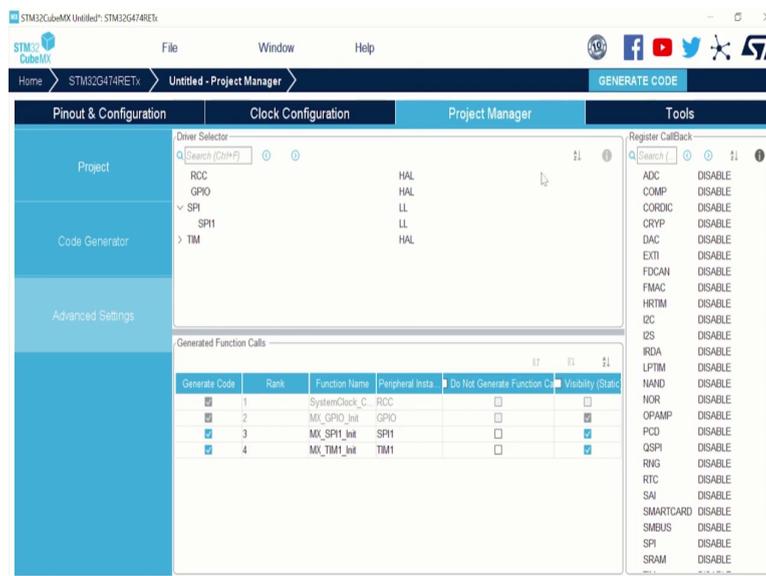
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The second option is the code generator. So, in this, there are again several options depending on the user's requirement by default all the library files have been added to the project folder, but in case there is some space constraint we can use the copy only the necessary library files, and similarly, other options can be chosen.

The third option is very important, so if we select this option set all pins all free pins analog. So, all the unused pins will be configured as analog if we select this option. So, this will be optimized in terms of power.

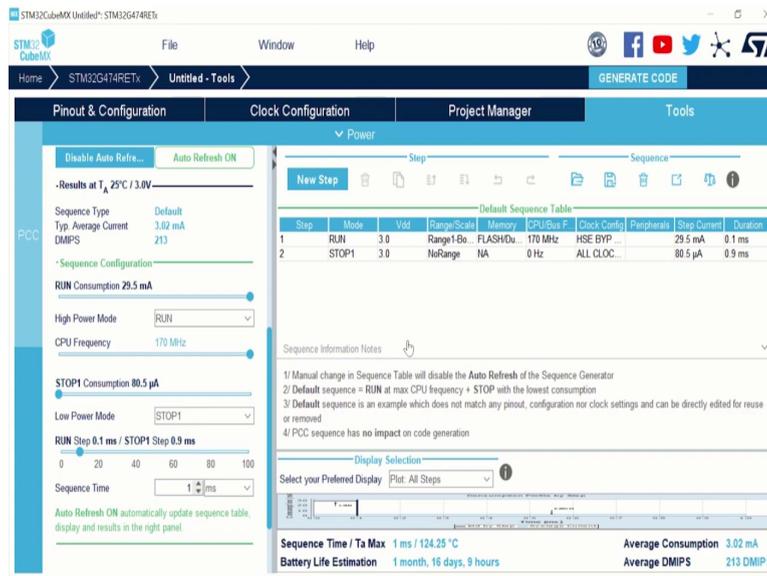
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While if you go on to the advanced setting this is again a very important section in this there are options for selecting the drivers. So, let us take an example of SPI by default hardware abstraction layer is selected as a driver, but if I want to use the lower-level libraries. So, the lower libraries are much more optimized we can directly write the registers it takes fewer clock cycles of the microcontroller to execute the commands.

So, this is very important for digital power applications. I would suggest we can start with the HAL layer to have a better understanding then as we have more understanding of the STM Cube MX store and more understanding of the firmware. Then we can select the lower-level libraries and optimize the code and accordingly we can see the difference.

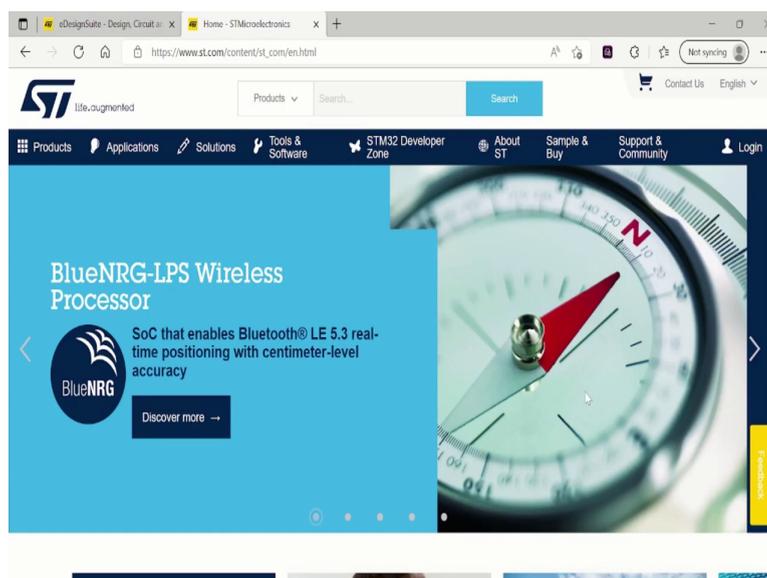
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The last section is the tools here we can see the power consumption and other things as we have not configured all the peripherals, but if you can configure them as per the requirement we can see what sort of power consumption and the stop consumption different modes, what sort of the microcontroller behavior will be there.

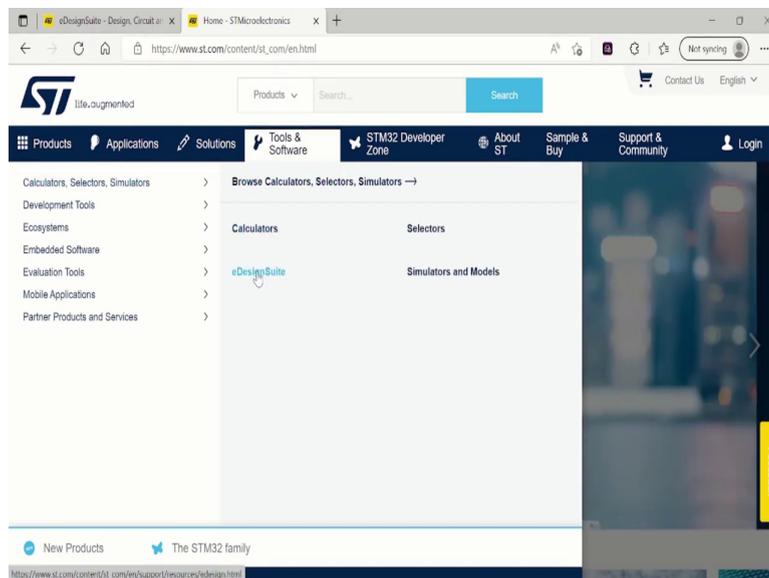
So, we can explore this, but we have to make sure that all the pins of the microcontrollers are configured as per our requirements. Then this tool page is very much important for us.

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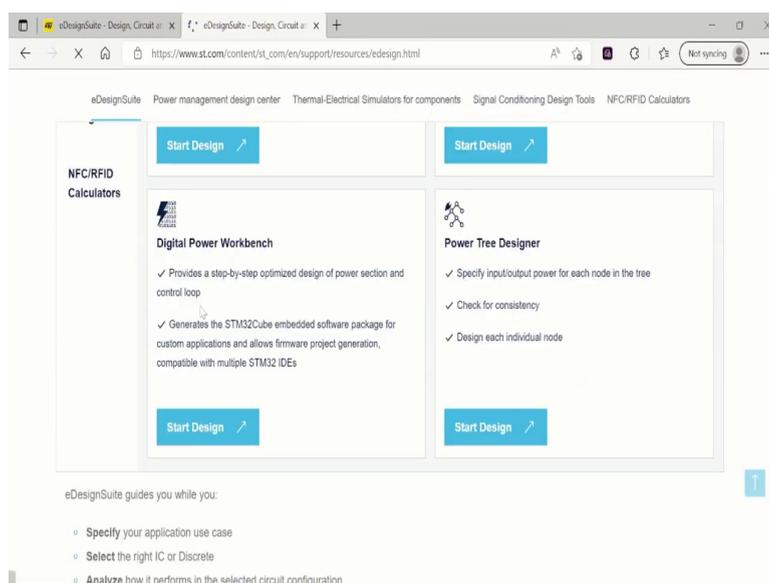
Based on the STM32CubeMX tool there is another power powerful tool from ST microelectronics which is a digital power workbench.

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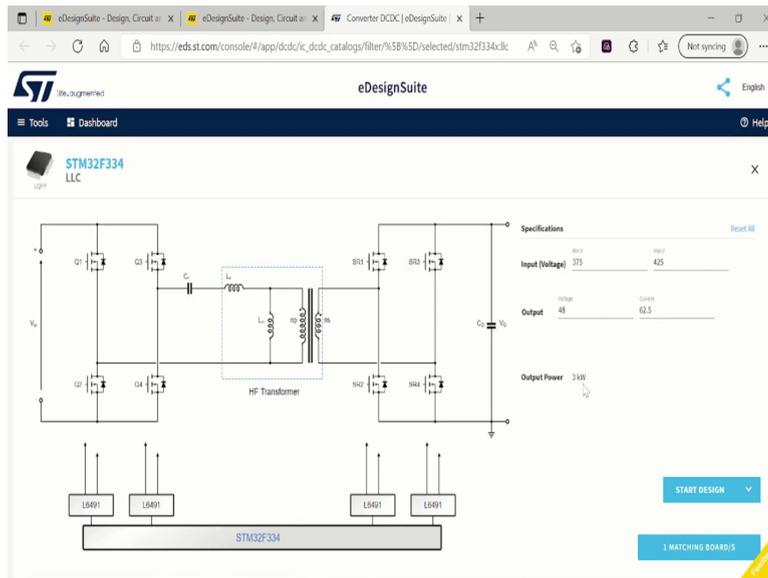
So, I am on the st website, if I go to the tools and software section and within the first section I could see the eDesign Suite.

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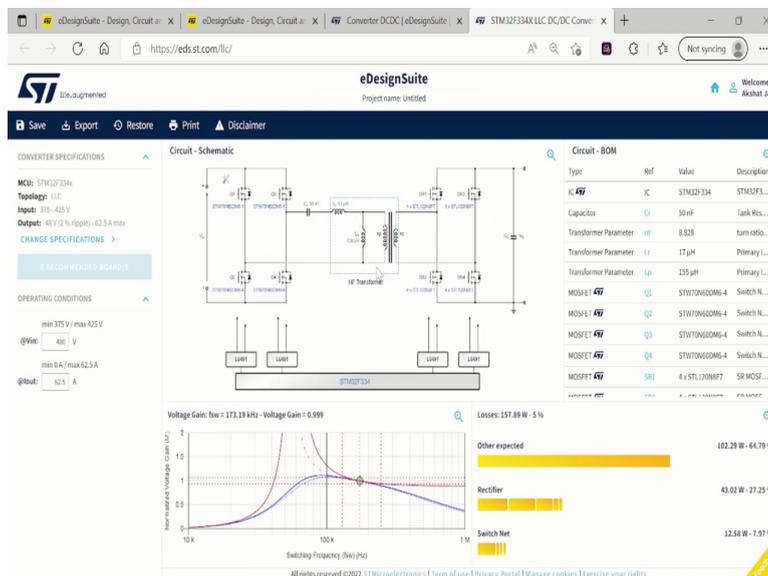
In eDesign suit, if you scroll down we can see the digital power workbench.

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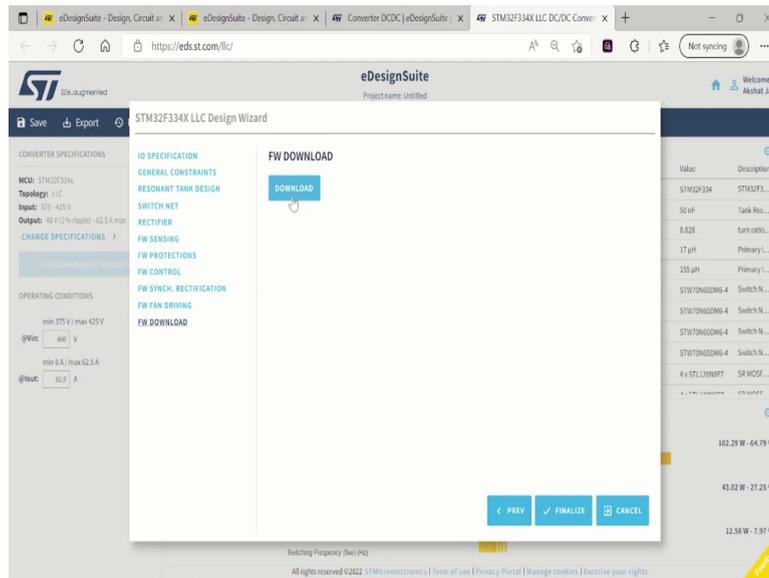
We can click on star design this is a very much new tool from st microelectronics right, now we have the full bridge LLC converter with synchronous rectification; it is based on STM32F334. More and more topologies and microcontrollers are being added. So, without changing any specifications it is a 3-kilowatt configuration I just click on start design.

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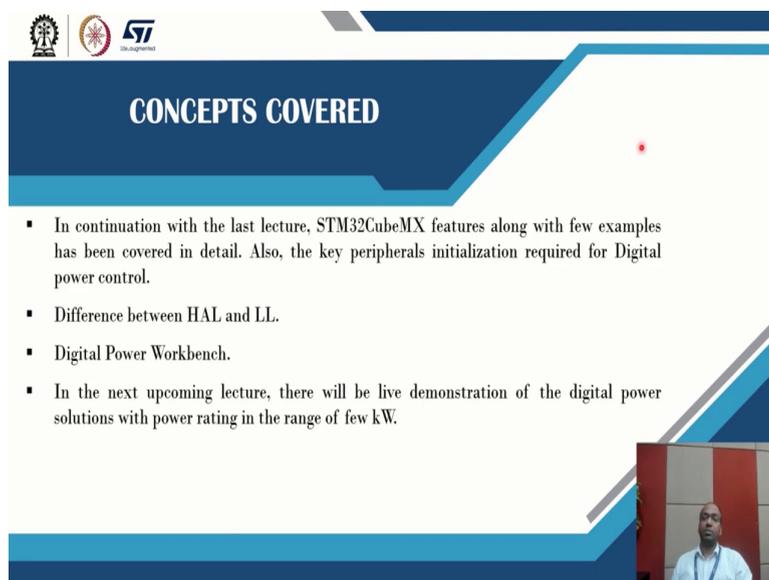
So, it has already taken my login, but if you want to do the login and make some changes in the configuration settings or you want to update the power rating, or some other MOSFETs you want to explore you can just do the login. And the most important thing is when we go to the specifications the last option is the firmware download.

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So, here we can download the firmware. So, it will be downloading your complete firmware package along with the STM Cube MX file, there also you can do certain changes and make your application work.

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Concepts covered in the last lecture and the ongoing lecture we have covered the features of the STM Cube MX software tool along with some examples, also we have discussed key peripherals required for the digital power application.

Then we discussed the advanced settings particularly the HAL layer and the lower layer differences. Then we also touched on the digital power workbench particularly for the LLC application while in the upcoming lecture, there will be live demonstrations of digital power solutions with a power rating in the range of a few kilowatts.

Thank you see you in the next lecture.