

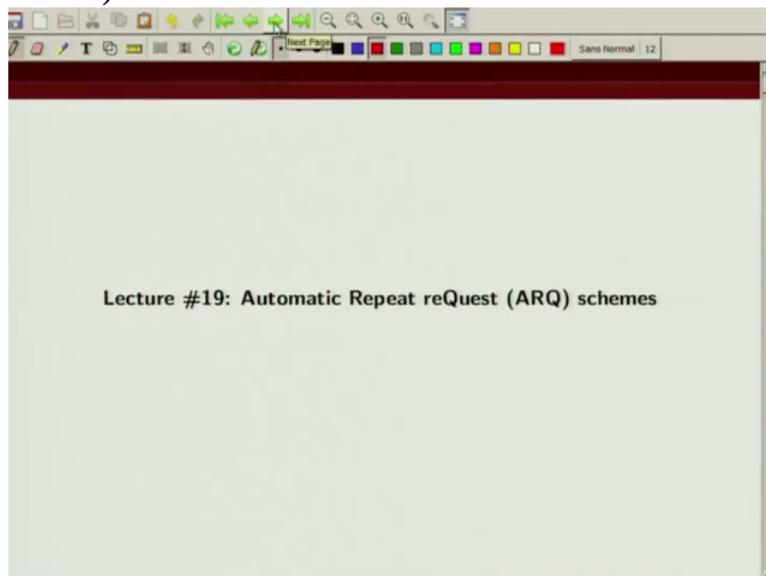
An Introduction to Coding Theory
Professor Adrish Banerji
Department of Electrical Engineering
Indian Institute of Technology, Kanpur
Module 08
Lecture Number 32
Automatic Repeat reQuest (ARQ) schemes

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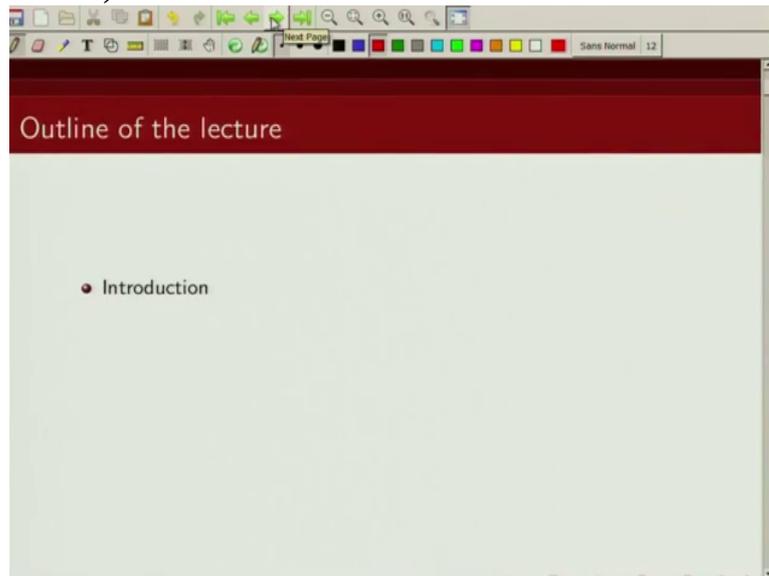
Welcome to the course on An Introduction to Coding Theory. I am Adrish Banerji. And today we are going to talk

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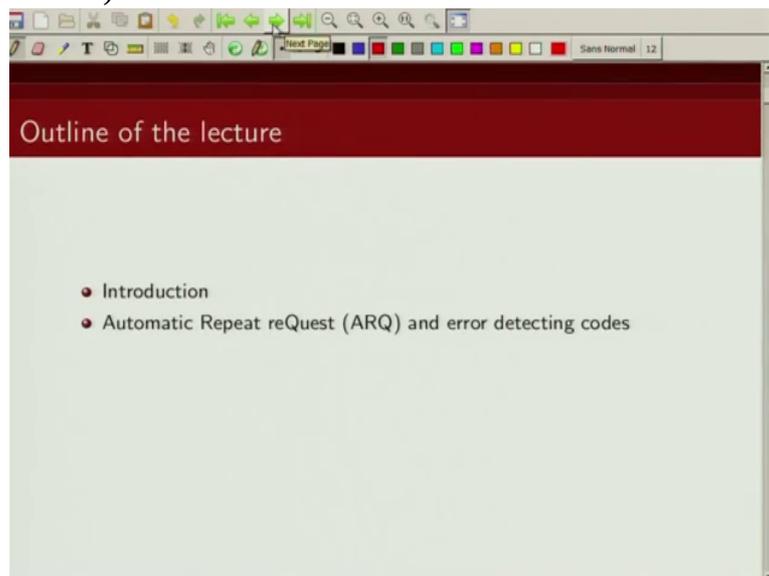
about Automatic Repeat Request schemes.

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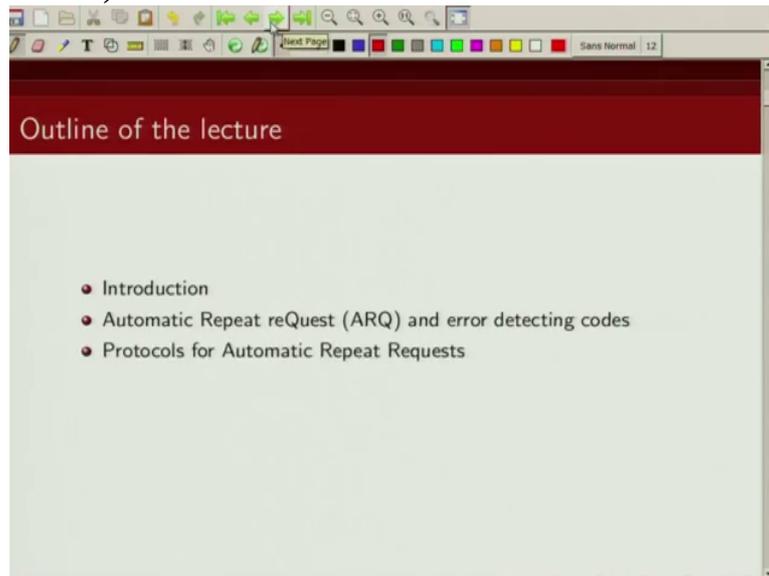
So this is the outline of the talk today. I am first going to briefly describe what is the A R Q scheme

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and then I will talk about the role of A R Q codes in A R Q schemes.

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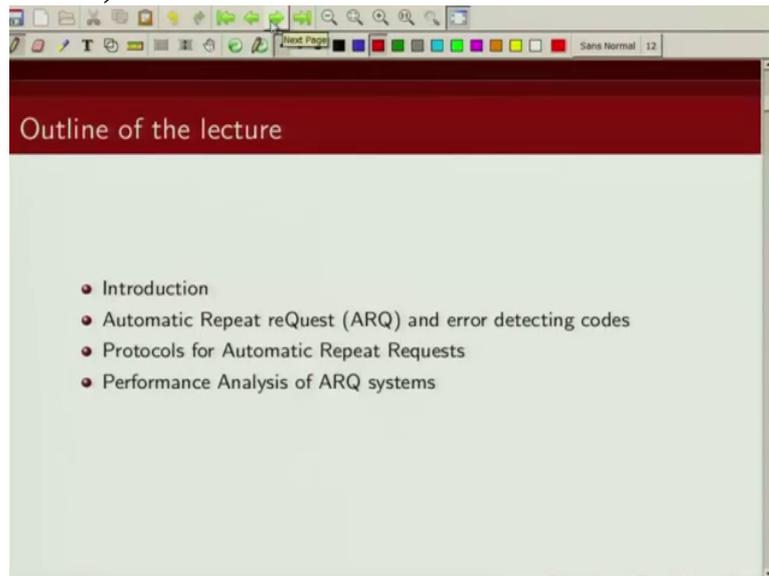
Subsequently I will be talking about 3 different protocols for

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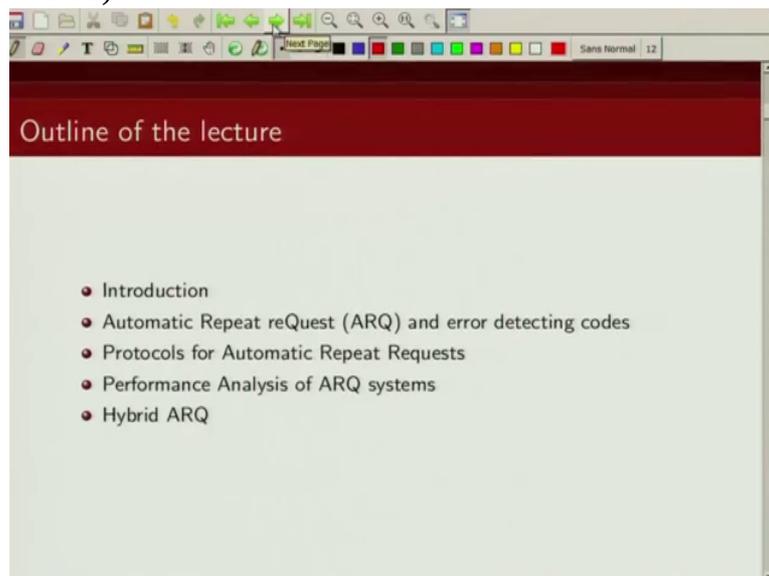
retransmission that we are going to use. And then I am

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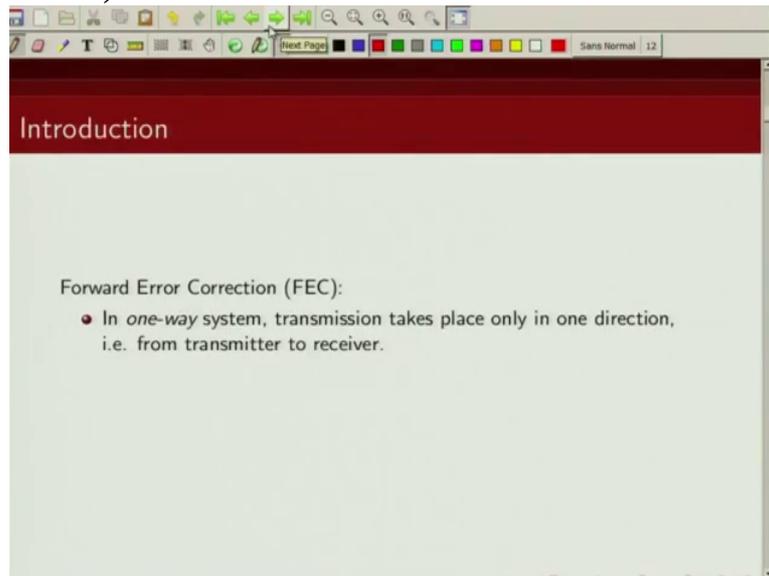
going to analyze the performance of those 3 protocols and I am going to analyze in terms of efficiency of those protocols as well as reliability of those A R Q protocols

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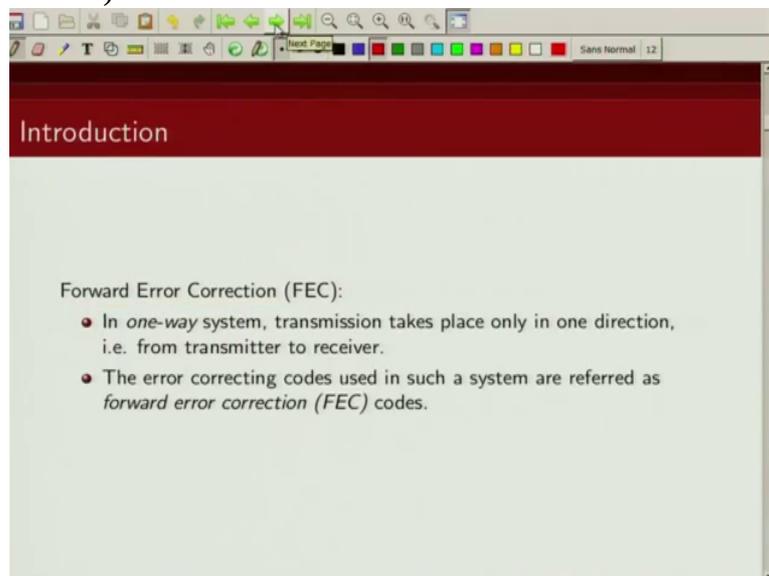
and finally I am going to talk about hybrid A R Q which is the combination of forward error correction and automatic Repeat Request schemes.

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So if you recall in the first lecture we talked about there are two possible strategies for error correction. One is forward error correction. So channels which are one way, so for example there is only one way communication from transmitter to receiver; so in those schemes, in those kinds of channels we used forward error correction schemes. Now

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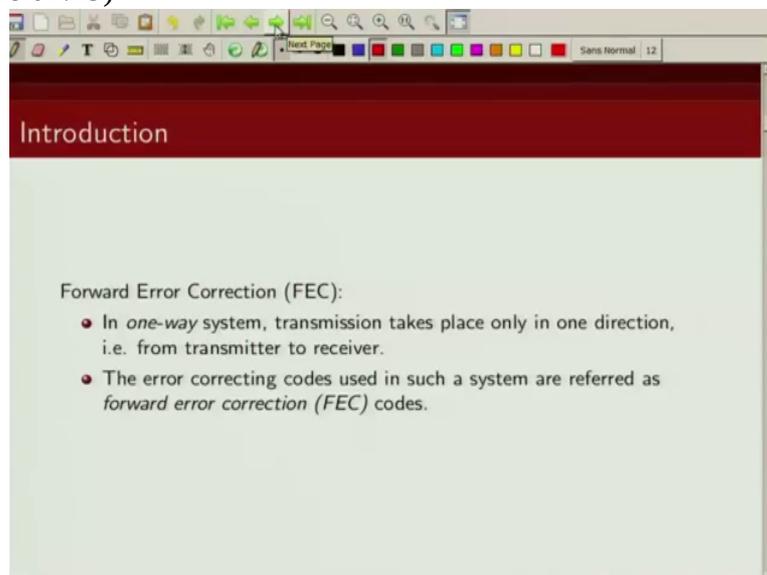
what do we do in forward error correcting schemes? So we encode our information sequence using an error correcting code and send it over the communication link.

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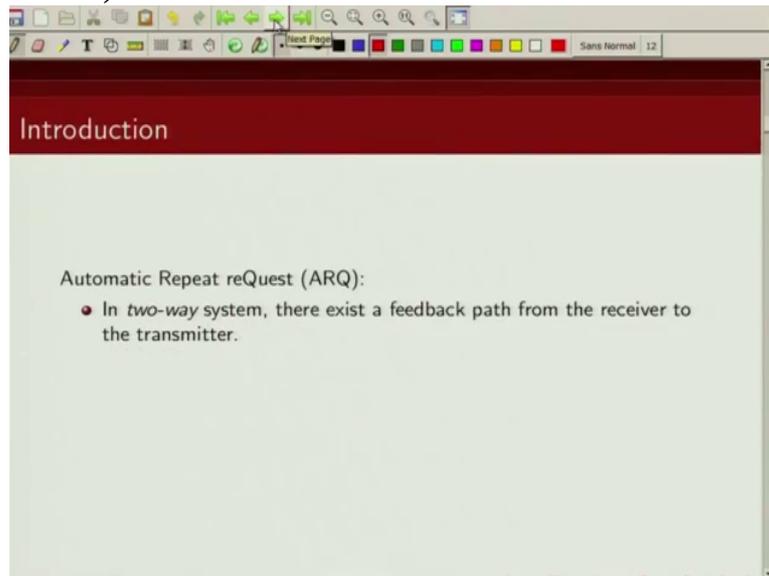
The receiver will try to detect and correct errors based on the redundant bits that we have transmitted. If the packet is not received correctly in a forward error correcting scheme, there is no mechanism for receiver to let the transmitter know that it has not received correctly because it is a one way channel. And the error correcting code used in these channels are known

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as forward error correction codes.

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Now channels where there is a feedback from the receiver

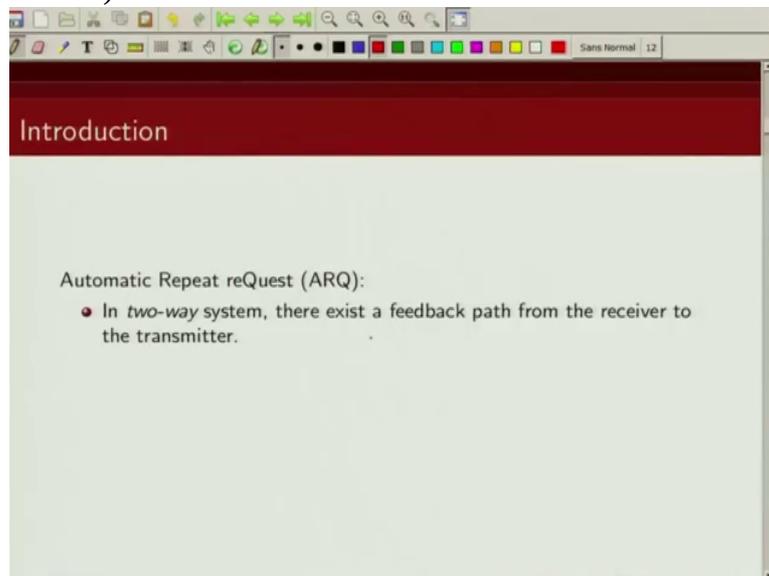
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to the transmitter, so the receiver, if it receives the information sequence, we can encode the information sequence using error correcting codes which we may use for error detection. Now once we transmit this packet to the receiver, the receiver will try to detect if there is a parity violation. Now if the receiver is able to detect that the bits are not received correctly, the receiver is going to send a negative acknowledgment to the transmitter because it is a two way channel, so through this feedback channel the receiver will inform the transmitter that it has not received the packet correctly and then the transmitter will re-transmit the packet. Now if the packet which was transmitted is received error free then the receiver will send the

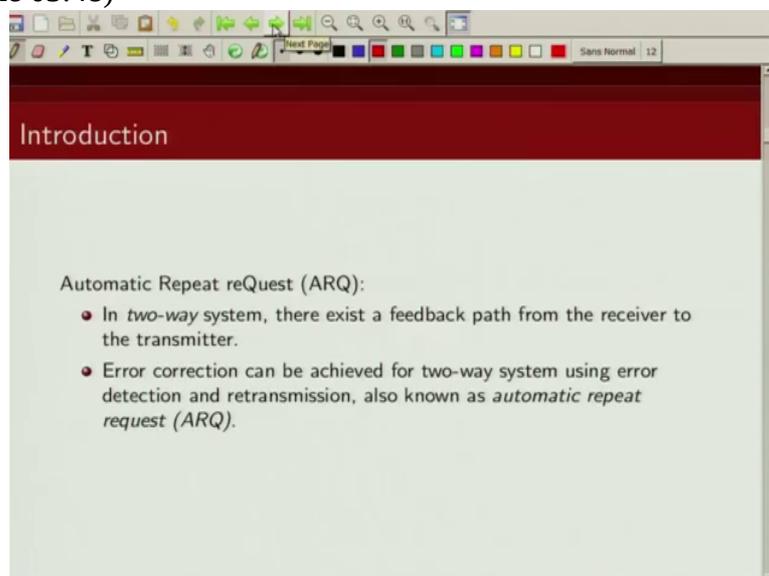
acknowledgement saying I have received the packet correctly and then the transmitter will send a new packet. So in a two way communication channel there exists

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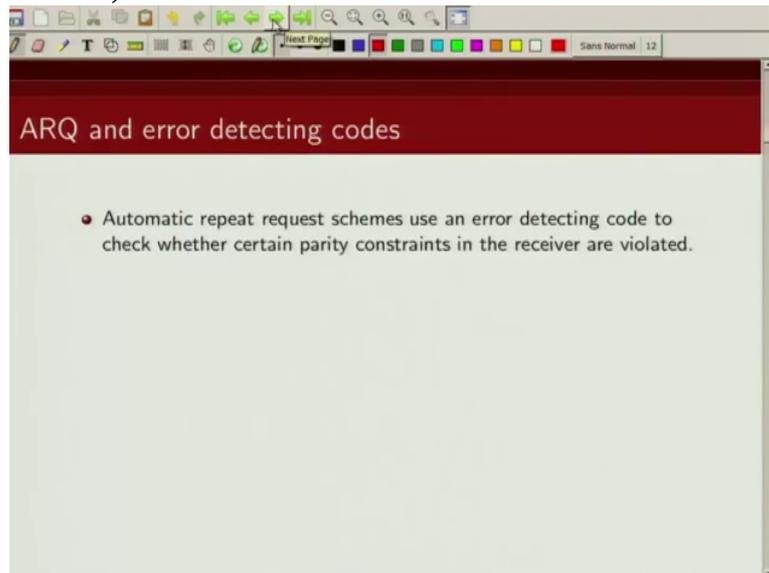
a feedback path from the receiver to the transmitter

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and the error correcting code that we use for such a communication channels are known as automatic repeat request which are, which is also known by this acronym A R Q.

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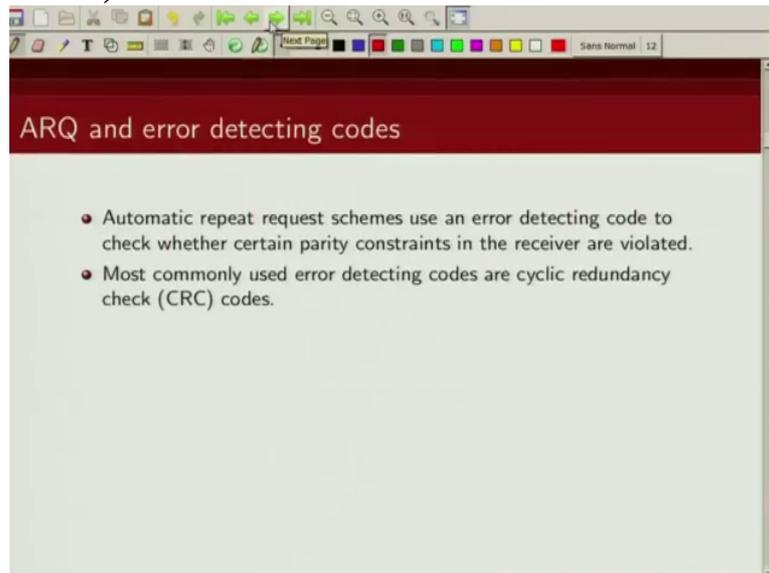
Now what is the role of error detecting code in A R Q schemes? So an automatic repeat request scheme uses an error detecting code to find out whether the packet that we transmitted over the communication channel, whether the packet has been

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received correctly or not. So what we do at the transmitter is we encode this information sequence using an error detecting code and we transmit over this communication channel. Now the receiver, uh this error detecting code will try to find out whether the packet has been received correctly or not using those additional parity bits.

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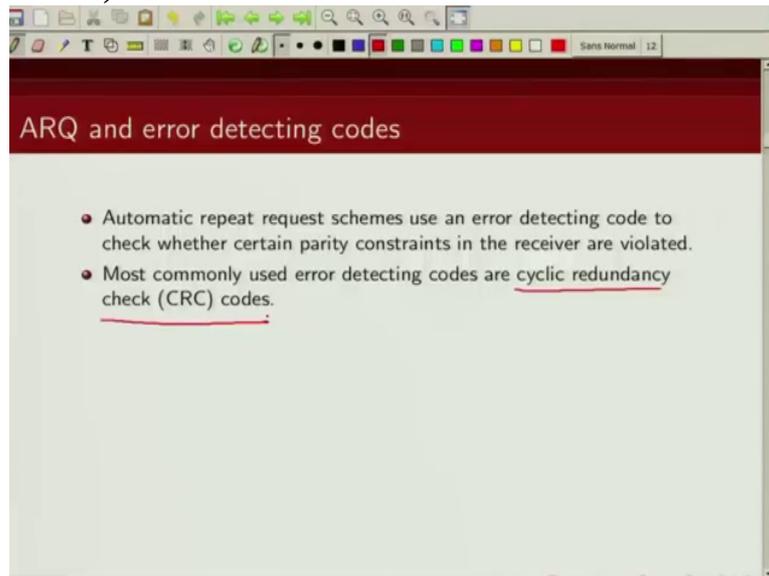
Now one of the most commonly used error detecting code in A R Q code application is what is known as C R C, cyclic redundancy check codes. C R C codes are error detecting codes which are very popularly used for A R Q applications. So what is done is we

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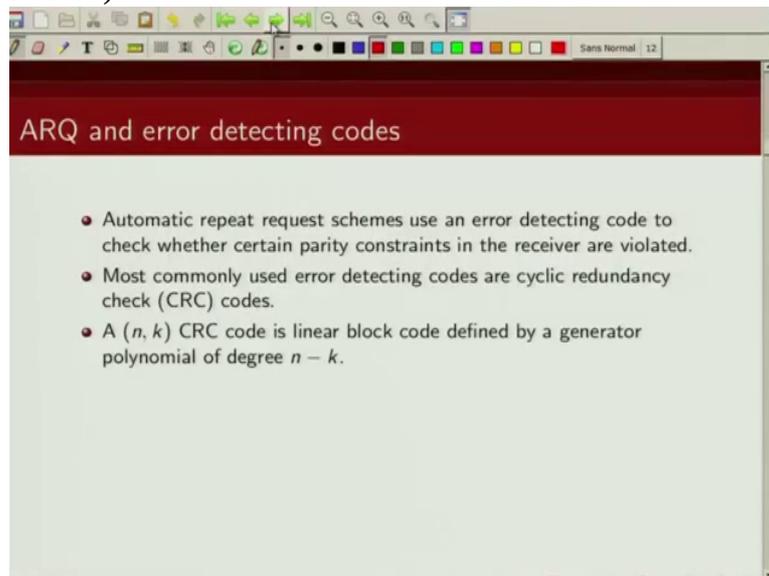


encode our information sequence using this C R C code and send it over the communication channel and then using those additional parity bits, at the receiver we will try to find out whether the bits have been received correctly or not.

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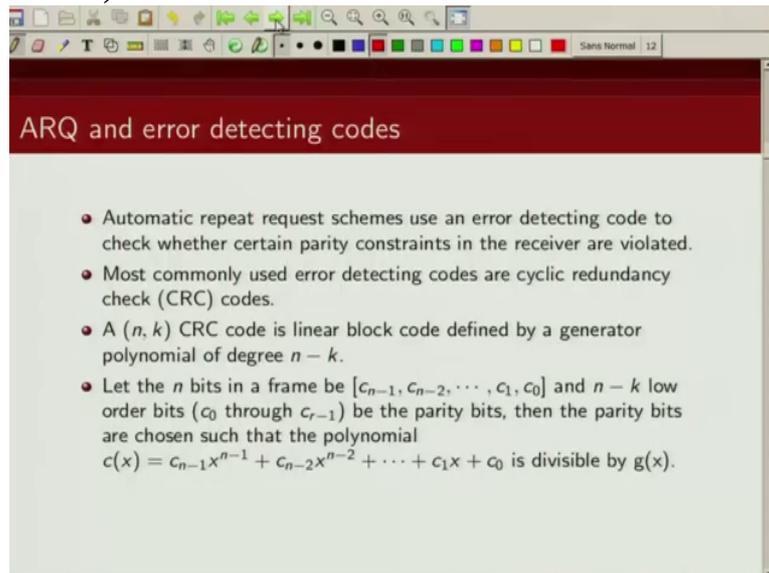


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So it is an n, k code, linear block code. So block size is, code length is n . Information sequence length is k . So number of parity bits is n minus k . So a CRC code is defined by generator polynomial of degree n minus k which is number of parity bits that we are using.

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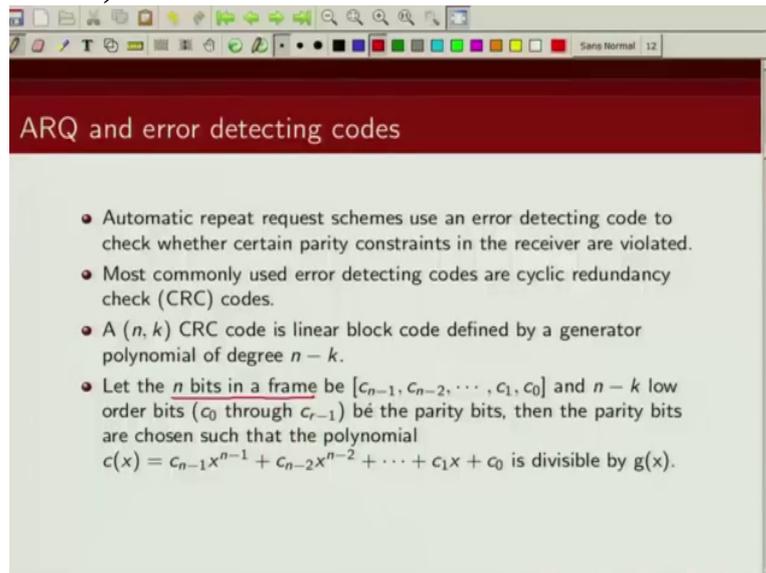
So let the n bits in a frame, now we are going to use interchangeably the word packet, frame. Essentially we are sending a block of

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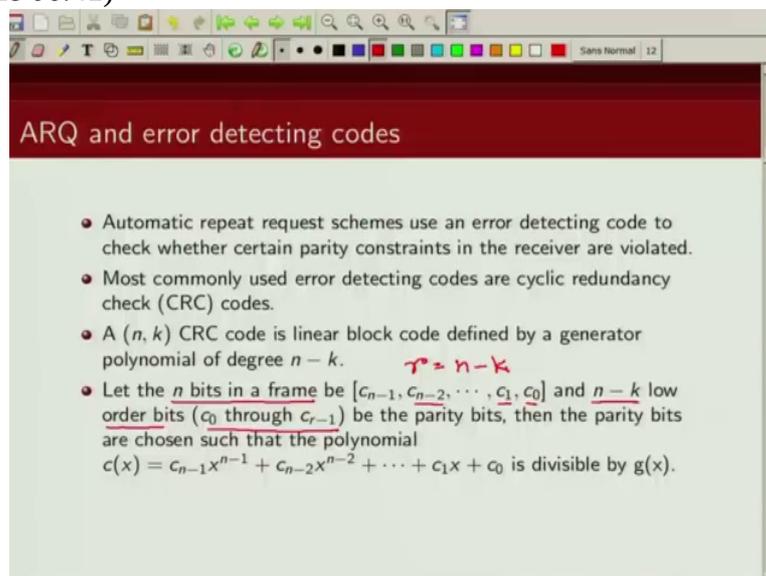
n bits. So let us denote

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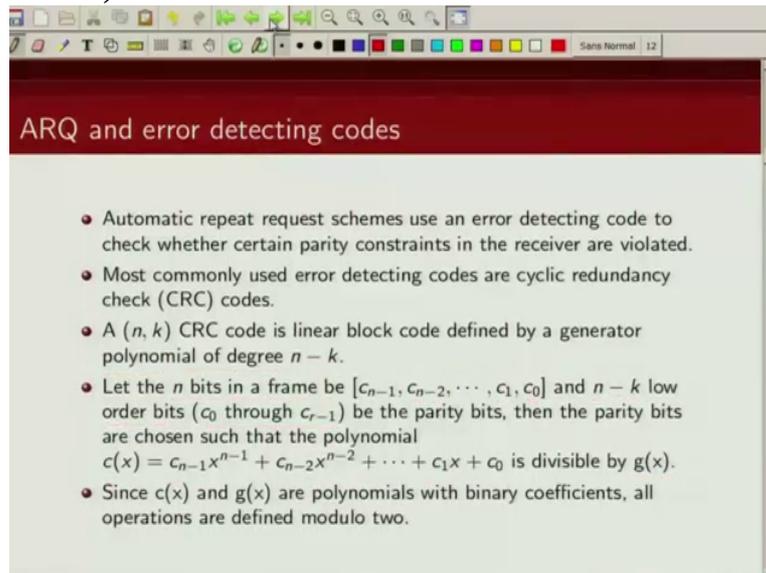
the block of n bits by $c_{n-1}, c_{n-2}, \dots, c_1, c_0$. So this is a block of n bits and let the $n - k$ low order bits that means from c_0 to c_{r-1} , what is r , r is essentially my $n - k$,

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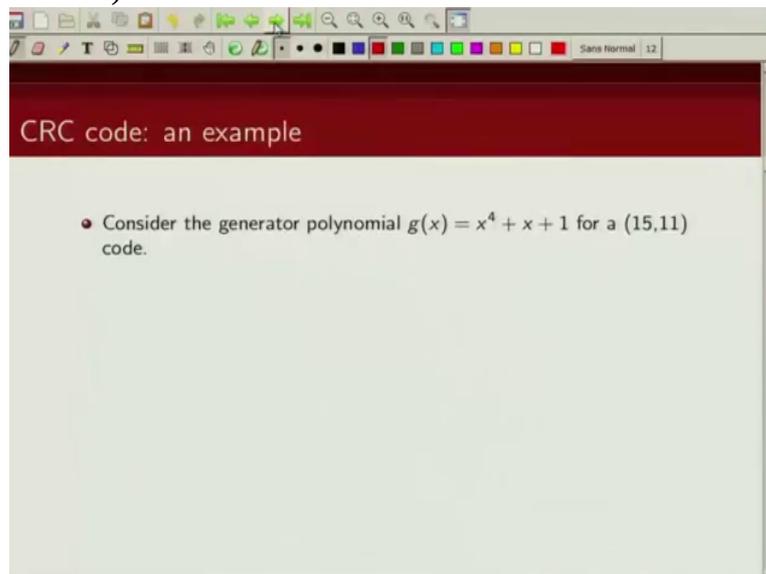
Ok. So let us assume that the low order $n - k$ bits are my parity bits. So then in CRC code, the parity bits are chosen in such a way such that this code polynomial is divisible by the generator polynomial of the CRC code. So we are going to choose this $c_0, c_1, c_2, c_3, \dots, c_{r-1}$ in such a way such that this code polynomial $c(x)$ is divisible by this generator polynomial of the CRC code of degree $n - k$. Now let us take an example

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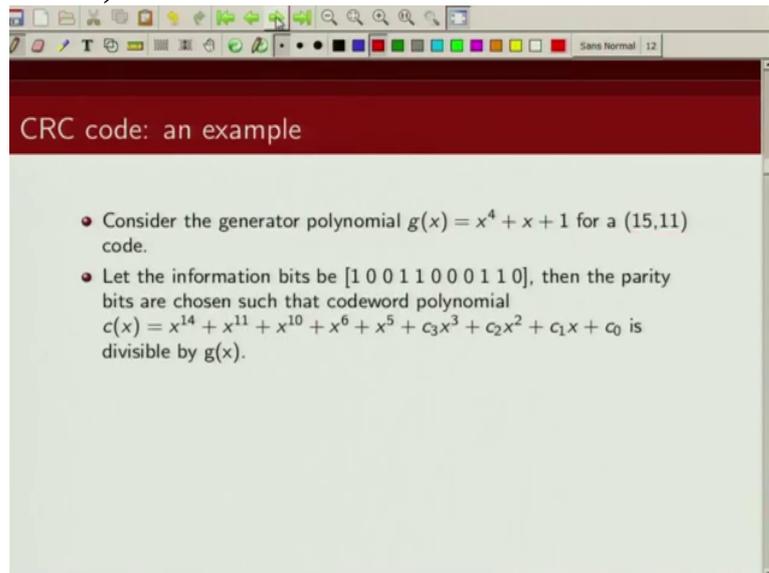
to illustrate this. And remember when we talking about, so these code polynomial as well as generator polynomials; they are polynomial with binary coefficients. So we are talking about all binary operations. So these operations are defined over modulo 2.

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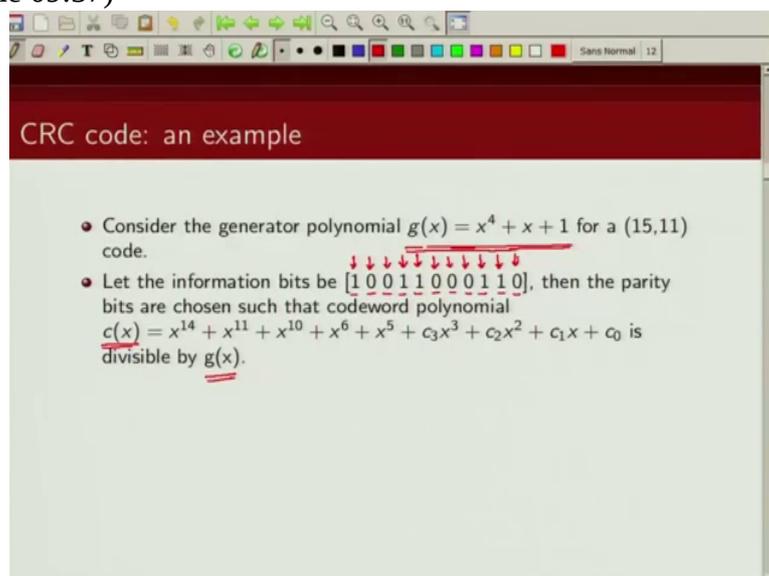
Let us take an example of a CRC code. So we consider a CRC code which has 4 parity bits whose generator polynomial is given by x to power 4 plus x plus 1. So this is a uh, generator polynomial for CRC code and our n, k code parameters are given by 15 11. So n is 15 and k is 11 and there are 4 parity bits.

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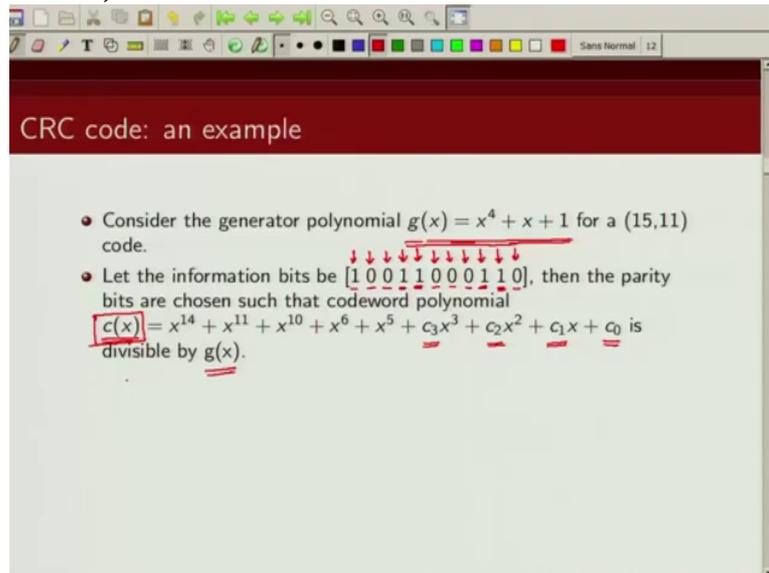
So let us assume those 11 bits are given by this. 1 0 0 1 1 0 0 0 1 1 0. So these are the 11 information bits. Now we want to encode these information bits using a C R C code which will add 4 parity bits whose generator polynomial is given by this. $v x$ is x^4 plus x plus 1. Now remember we need to choose 4 parity bits in such a way such that this code polynomial c of x is divisible by this generator polynomial g of x . So we are going to write this information sequence in this polynomial form. So this is my c_{14} , c_{13} , c_{12} , c_{11} , c_{10} , c_9 , c_8 , c_7 , c_6 , c_5 , c_4 and then

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c_3 , c_2 , c_1 , c_0 these are my parity bits. So remember this is x^{14} plus x^{11} plus x^{10} plus x^6 plus x^5 plus $c_3 x^3$ plus $c_2 x^2$ plus $c_1 x$ plus c_0 . This is my code polynomial. Now I need to choose this c_3 , c_2 , c_1 , c_0 in such a way such that this code polynomial c of x

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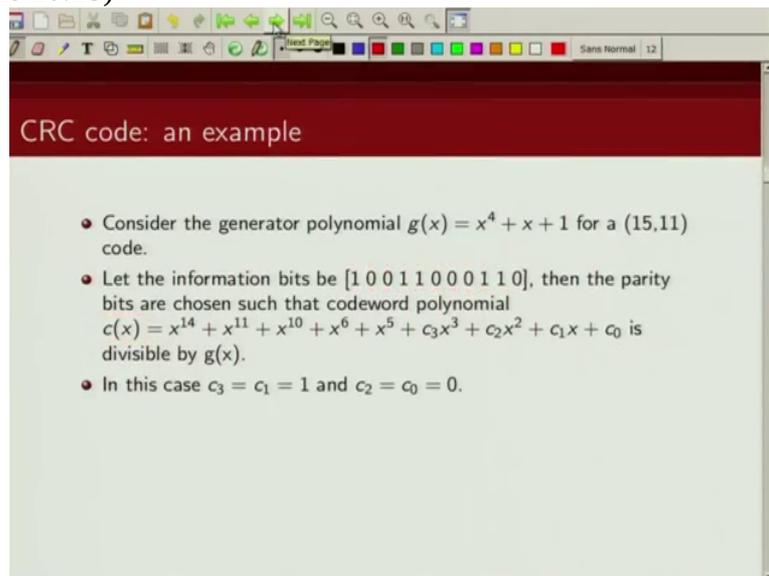
The slide is titled "CRC code: an example" and contains the following text:

- Consider the generator polynomial $g(x) = x^4 + x + 1$ for a (15,11) code.
- Let the information bits be $[1\ 0\ 0\ 1\ 1\ 0\ 0\ 0\ 1\ 1\ 0]$, then the parity bits are chosen such that codeword polynomial $c(x) = x^{14} + x^{11} + x^{10} + x^6 + x^5 + c_3x^3 + c_2x^2 + c_1x + c_0$ is divisible by $g(x)$.

Red arrows point from the information bits to the corresponding terms in the polynomial. Red boxes highlight the polynomial expression and the phrase "divisible by g(x)".

is divisible by g of x , this generator polynomial for this C R C code.

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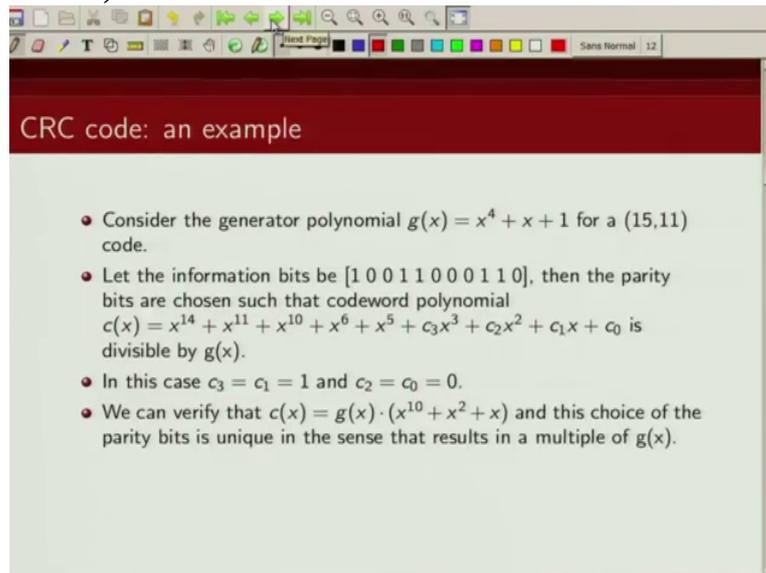


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- In this case $c_3 = c_1 = 1$ and $c_2 = c_0 = 0$.

So in this case we can verify that if we choose c_3 and c_1 to be 1, and c_2 and c_0 to be zero, then this code polynomial is divisible by g of x

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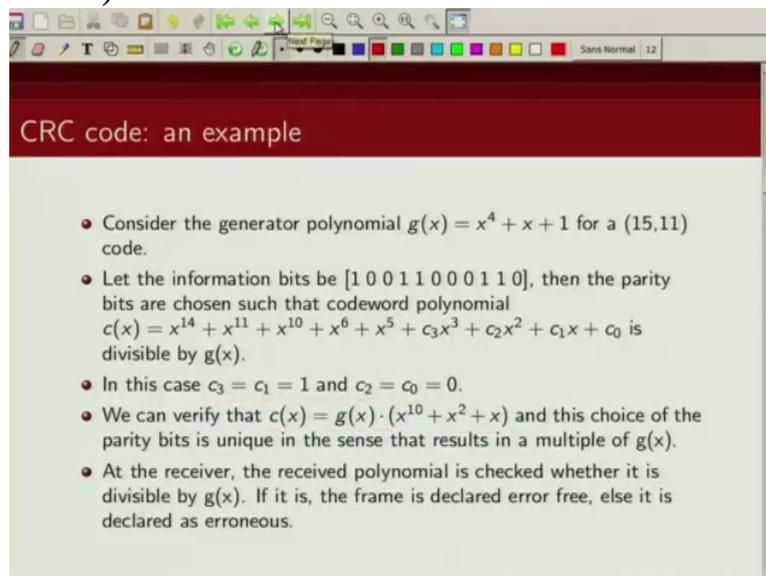


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- Let the information bits be [1 0 0 1 1 0 0 0 1 1 0], then the parity bits are chosen such that codeword polynomial $c(x) = x^{14} + x^{11} + x^{10} + x^6 + x^5 + c_3x^3 + c_2x^2 + c_1x + c_0$ is divisible by $g(x)$.
- In this case $c_3 = c_1 = 1$ and $c_2 = c_0 = 0$.
- We can verify that $c(x) = g(x) \cdot (x^{10} + x^2 + x)$ and this choice of the parity bits is unique in the sense that results in a multiple of $g(x)$.

and we can see that in this case if we choose our c_3, c_2, c_1, c_0 in this fashion our $c(x)$ is divisible by $g(x)$ and this is basically product of $g(x)$ into $x^{10} + x^2 + x$. And this is a unique choice of these parity bits, c_0, c_1, c_2, c_3 which will ensure that $c(x)$ is divisible by $g(x)$.

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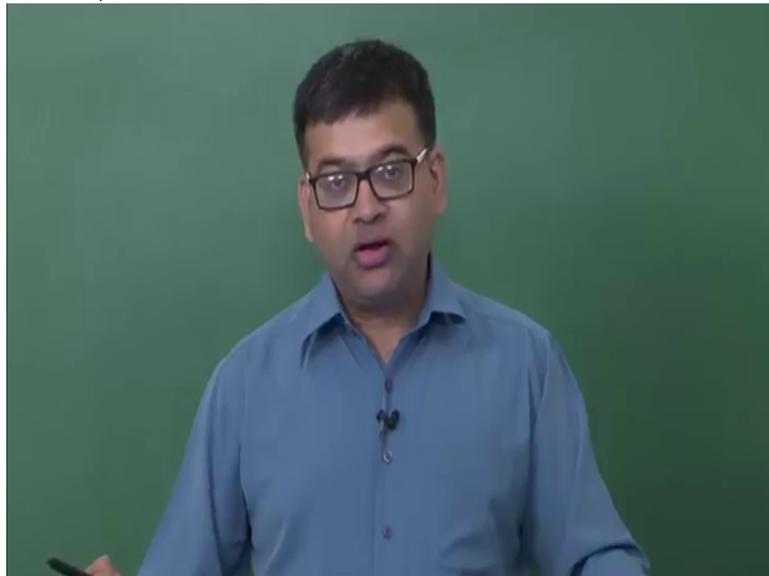


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- We can verify that $c(x) = g(x) \cdot (x^{10} + x^2 + x)$ and this choice of the parity bits is unique in the sense that results in a multiple of $g(x)$.
- At the receiver, the received polynomial is checked whether it is divisible by $g(x)$. If it is, the frame is declared error free, else it is declared as erroneous.

Now at the receiver what we are going to do is once we get the received polynomial we are going to check whether there are errors or not.

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How are we going to check? We are going to divide this received polynomial by g of x . If it is divisible by g of x that means there are either no error or it has converted into some other codeword and the errors are undetected, Ok. Now if this received polynomial

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A screenshot of a presentation slide. The slide has a dark red header with the title "CRC code: an example" in white text. Below the header, there is a list of five bullet points in red text on a light green background. The slide is shown within a window that has a standard toolbar at the top and a status bar at the bottom indicating "Sans Normal | 12".

CRC code: an example

- Consider the generator polynomial $g(x) = x^4 + x + 1$ for a (15,11) code.
- Let the information bits be [1 0 0 1 1 0 0 0 1 1 0], then the parity bits are chosen such that codeword polynomial $c(x) = x^{14} + x^{11} + x^{10} + x^6 + x^5 + c_3x^3 + c_2x^2 + c_1x + c_0$ is divisible by $g(x)$.
- In this case $c_3 = c_1 = 1$ and $c_2 = c_0 = 0$.
- We can verify that $c(x) = g(x) \cdot (x^{10} + x^2 + x)$ and this choice of the parity bits is unique in the sense that results in a multiple of $g(x)$.
- At the receiver, the received polynomial is checked whether it is divisible by $g(x)$. If it is, the frame is declared error free, else it is declared as erroneous.

is divisible by g of x then we declare that the frame is error free or else if it is not divisible by g of x , we know that there is an error in the received sequence. So this is how we are going to make use of this C R C for error

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detection

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CRC codes in practice

- Examples of some popular CRC codes

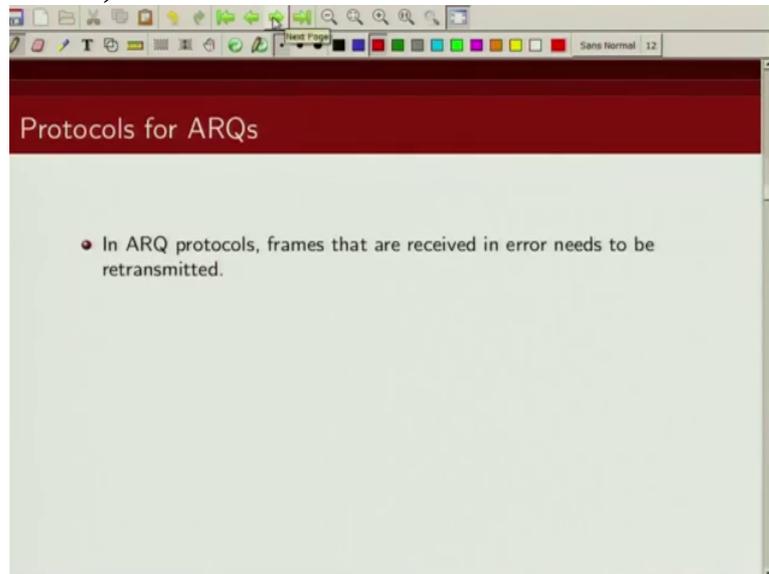
Code	$g(x)$	n_{max}	d_{min}
CRC-12	$x^{12} + x^{11} + x^3 + x^2 + x + 1$	2,047	4
CRC-ANSI	$x^{16} + x^{15} + x^2 + 1$	32,767	4
CRC-CCITT	$x^{16} + x^{12} + x^5 + 1$	32,767	4

Table: Properties of Three Standard CRC Codes

I have listed some of the popular CRC codes. So this CRC 12 code, its generator polynomial is given by this, $x^{12} + x^{11} + x^3 + x^2 + x + 1$. The maximum n that it can handle is two thousand forty seven and its minimum distance is 4. So it can detect 3 errors. Another popular code is CRC ANSI code which has, this is 16 bit CRC code. Generator polynomial given by $x^{16} + x^{15} + x^2 + 1$ and can handle n of thirty two thousand seven sixty seven. Its minimum distance is also 4. Another 16 bit CRC code which is popular is CRC CCITT code and again its generator polynomial is given by $x^{16} + x^{12} + x^5 + 1$. Its minimum distance is also 4 and it can take maximum n of thirty two thousand seven sixty seven. So if you subtract from this n , the number of parity

bits in this case its 12, in this case it is 16, in this case it is 16 you will get the size of the information sequence that you are encoding using these C R C codes.

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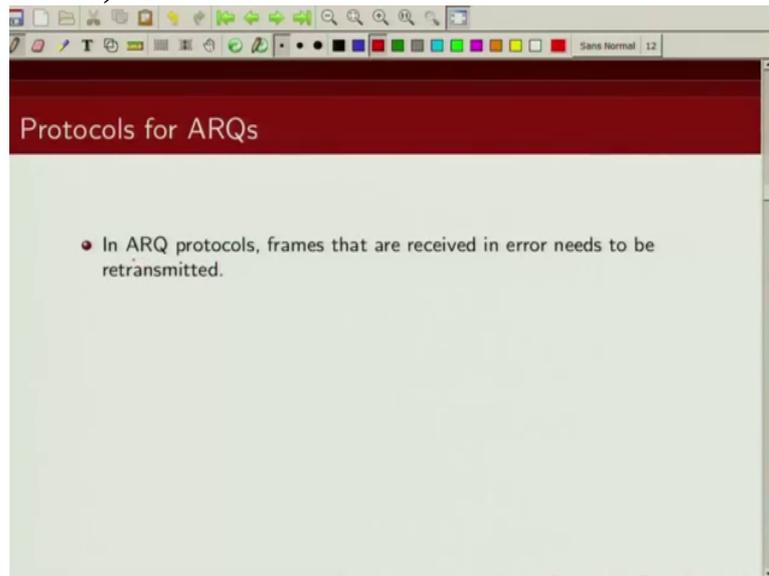
Now that we have explained what is an A R Q scheme

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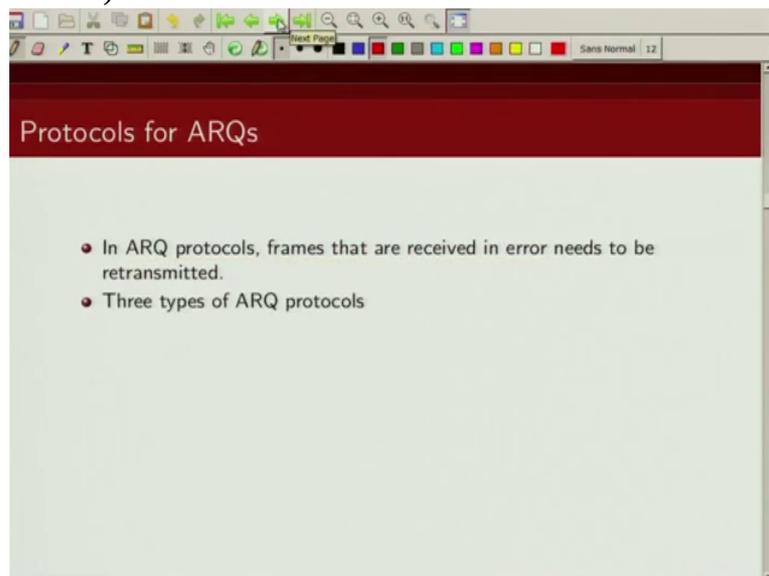
and the role played by error detecting code, let us talk about what are the different protocols that we can employ when a packet is detected to be in error? What are the different ways in which we can do retransmission? So as you know

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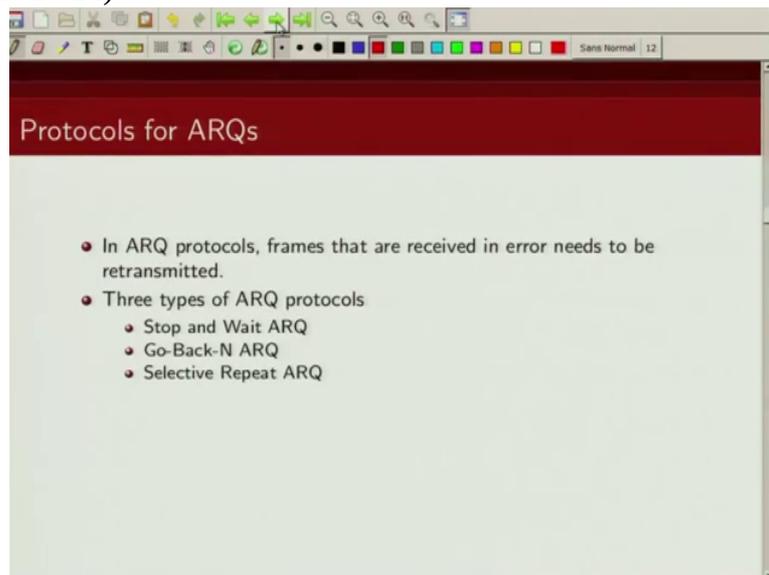
in A R Q protocols if the frames are received in error, you need to retransmit those frames or packets.

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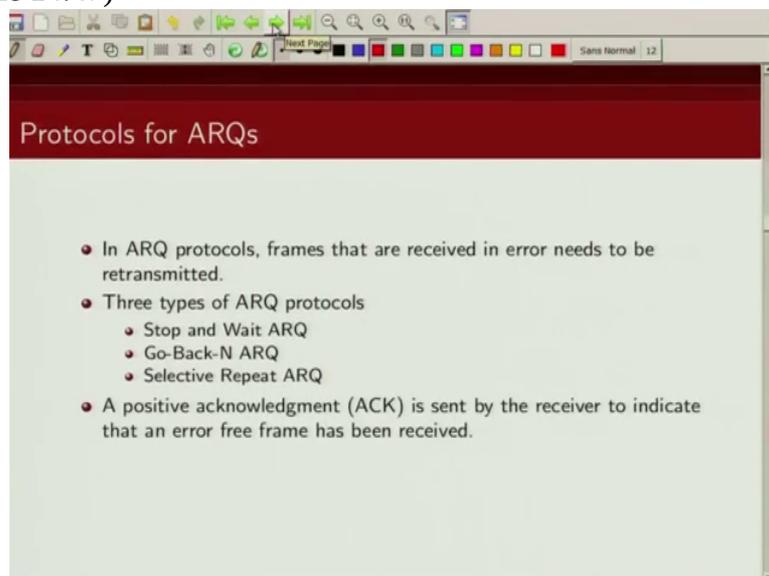
So we are going to talk about 3 different protocols for A R Q,

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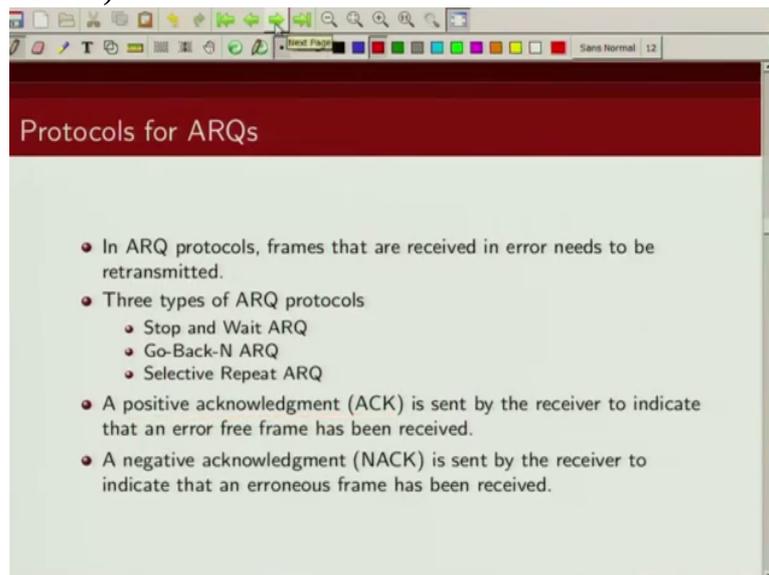
the first one is called Stop and Wait, the second is known as Go Back N and the third is known as Selective Repeat A R Q scheme. We are going to talk about each one of them in detail and we are also going to do performance analysis of each of these A R Q protocols.

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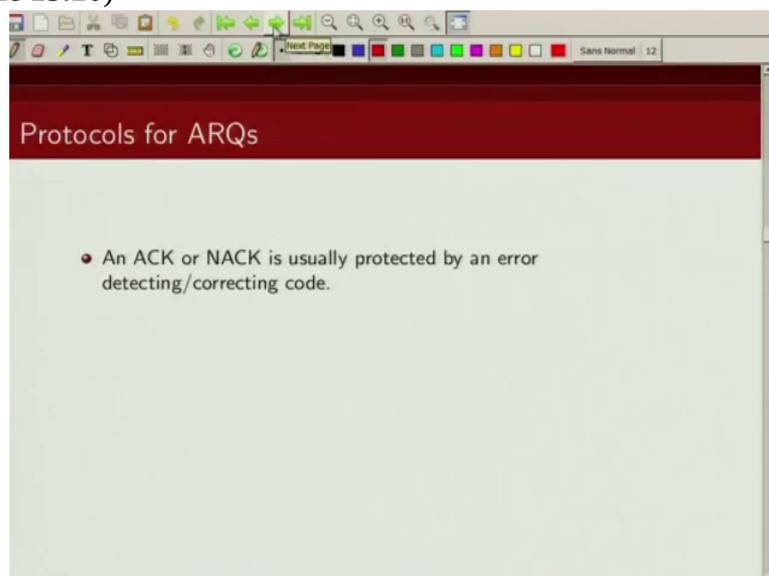
Now the way these A R Q protocols work is basically uh, if the packet is received error free or there are undetected errors, the receiver is going to send a positive acknowledgement to indicate that the packet has been successfully received otherwise

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it is going to send a negative acknowledgment and transmitter is then going to re-transmit some packets, Ok.

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Now when we are sending this acknowledgment and negative acknowledgment from the receiver to the transmitter through the feedback channel we assume

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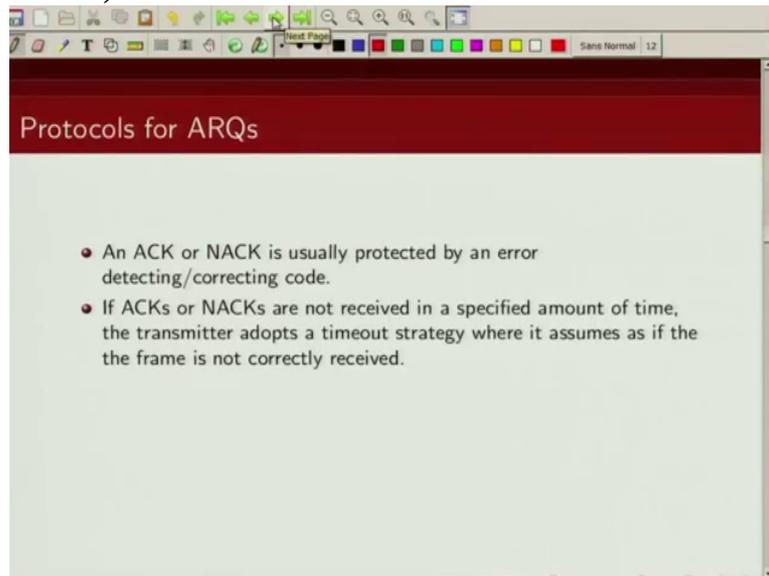
that these have been protected using some error correcting and error detecting code. So in our analysis we will assume that there is no error in sending these acknowledgment and negative acknowledgment signals from the receiver

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to the transmitter.

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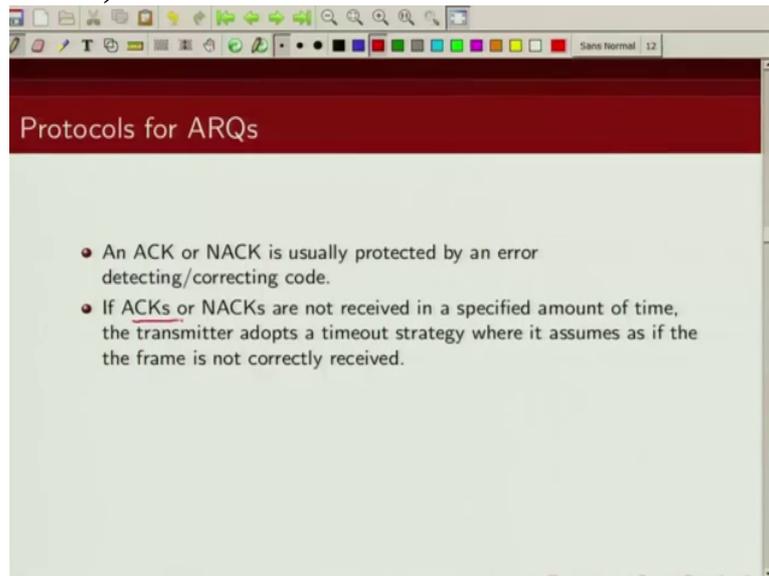
We will also assume that if the transmitter does not receive

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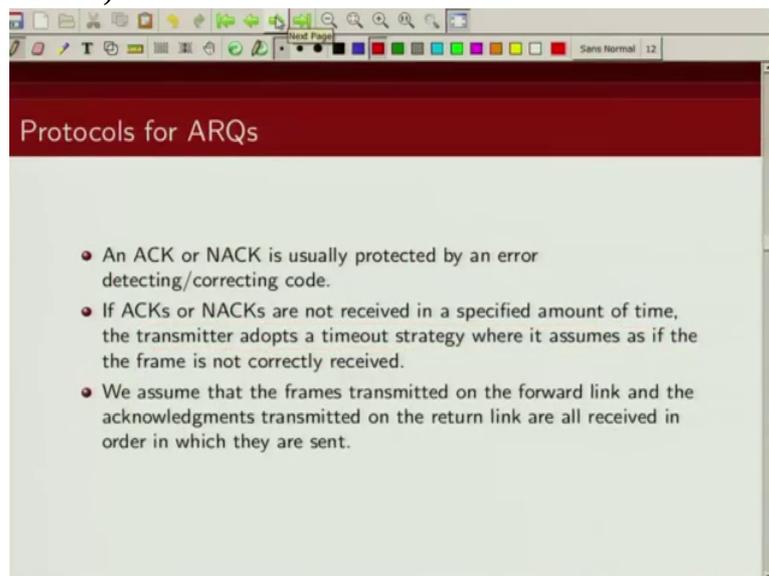
acknowledgment or negative acknowledgment in time, so there is a timeout, so we fix up a time, if we receive a acknowledgment or negative acknowledgment of a packet we have transmitted, after we have transmitted that packet, if we do not receive an ACK or an NACK between, within a particular time instance we will assume that the packet has not been successfully received, Ok.

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So if ACKs and NACKs are not received in specified amount of time, the transmitter adopts a timeout strategy where it will assume as if the frame is not correctly received. The another

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assumption that we are going to make is that the all the frames that we transmit over the forward link as well as the acknowledgment that are sent over the reverse link , they are all received in order. So they are all received in order. So the packets that we are sending first, the receiver is going to receive them first,

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the transmitter is going to receive the NACK and ACK of those packets first which have transmitted earlier. That's what I mean when I said all are received in order. So if I send packet number 1

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A screenshot of a presentation slide. The slide has a dark red header with the title "Protocols for ARQs" in white. Below the header, there are three bullet points in black text. The third bullet point has some text underlined in red. At the bottom right of the slide, there is a small red number "1". The slide is shown within a window that has a toolbar at the top with various icons and a status bar at the bottom that says "Sand Normal 12".

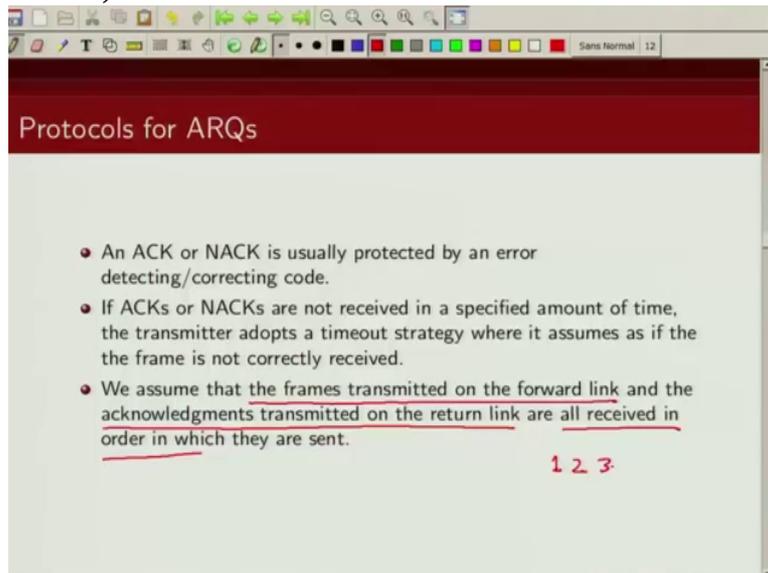
Protocols for ARQs

- An ACK or NACK is usually protected by an error detecting/correcting code.
- If ACKs or NACKs are not received in a specified amount of time, the transmitter adopts a timeout strategy where it assumes as if the the frame is not correctly received.
- We assume that the frames transmitted on the forward link and the acknowledgments transmitted on the return link are all received in order in which they are sent.

1

first, then 2 first and 3 first,

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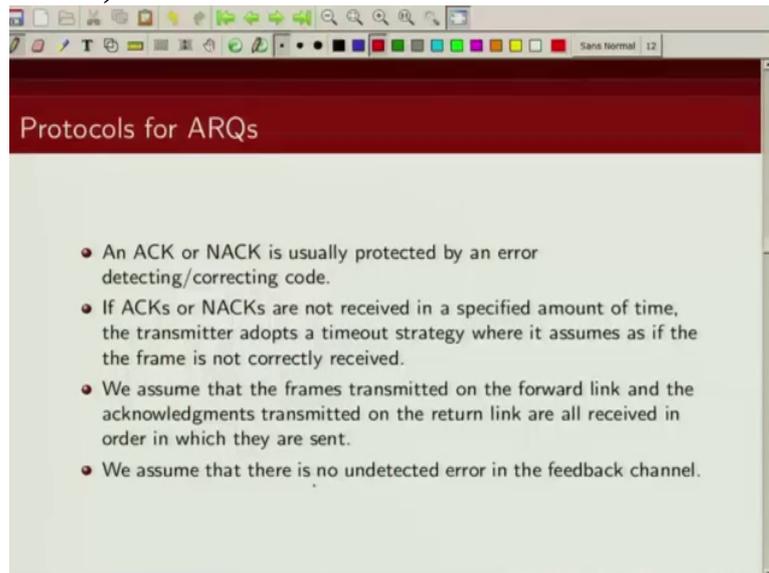
the receiver will also receive packet 1 first then 2, and then 3. And similarly the transmitter will first receive the NACK or

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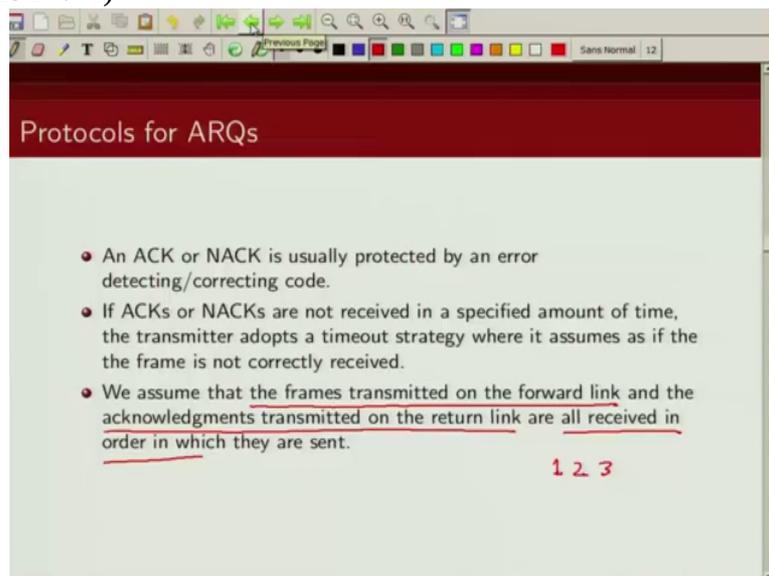
ACK of packet 1, then NACK and ACK of packet 2, and then NACK or ACK of packet 3. That's what I mean when I say all are received in order.

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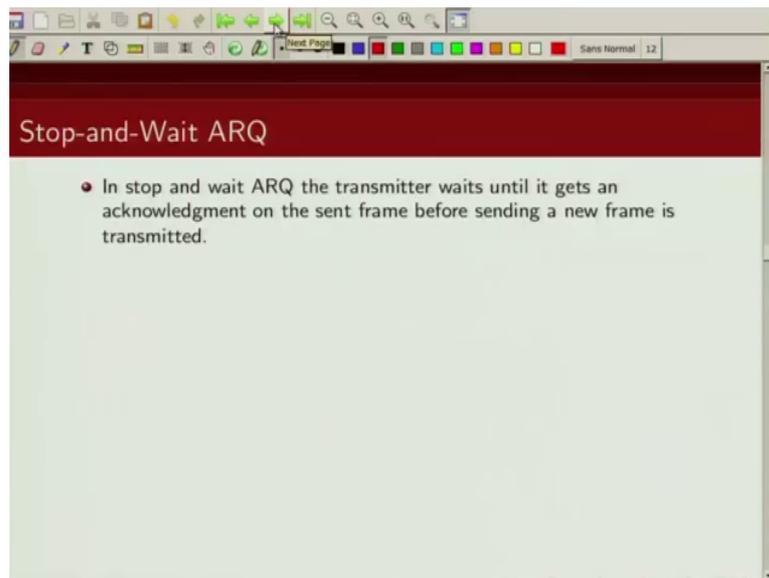
We also assume that there is no undetected error in the feedback channel. So

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the NACK and ACK that are sent are received correctly by the transmitter.

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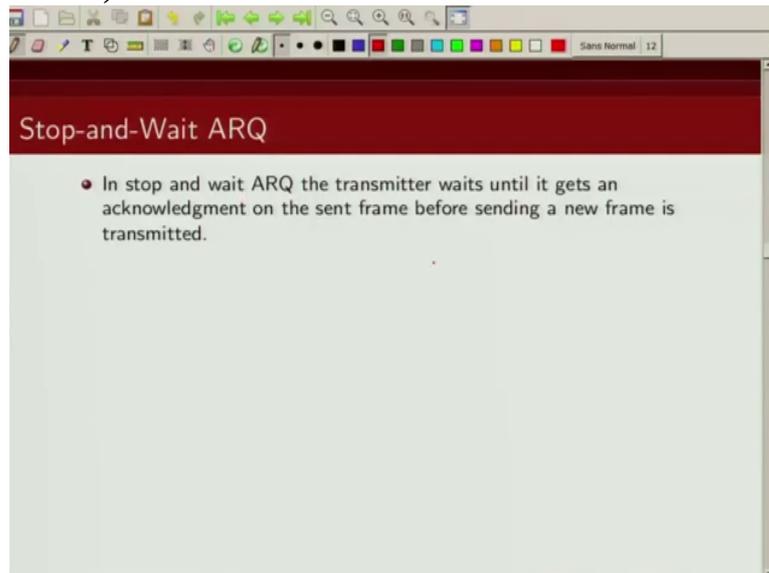
Now let us talk about each of these protocols in detail. The first protocol that we are going to talk about is known as Stop and Wait A R Q. So as the name suggests, we transmit the packet and wait

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until we get acknowledgement of that particular packet or the frame. So in Stop and Wait

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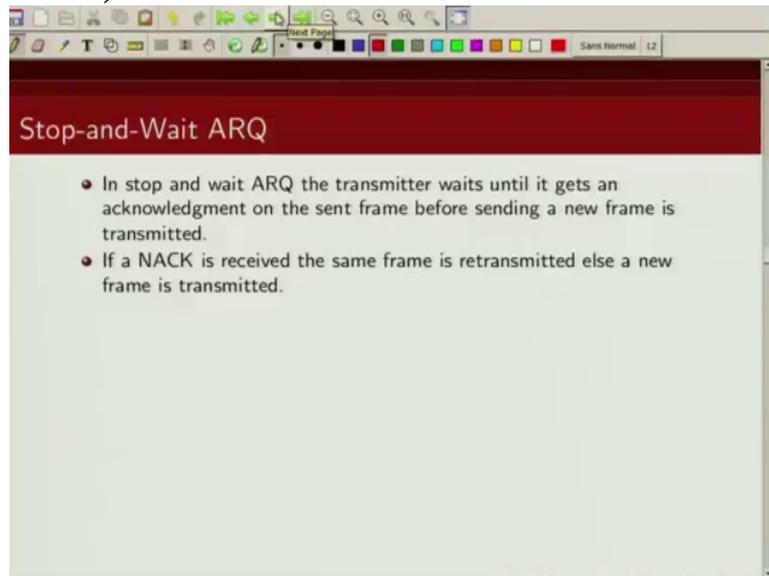
A R Q, the transmitter waits until it gets the acknowledgment of the sent frame before sending a new frame.

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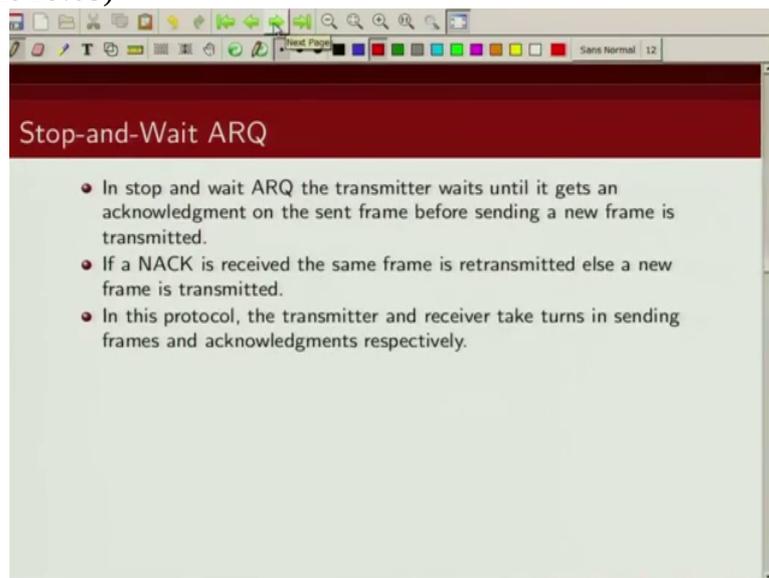
So in Stop and Wait A R Q Protocol we are going to send the packet, the receiver is going to receive it, it is going to detect, depending on whether the packet is received correctly or not, it is going to send back to transmitter an acknowledgment or negative acknowledgment and based on that the transmitter will now send either a new frame or a packet or the same packet if it is not received correctly.

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So if NACK is received, the same frame is re-transmitted else a new frame is

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transmitted. So in this protocol the transmitter and the receiver take turns in sending frames and acknowledgment.

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So transmitter is sending a packet, is sending a frame to the receiver. The receiver is sending an ACK or a NACK to the transmitter back

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Stop-and-Wait ARQ

- In stop and wait ARQ the transmitter waits until it gets an acknowledgment on the sent frame before sending a new frame is transmitted.
- If a NACK is received the same frame is retransmitted else a new frame is transmitted.
- In this protocol, the transmitter and receiver take turns in sending frames and acknowledgments respectively.
- To avoid potential confusion caused by lost frame, a sequence number is often included in the header of the frame and acknowledgments.

The diagram illustrates the Stop-and-Wait ARQ protocol. It shows a timeline for the Transmitter and Receiver. The Transmitter sends frame 1, which is received by the Receiver. The Receiver sends ACK 1, and the Transmitter receives it. The Transmitter then sends frame 2, which is received by the Receiver. The Receiver sends NACK 1, and the Transmitter receives it. The Transmitter then retransmits frame 2, which is received by the Receiver. The Receiver sends ACK 2, and the Transmitter receives it. The Transmitter then sends frame 3, which is received by the Receiver. The Receiver sends ACK 3, and the Transmitter receives it. The Transmitter then sends frame 4, which is received by the Receiver. The Receiver sends ACK 4, and the Transmitter receives it. The Transmitter then sends frame 5, which is received by the Receiver. The Receiver sends NACK 2, and the Transmitter receives it. The Transmitter then retransmits frame 5, which is received by the Receiver. The Receiver sends ACK 5, and the Transmitter receives it. The diagram also indicates 'Idle time' between the first ACK and the second frame, and 'Retransmission' for the second frame and the fifth frame. 'Error' is indicated at the Receiver side for the second and fifth frames.

and to avoid potential confusion caused by lost frame the sequence number is often included in the header of the frame that we are sending, similarly there is a sequence number embedded in the negative or positive

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acknowledgment so that the transmitter knows which packet has been received correctly or not. So in this

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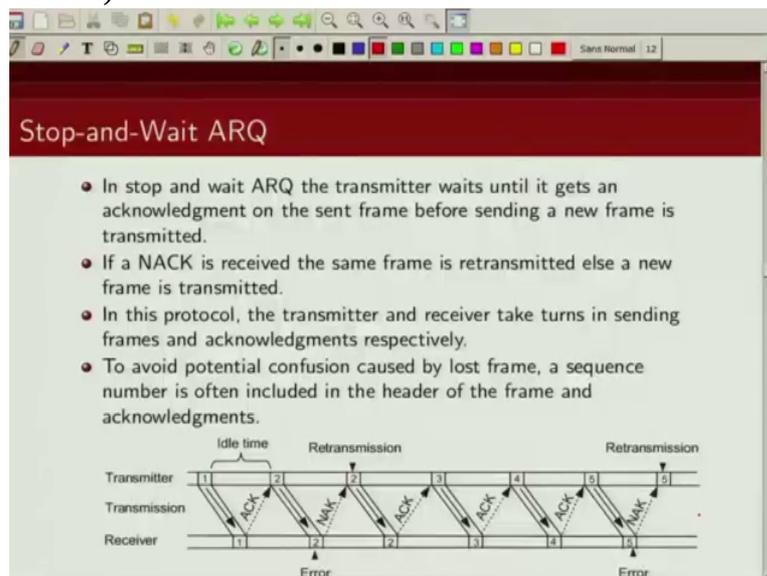


diagram I have shown how this Stop and Wait A R Q works. So this is my transmitter, this is my receiver. So let's say it

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Stop-and-Wait ARQ

- In stop and wait ARQ the transmitter waits until it gets an acknowledgment on the sent frame before sending a new frame is transmitted.
- If a NACK is received the same frame is retransmitted else a new frame is transmitted.
- In this protocol, the transmitter and receiver take turns in sending frames and acknowledgments respectively.
- To avoid potential confusion caused by lost frame, a sequence number is often included in the header of the frame and acknowledgments.

The diagram illustrates the Stop-and-Wait ARQ protocol. It shows three horizontal timelines: Transmitter, Transmission, and Receiver. The Transmitter timeline shows frames 1, 2, 3, 4, 5 being sent. The Receiver timeline shows frames 1, 2, 3, 4, 5 being received. The Transmission timeline shows ACK and NACK messages. Frame 1 is sent, received, and ACKed. Frame 2 is sent, received, and NACKed, leading to a retransmission. Frame 3 is sent, received, and ACKed. Frame 4 is sent, received, and ACKed. Frame 5 is sent, received, and NACKed, leading to a retransmission. Idle times and errors are indicated.

transmits frame number 1, Ok. Now there is some propagation and the transmission delay so basically after some time, the receiver receives this packet 1.

(Refer Slide Time 20:12)

Stop-and-Wait ARQ

- In stop and wait ARQ the transmitter waits until it gets an acknowledgment on the sent frame before sending a new frame is transmitted.
- If a NACK is received the same frame is retransmitted else a new frame is transmitted.
- In this protocol, the transmitter and receiver take turns in sending frames and acknowledgments respectively.
- To avoid potential confusion caused by lost frame, a sequence number is often included in the header of the frame and acknowledgments.

The diagram illustrates the Stop-and-Wait ARQ protocol. It shows three horizontal timelines: Transmitter, Transmission, and Receiver. The Transmitter timeline shows frames 1, 2, 3, 4, 5 being sent. The Receiver timeline shows frames 1, 2, 3, 4, 5 being received. The Transmission timeline shows ACK and NACK messages. Frame 1 is sent, received, and ACKed. Frame 2 is sent, received, and NACKed, leading to a retransmission. Frame 3 is sent, received, and ACKed. Frame 4 is sent, received, and ACKed. Frame 5 is sent, received, and NACKed, leading to a retransmission. Idle times and errors are indicated.

And using the C R C code it detects whether the packet is in error or not. So in this case, the packet is not in error so it sends a positive acknowledgement. So it sends an acknowledgement to the, back to transmitter. At this time

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Stop-and-Wait ARQ

- In stop and wait ARQ the transmitter waits until it gets an acknowledgment on the sent frame before sending a new frame is transmitted.
- If a NACK is received the same frame is retransmitted else a new frame is transmitted.
- In this protocol, the transmitter and receiver take turns in sending frames and acknowledgments respectively.
- To avoid potential confusion caused by lost frame, a sequence number is often included in the header of the frame and acknowledgments.

instance the transmitter has received an acknowledgement of frame number 1. So now it is going to send frame

(Refer Slide Time 20:37)

Stop-and-Wait ARQ

- In stop and wait ARQ the transmitter waits until it gets an acknowledgment on the sent frame before sending a new frame is transmitted.
- If a NACK is received the same frame is retransmitted else a new frame is transmitted.
- In this protocol, the transmitter and receiver take turns in sending frames and acknowledgments respectively.
- To avoid potential confusion caused by lost frame, a sequence number is often included in the header of the frame and acknowledgments.

number 2. Again after some delays the frame number 2 is received at this

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Stop-and-Wait ARQ

- In stop and wait ARQ the transmitter waits until it gets an acknowledgment on the sent frame before sending a new frame is transmitted.
- If a NACK is received the same frame is retransmitted else a new frame is transmitted.
- In this protocol, the transmitter and receiver take turns in sending frames and acknowledgments respectively.
- To avoid potential confusion caused by lost frame, a sequence number is often included in the header of the frame and acknowledgments.

The diagram illustrates the Stop-and-Wait ARQ protocol between a Transmitter and a Receiver. The Transmitter sends frames 11, 12, 13, 14, 15, and 16. The Receiver receives frames 11, 12, 13, 14, and 15. Frame 11 is successfully received and acknowledged with ACK. Frame 12 is received but causes an Error, leading to a NACK and retransmission. Frame 13 is received and acknowledged with ACK. Frame 14 is received and acknowledged with ACK. Frame 15 is received and acknowledged with ACK. Frame 16 is received but causes an Error, leading to a NACK and retransmission. The diagram also shows idle time for the transmitter while waiting for acknowledgments.

Since it is a negative acknowledgement, the transmitter will decide to send the same packet again. So you can see there is an idle time involved here

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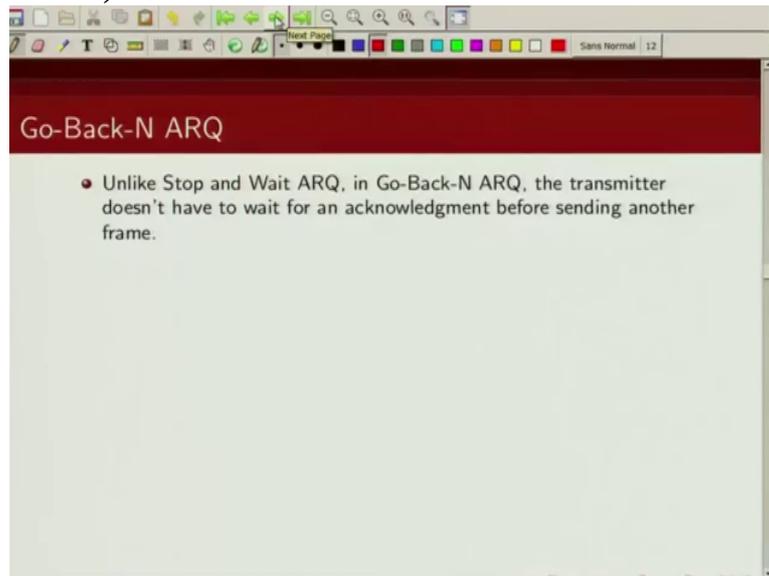
Stop-and-Wait ARQ

- In stop and wait ARQ the transmitter waits until it gets an acknowledgment on the sent frame before sending a new frame is transmitted.
- If a NACK is received the same frame is retransmitted else a new frame is transmitted.
- In this protocol, the transmitter and receiver take turns in sending frames and acknowledgments respectively.
- To avoid potential confusion caused by lost frame, a sequence number is often included in the header of the frame and acknowledgments.

The diagram illustrates the Stop-and-Wait ARQ protocol between a Transmitter and a Receiver. The Transmitter sends frames 11, 12, 13, 14, 15, and 16. The Receiver receives frames 11, 12, 13, 14, and 15. Frame 11 is successfully received and acknowledged with ACK. Frame 12 is received but causes an Error, leading to a NACK and retransmission. Frame 13 is received and acknowledged with ACK. Frame 14 is received and acknowledged with ACK. Frame 15 is received and acknowledged with ACK. Frame 16 is received but causes an Error, leading to a NACK and retransmission. The diagram also shows idle time for the transmitter while waiting for acknowledgments.

for the transmitter. it sends the packet and waits for the round trip delay

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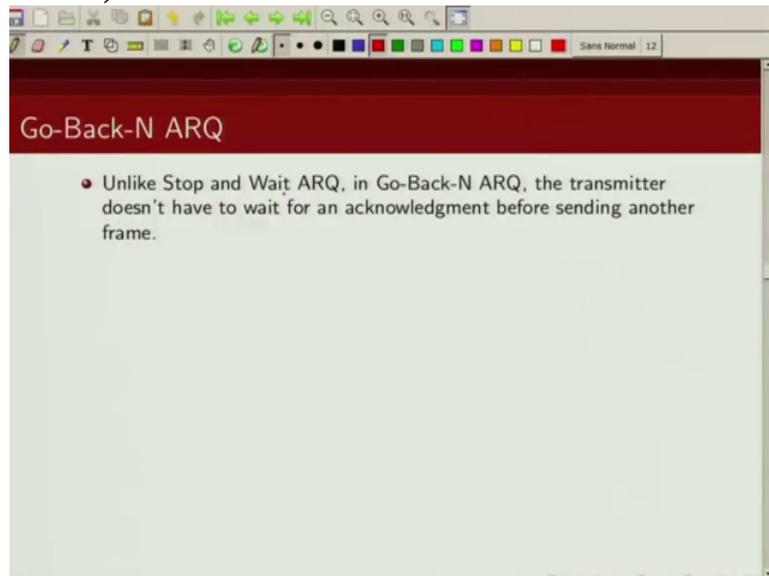
Now as I said the problem with Stop and Wait A R Q is you send a packet you wait for to receive its acknowledgement

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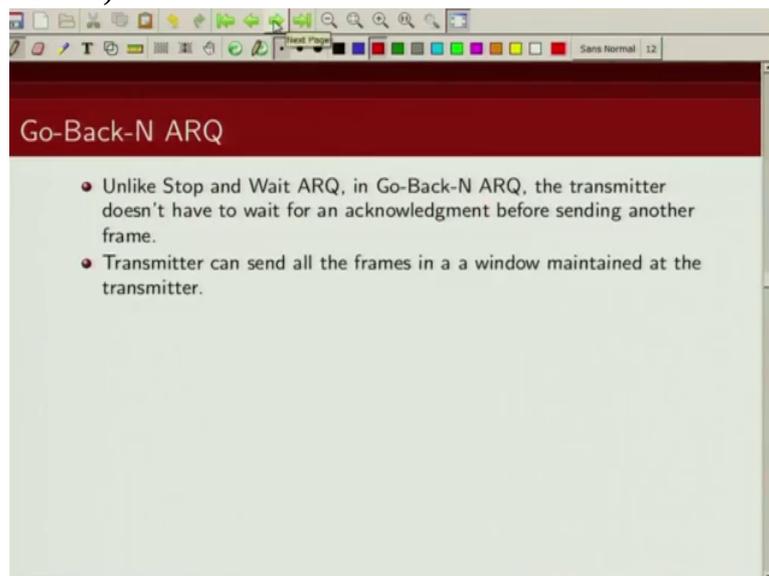
before you send the next packet. So you have this large idle time, possibly large idle time. So this problem has been solved in Go Back N A R Q protocol. So unlike

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in Stop and Wait A R Q, in Go Back N A R Q, the transmitter does not wait for an acknowledgement before sending another frame, Ok. So

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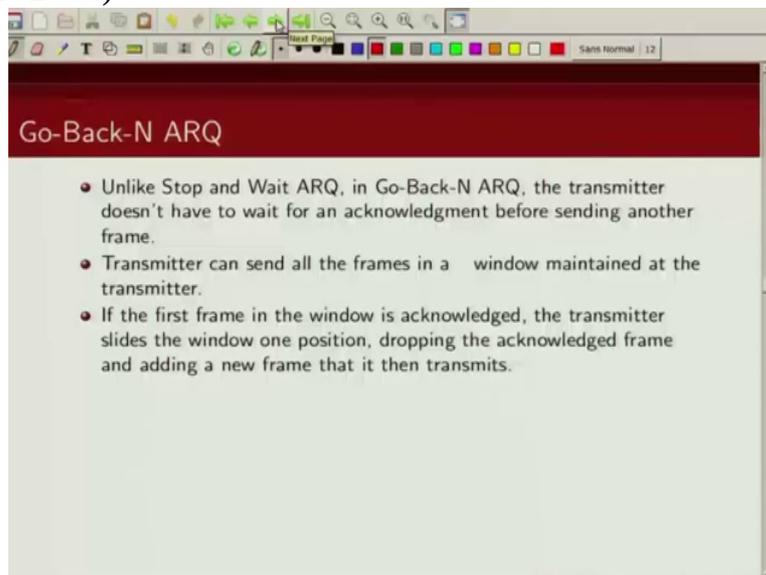
it's like a sliding window kind of protocol, so you can send packet number

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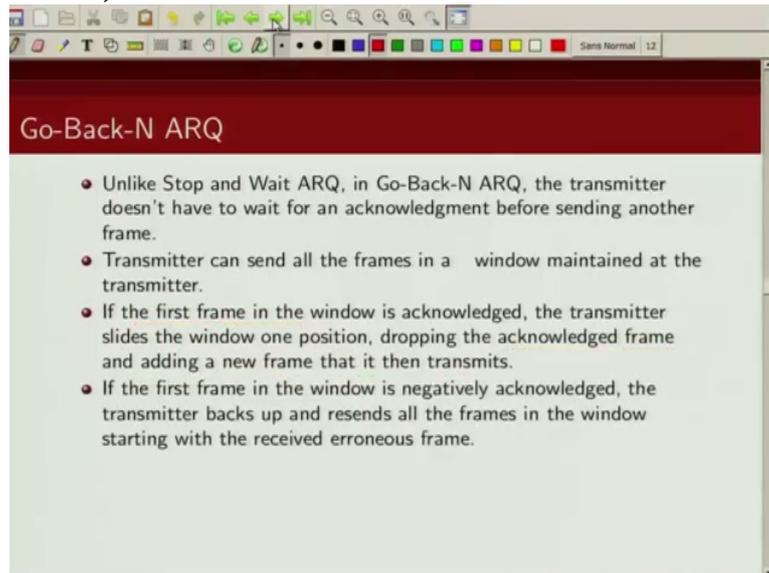
1, 2 and let's see, Ok and in the mean time you send packet 1, 2, 3, 4, 5 up to n. Now you send packet number n plus 1 only when you would have received the acknowledgement for packet number 1. You are going to send packet number n plus 2 only when you get acknowledgement for packet number 2. So you send a block of frames or packets, Ok in this, this particular scheme we do not wait for each individual packet acknowledgement or negative acknowledgement to reach the receiver. We are sending a block of

(Refer Slide Time 24:54)



packets, you can say. Now if the first packet or first frame in this window is acknowledged to be received correctly, what the transmitter does is, transmitter slides this window one position dropping the acknowledged packet or the frame and then adding a new frame that is then transmitted.

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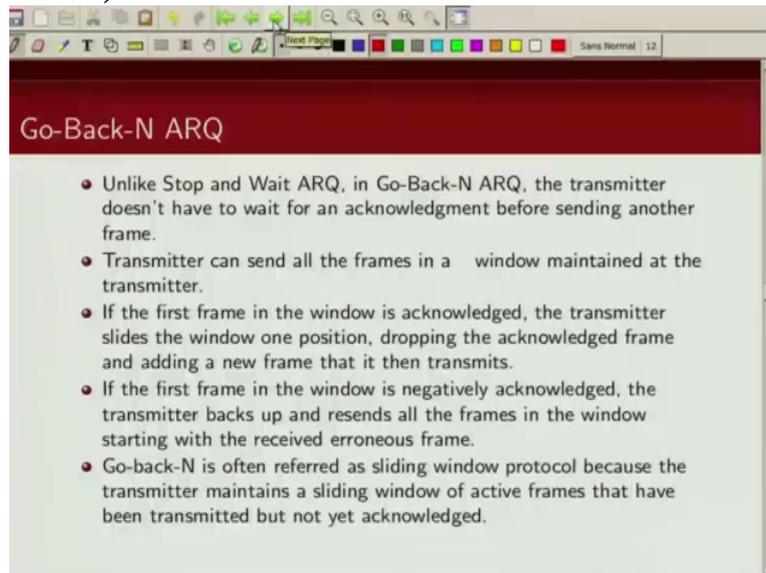
Now what happens if the first frame in that window is negatively acknowledged, that is it is not received correctly? Then the transmitter backs up and resends all the frames in the window starting from

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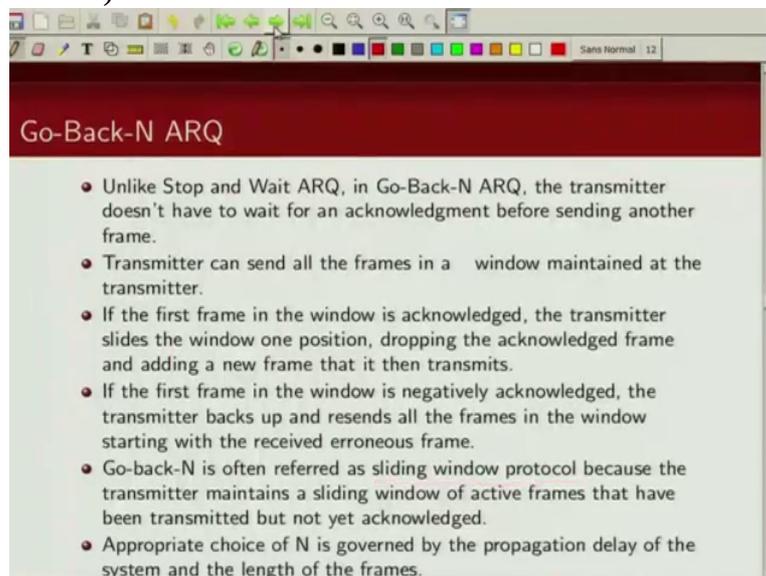
the erroneous received frame. So for example if I am sending packet 1 to n and my fifth packet is in error, I get a NACK corresponding to fifth packet, then fifth onward I am going to start sending those packets again.

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So this Go Back N is often referred to as Sliding Window Protocol because transmitter maintains a sliding window active frames that has been transmitted and for whose acknowledgement it is basically waiting to get the acknowledgement for those frames or packets.

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Now as you can see here, this window size or sliding window size n is crucial. Because we don't want

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a very special case of n equal to 1 is Stop and Wait. And the other extreme is if n is too large then of course that is also not good. So we need to choose appropriately what n is

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A screenshot of a presentation slide. The slide has a red header with the title "Go-Back-N ARQ". Below the header is a list of six bullet points. The slide is displayed in a window with a standard operating system taskbar at the top and a toolbar on the left side. The text is in a sans-serif font.

Go-Back-N ARQ

- Unlike Stop and Wait ARQ, in Go-Back-N ARQ, the transmitter doesn't have to wait for an acknowledgment before sending another frame.
- Transmitter can send all the frames in a window maintained at the transmitter.
- If the first frame in the window is acknowledged, the transmitter slides the window one position, dropping the acknowledged frame and adding a new frame that it then transmits.
- If the first frame in the window is negatively acknowledged, the transmitter backs up and resends all the frames in the window starting with the received erroneous frame.
- Go-back-N is often referred as sliding window protocol because the transmitter maintains a sliding window of active frames that have been transmitted but not yet acknowledged.
- Appropriate choice of N is governed by the propagation delay of the system and the length of the frames.

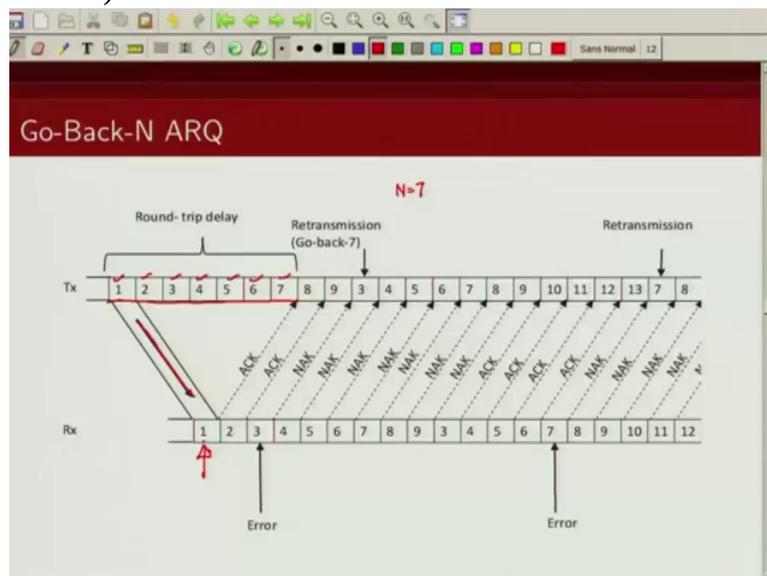
depending on what is the propagation delay of the system or what's the length of the frame that we are sending.

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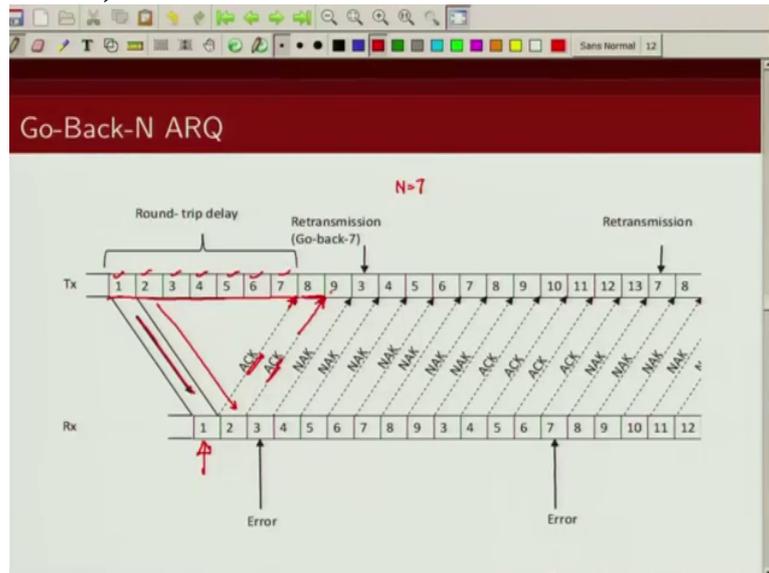
violation or not. If the, if C R C code detects any error, it will send a negative acknowledgement, otherwise it will send a positive acknowledgement. So in this case,

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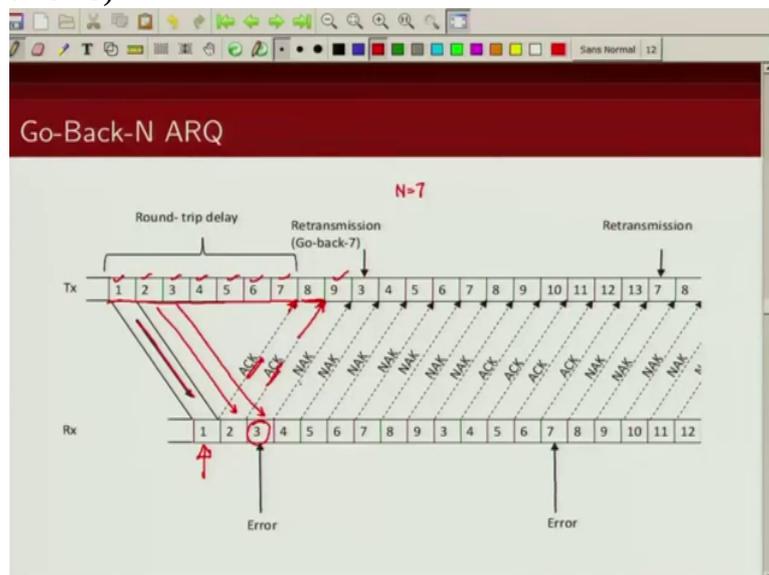
this packet 1 was received correctly. So positive acknowledgement was sent. Now there is a delay in receiving this positive acknowledgement so at this particular time instance the transmitter receives a positive acknowledgement about frame number 1. Remember I said here we are sending a block of n bits. Now we are going to send n plus 1 frame only when we get the acknowledgement about frame number 1. So at this time instance we are getting an acknowledgement of the frame number 1, so this is the acknowledgement, so we are going to send packet number 8. Now in the mean time the second packet was sent and this was received correctly. So at this instance we get,

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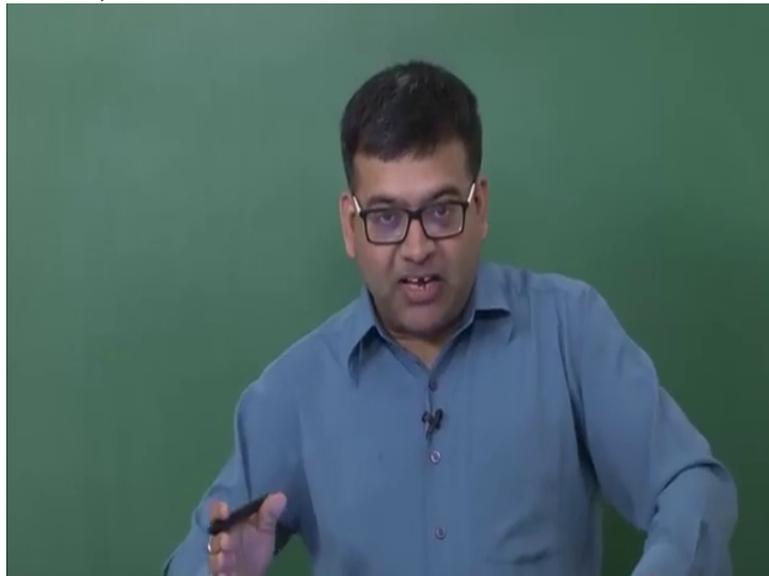
the transmitter gets an acknowledgement that the second packet has been received correctly. So it transmits a new packet which is a packet for frame number 9. Next this 3 has been received. Now this 3 has been received in error.

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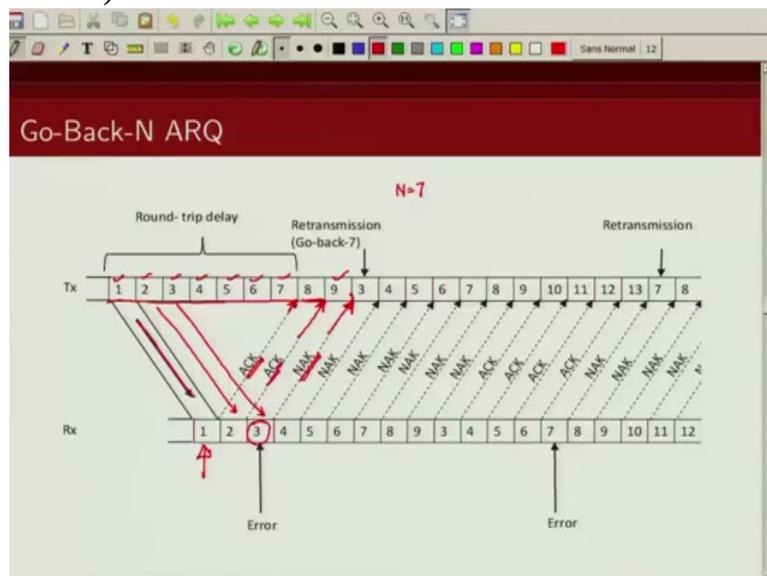
So this receiver is going to send a negative acknowledgement. So receiver sends a negative acknowledgement which is received at the transmitter at this time instance. Now since this is a negative acknowledgement, as I said if you get intimation about an incorrect frame received we are going to,

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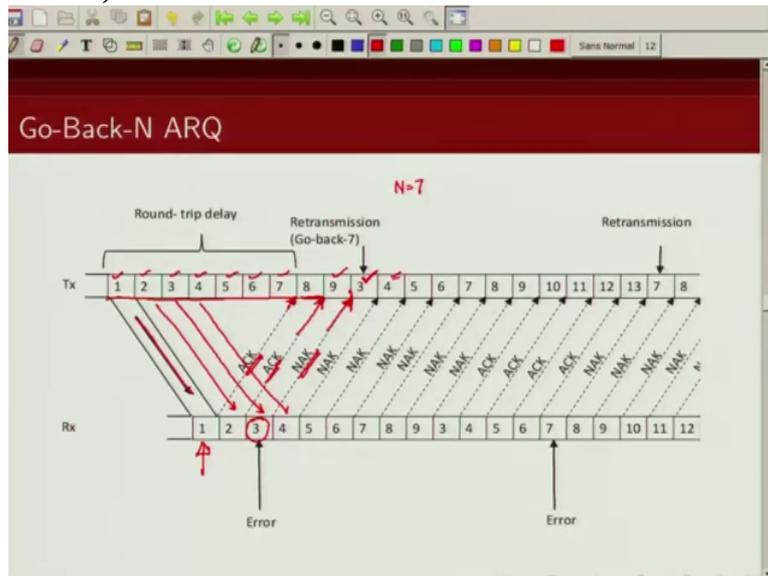
the transmitter is going to back off and start sending again from the packet which has been erroneously received. In this case

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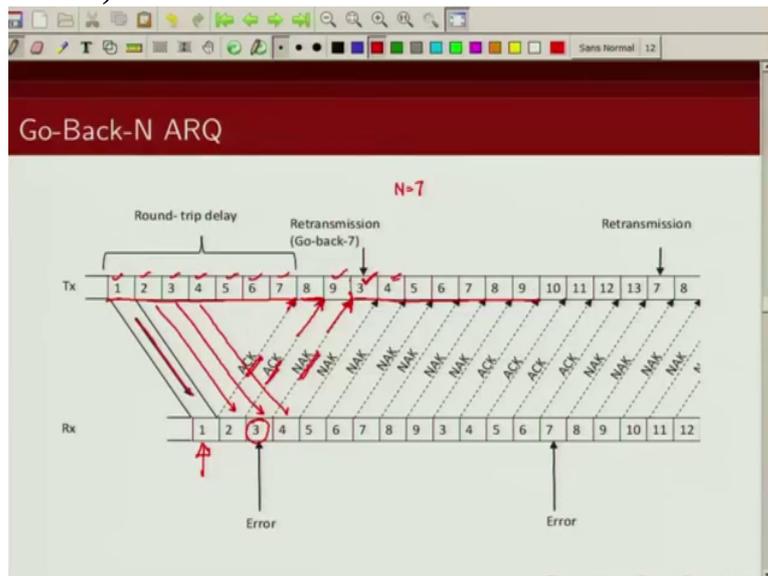
the third packet was not received correctly. So we will start sending again from packet number 3. So at this time instance I am going to send packet, frame number 3, Ok now similarly you get a 4 here, you have received correctly so you send, now the next packet after 3 is 4, so like that basically we are going to send

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next 7; 3,4, 5,6, 7,8,9 we are going to send. So in

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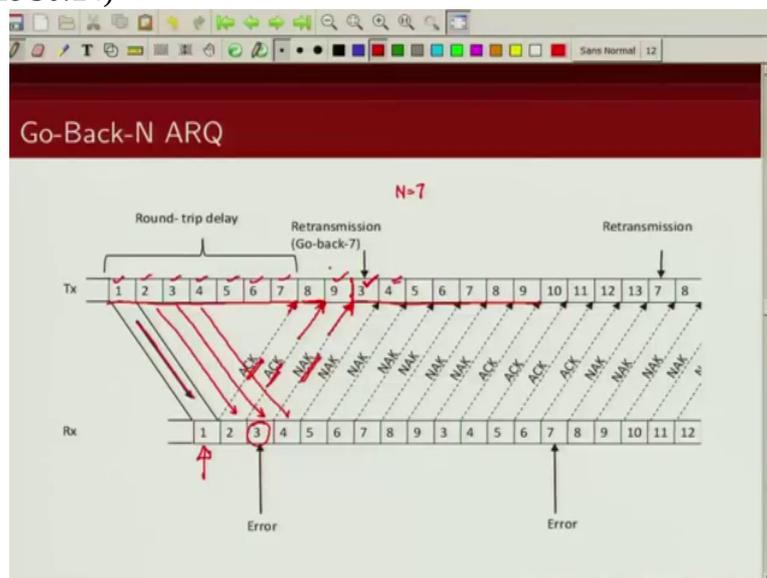
Go Back N what we do is whenever we receive a packet

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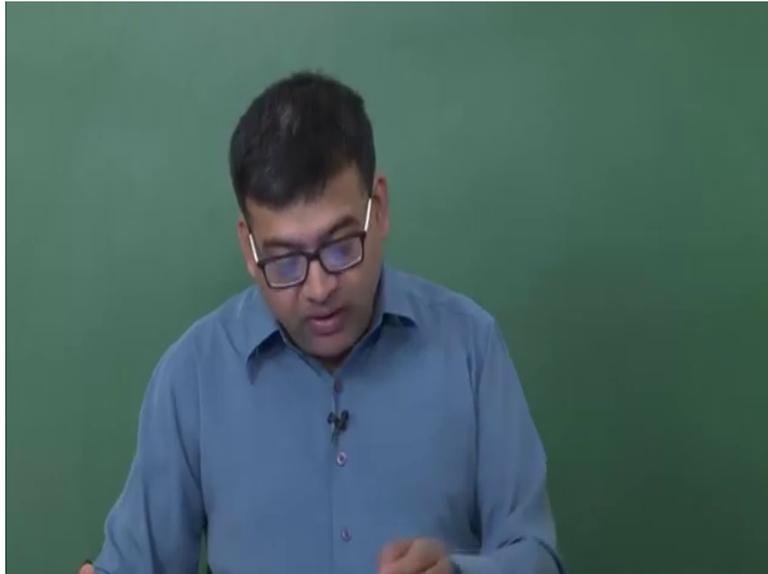
in error, we start retransmitting from this packet. So in this case, we receive packet number 3 in error. We start retransmitting from packet number 3, 4, 5. So it may so happen that earlier we sent packet

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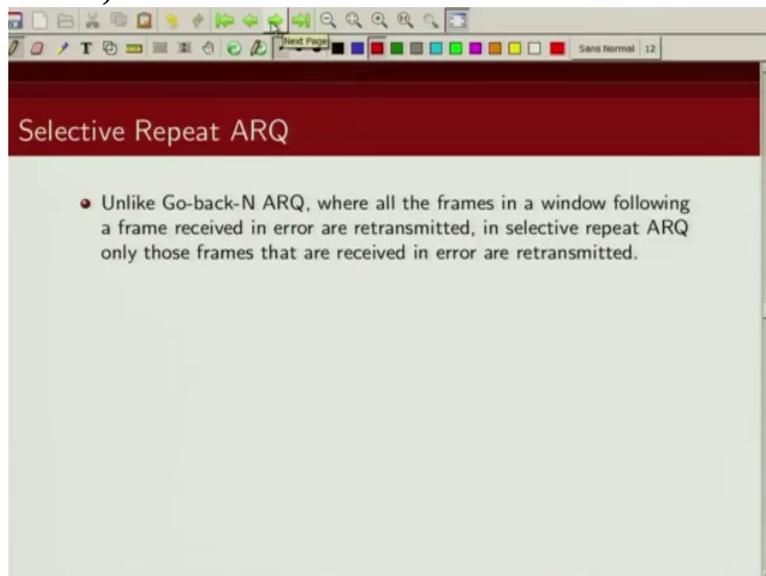
number 1 to 7; 4, 5, 6 might have been received correctly but we ignore them.

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We again retransmit the packet. So in terms of efficiency this is still not very good because we are wasting, we are retransmitting some packets which we might have received correctly. So the other extreme is what is known as Selective Repeat A R Q.

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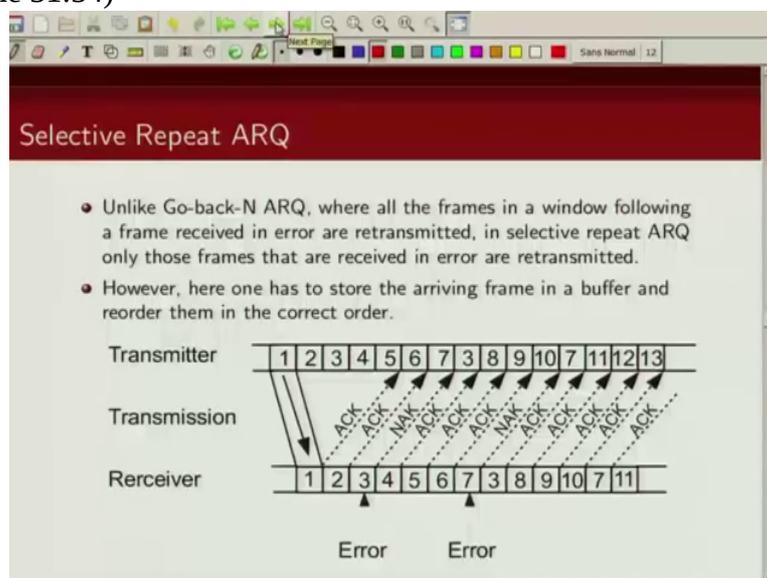
So in this case, when a frame is received in error, we are only going to send those frames which have been received in error. So unlike

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Go Back N, in Go Back N what were we doing? If a packet number i was received in error, we start retransmitting again from packet i ; i , packet number $i + 1$, $i + 2$, $i + 3$. But in Selective Repeat if a packet i is not received correctly we are only going to send, retransmit packet i . We are not going to retransmit packet $i + 1$ or $i + 2$ if they were received correctly, Ok. So we are selectively only retransmitting those packets which are not received correctly by the receiver.

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What is the drawback of the system? Now since we are only selectively asking for retransmission of only those packets which are in

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Selective Repeat ARQ

- Unlike Go-back-N ARQ, where all the frames in a window following a frame received in error are retransmitted, in selective repeat ARQ only those frames that are received in error are retransmitted.
- However, here one has to store the arriving frame in a buffer and reorder them in the correct order.

Transmitter: 1 2 3 4 5 6 7 3 8 9 10 7 11 12 13

Transmission: ACK ACK NAK ACK ACK ACK NAK ACK ACK ACK ACK ACK

Receiver: 1 2 3 4 5 6 7 3 8 9 10 7 11

Error Error

So you, at this time instance you are getting a negative

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Selective Repeat ARQ

- Unlike Go-back-N ARQ, where all the frames in a window following a frame received in error are retransmitted, in selective repeat ARQ only those frames that are received in error are retransmitted.
- However, here one has to store the arriving frame in a buffer and reorder them in the correct order.

Transmitter: 1 2 3 4 5 6 7 3 8 9 10 7 11 12 13

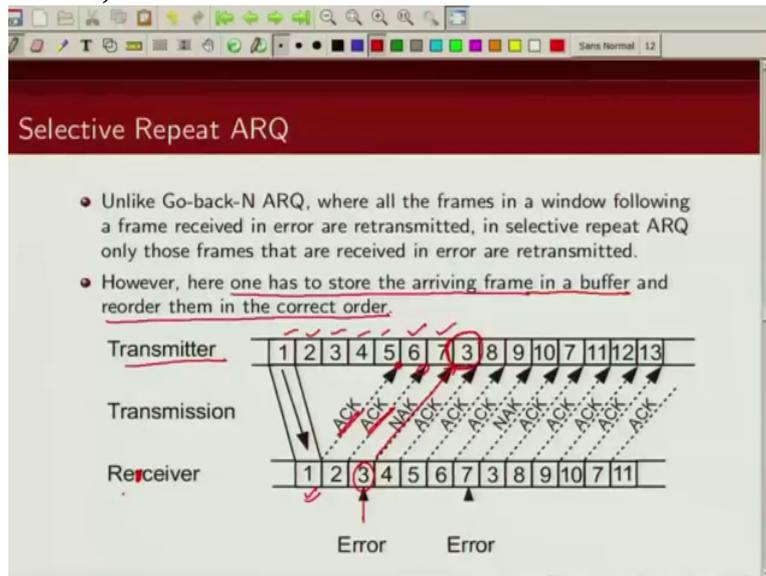
Transmission: ACK ACK NAK ACK ACK ACK NAK ACK ACK ACK ACK ACK

Receiver: 1 2 3 4 5 6 7 3 8 9 10 7 11

Error Error

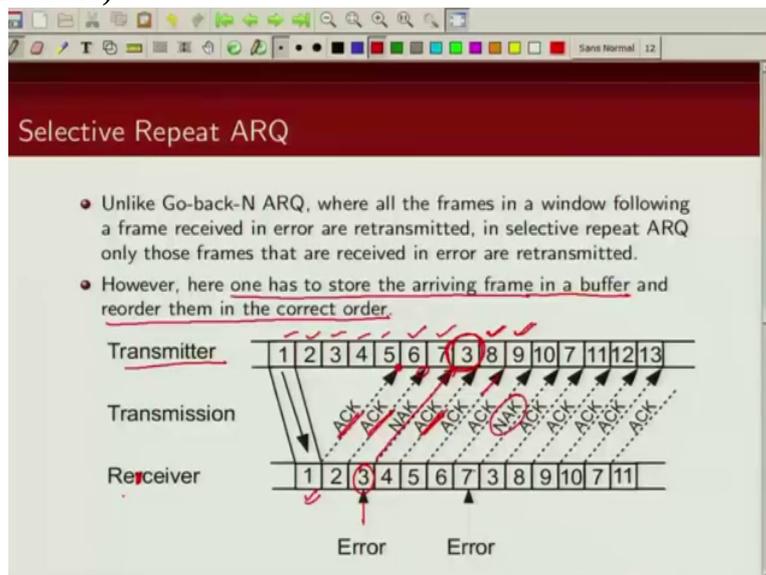
acknowledgement of packet number 3. So what this transmitter will do is it will retransmit packet

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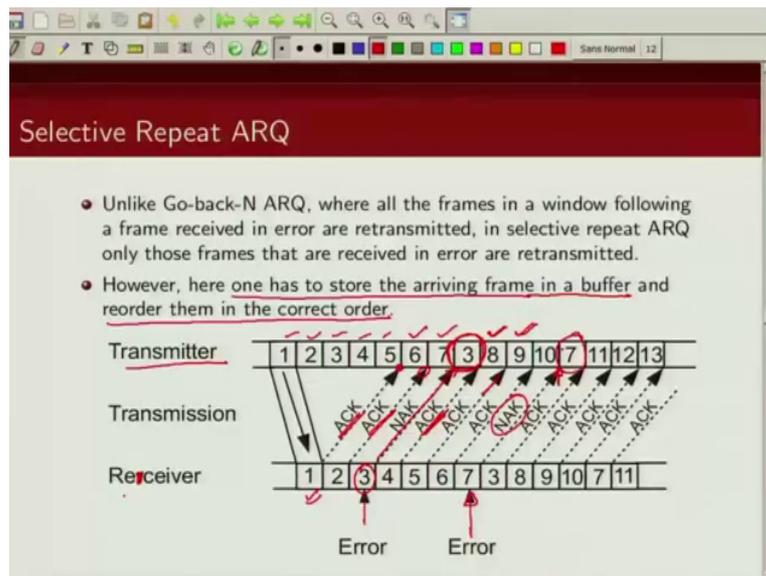
number 3. Now note the difference from Go Back N. In Go Back N what we would have done was we would have started retransmitting packet 4, 5, 6. Here since packet 4 is received correctly we don't retransmit it. We send the next packet. We have already sent up to packet number 7, so now we are going to send packet 8. So this is the difference from Go Back N. In Go Back N we start retransmitting again from the incorrectly received packet. Now here you get acknowledgement for packet number 5, so you send the next packet which is packet number 10. Now here you see acknowledgement for sixth, look at this example again.

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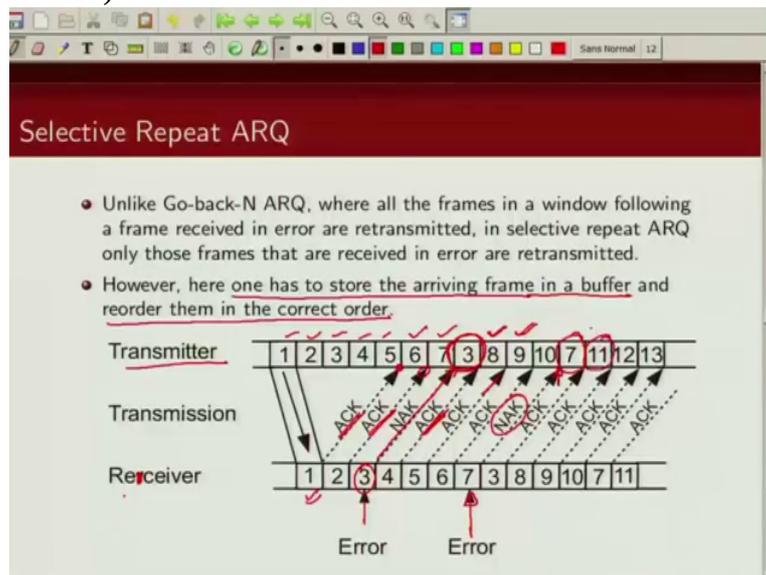
So the seventh bit, seventh frame was not received correctly so you send a negative acknowledgement for this instance, the transmitter receives a negative acknowledgement. So what you are going to do is you are going to insert this

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frame number 7 which was incorrectly received here and continue. Subsequently you received an acknowledgement about the packet that you sent so

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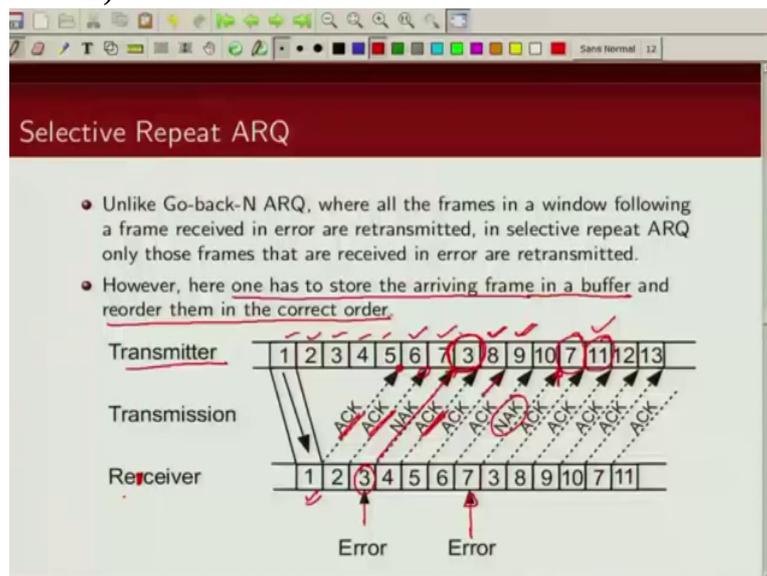
you send the new packet, packet number 11. So you can see in Selective Repeat

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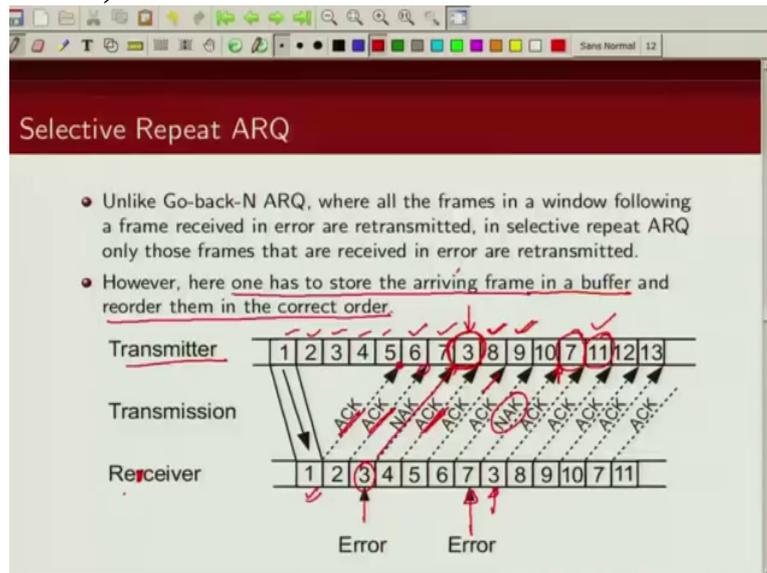
ARQ, we are selectively retransmitting only those packets which are not received correctly. So you can see here I am

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receiving packet number 3 here correctly because this was not, I mean, actually received packet number 3 correctly at the receiver because

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this was not incorrect. In the mean time, I have already received correctly packet number 1, 2, 4, 5, 6, 7. So once I get packet number 3 or frame number 3, I need to put it back in the order after

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packet number 2 and before packet number 4. So that's what I meant; at the receiver you need to keep buffer to save

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Selective Repeat ARQ

- Unlike Go-back-N ARQ, where all the frames in a window following a frame received in error are retransmitted, in selective repeat ARQ only those frames that are received in error are retransmitted.
- However, here one has to store the arriving frame in a buffer and reorder them in the correct order.

The diagram illustrates the Selective Repeat ARQ protocol. It shows three rows: Transmitter, Transmission, and Receiver. The Transmitter row contains frames 1 through 13. The Transmission row shows arrows representing the flow of frames, with ACKs and NAKs. The Receiver row shows frames 1 through 11. Red circles highlight the retransmitted frames 3 and 7. Red arrows at the bottom point to the error locations for frames 3 and 7.

or store

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Selective Repeat ARQ

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- However, here one has to store the arriving frame in a buffer and reorder them in the correct order.

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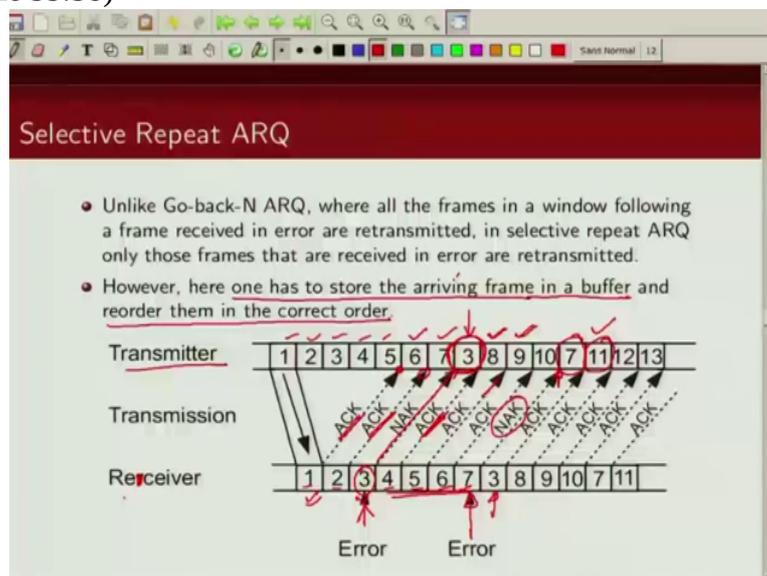
all the packets that you have received before the, this packet number 3 you have received, so 4, 5,6 so you need to store them in the buffer and once you get packet number 3, you put it back in the order after packet number 2. So these are the three protocols you can see, Stop and Wait very inefficient but very simple

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protocol, Selective Repeat from efficiency point of view very good but from the complexity point of view the most complex and Go Back N is in-between, I mean efficiency more than [Selective Repeat/Stop and Wait] Stop and Wait but then

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it is easier than Selective Repeat Request scheme.

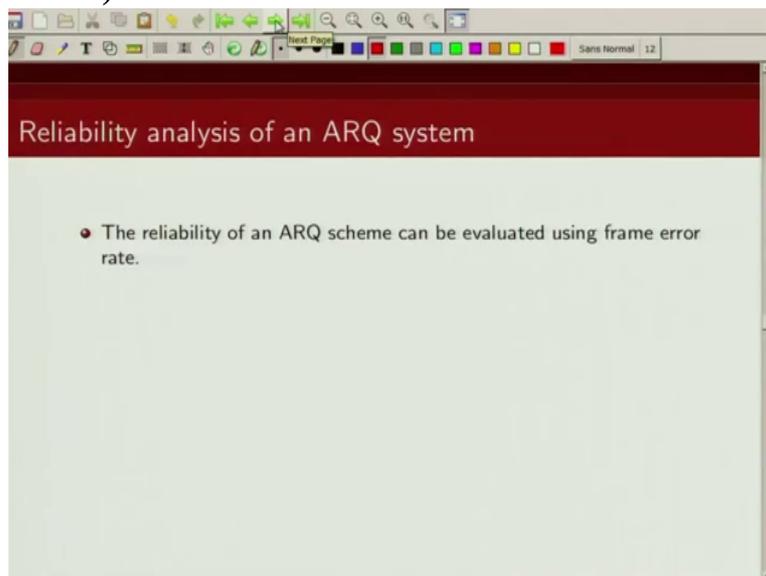
So now we are

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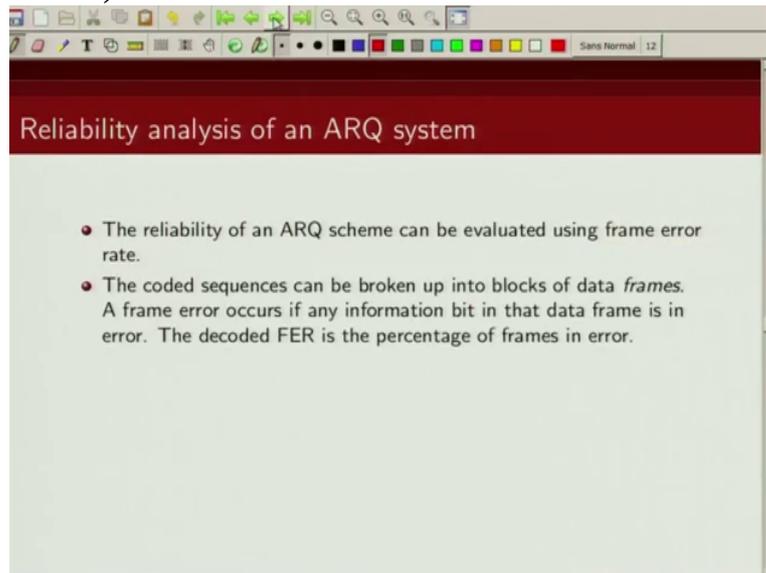
going to talk about performance evaluation of these A R Q protocols. So the first thing we are going to talk about is reliability of

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these A R Q schemes. So how can we evaluate the reliability of these A R Q schemes? We can evaluate their reliability using frame error rate. So when you send a packet and

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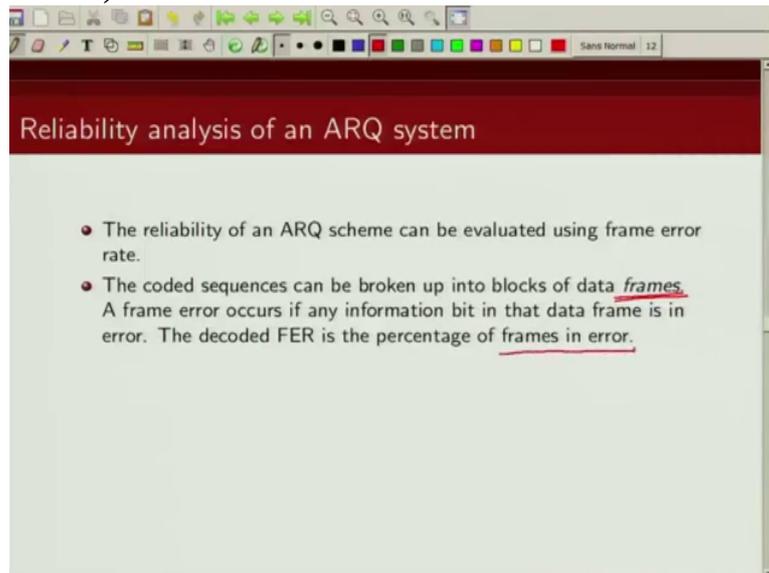
the packet is received in error, we say the frame or the packet is in error, right. So what we are doing is we are sending this coded sequence into small, small blocks of data which are known as frames or packets and the frame error happens if any of the information bit in that frame is

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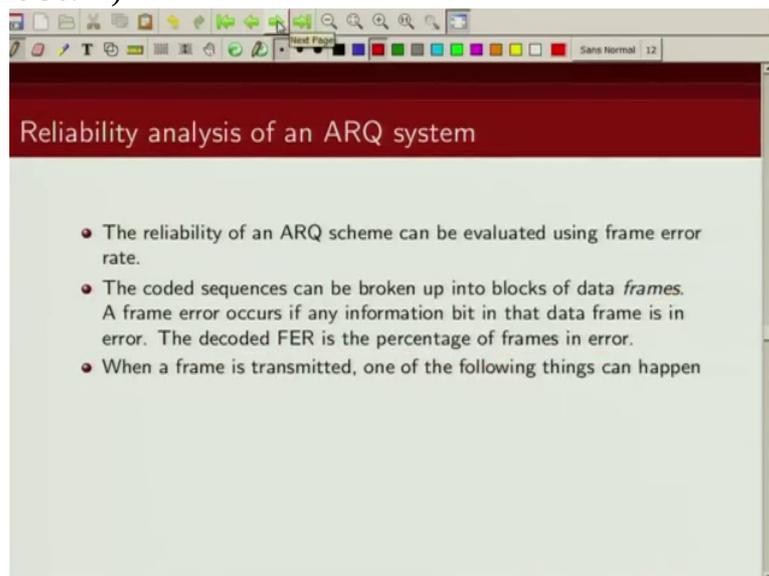
in error. And the percentage of frames which are in error is known as

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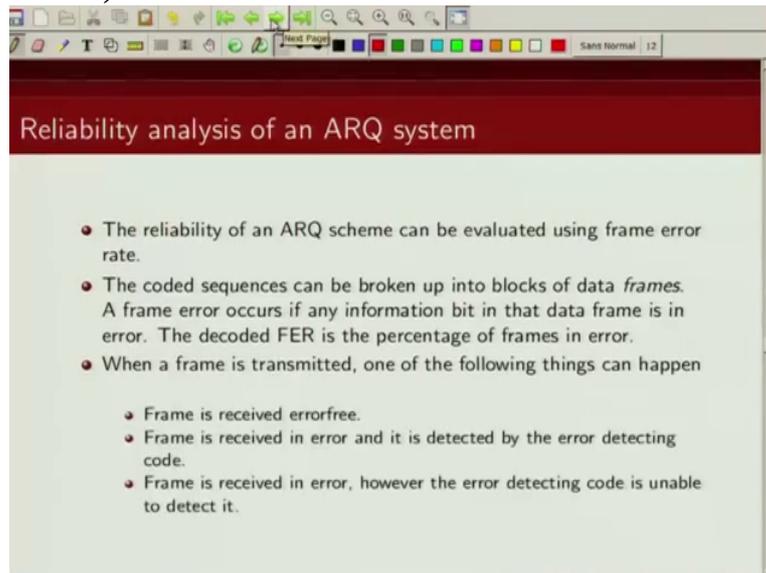
frame error rate. So that's the decoding frame error rate, it is percentage of frames in error.

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Now what happens when we are sending a frame? What happens when a transmitter sends a frame?

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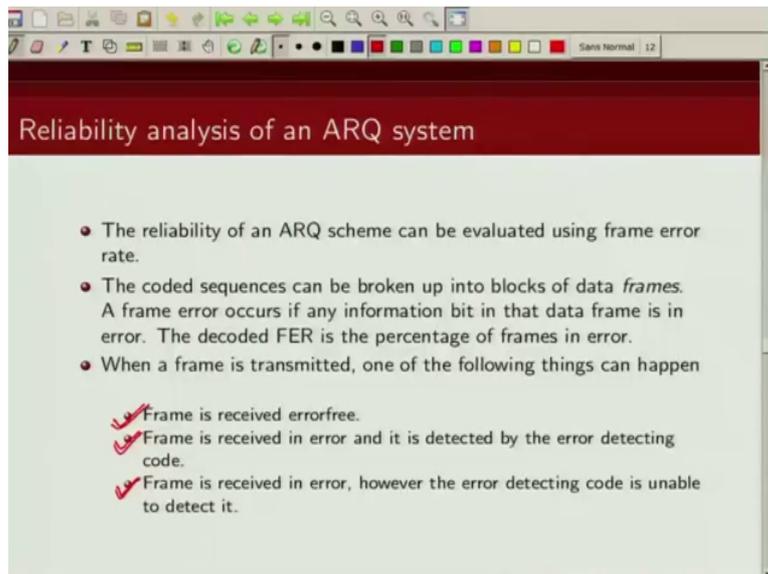
So there are 3 possibilities. The first possibility is the frame is received correctly. So there has been no transmission errors. Whatever is sent exactly the same thing is received. So the frame is received error free. The second possibility is frame is received in error and the error detecting code that we are using is able to detect that there is an error. So that's option number 2. And the third option is frame is received in error however the error detecting code

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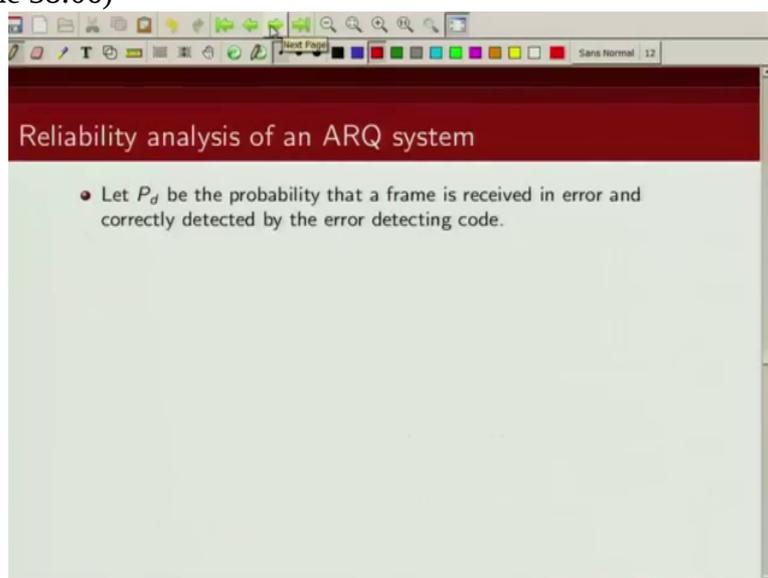
that we use is unable to detect that error. So when does that situation happen? We call; we say an undetected error has happened. An undetected error happens if the transmitted codeword gets transformed into some other codeword. So the error pattern is such that it transforms one codeword to the, some other codeword and then we would not be able to detect the error.

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So that's the third possibility. So

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let P_d be the probability that the frame is received in error and it is correctly detected by the error detecting code

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Reliability analysis of an ARQ system

- Let P_d be the probability that a frame is received in error and correctly detected by the error detecting code.
- Let P_u be the probability that a frame is received in error and the error detecting code is unable to detect it.

and let P_u be the probability that the frame is received in error but the error detecting code is unable to detect it.

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Reliability analysis of an ARQ system

- Let P_d be the probability that a frame is received in error and correctly detected by the error detecting code.
- Let P_u be the probability that a frame is received in error and the error detecting code is unable to detect it.
- Assuming there is no limit on retransmissions, the frame error rate is given by

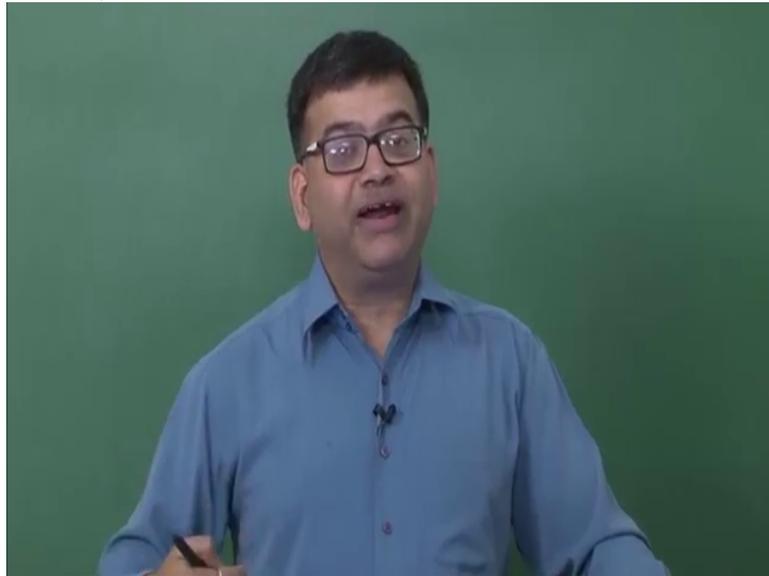
$$\begin{aligned} FER &= P_u + P_d P_u + P_d^2 P_u + P_d^3 P_u \dots + \\ &= P_u \sum_{i=0}^{\infty} P_d^i = \frac{P_u}{1 - P_d} \end{aligned}$$

- This analysis is true for any ARQ protocol.

Now we are trying to find out an expression for frame error rate for this ARQ protocol. So let's assume that there is no limit on retransmission. So whenever we get a negative acknowledgement we will ask for retransmission. So when does the frame error occur? A frame error occurs if we accept a packet which is in error but the error detecting code is not able to detect those error and that probability is given by P_u , Ok. So it can happen that the first time that we send the packet the error detecting code is not able to detect the error and it accepts the packet. So this is given by this probability. Or it may happen that first time instance it was able to detect the packet but when we retransmitted it for the first time, the

error detecting code was not able to detect the error. That probability is given by P_d times P_u or it may so happen that two times, you were able correctly detect the error but the third time, you were not able to detect the error. So that probability is given by this. Similarly we can write $P_d^3 P_u$ plus $P_d^4 P_u$ like that. So this can be written in this form which is, can be written as geometric series, so which can be written as P_u divided by $1 - P_d$. Now this is true for any ARQ protocol. Now so the frame error rate depends on P_u and P_d . So P_d is the probability that it is able to

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correctly detect that the frame is in error. So that depends on the error detecting capability of the error detecting code that we are using. Similarly this

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Reliability analysis of an ARQ system

- Let P_d be the probability that a frame is received in error and correctly detected by the error detecting code.
- Let P_u be the probability that a frame is received in error and the error detecting code is unable to detect it.
- Assuming there is no limit on retransmissions, the frame error rate is given by

$$FER = P_u + P_d P_u + P_d^2 P_u + P_d^3 P_u \dots +$$

$$= P_u \sum_{i=0}^{\infty} P_d^i = \frac{P_u}{1 - P_d}$$

- This analysis is true for any ARQ protocol.

undetected error probability is also related to the weight distribution of the error detecting code that we are using. In fact we have, when we talked about linear block code, we talked about what's the probability of undetected error because we are dealing with linear block code. So if you are sending all zero codewords and what we receive

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a nonzero codeword then there is an error. So we can use the weight distribution of a linear block code to find out the probability of undetected error. So you can see that frame error rate performance of this A R Q protocol does not depend on what we are transmitting or how we are transmitting. It only depends on the error detecting code that we are choosing.

So

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A screenshot of a presentation slide. The slide has a dark red header with the title "Reliability analysis of an ARQ system" in white text. Below the header, there is a list of three bullet points. The first two define P_d and P_u . The third states that assuming no limit on retransmissions, the frame error rate is given by a formula. The formula is $FER = P_u + P_d P_u + P_d^2 P_u + P_d^3 P_u \dots + P_u \sum_{i=0}^{\infty} P_d^i = \frac{P_u}{1 - P_d}$. Below the formula, there are two more bullet points: one stating the analysis is true for any ARQ protocol, and another stating that reliability depends only on the error detecting code and is independent of the number of retransmissions.

Reliability analysis of an ARQ system

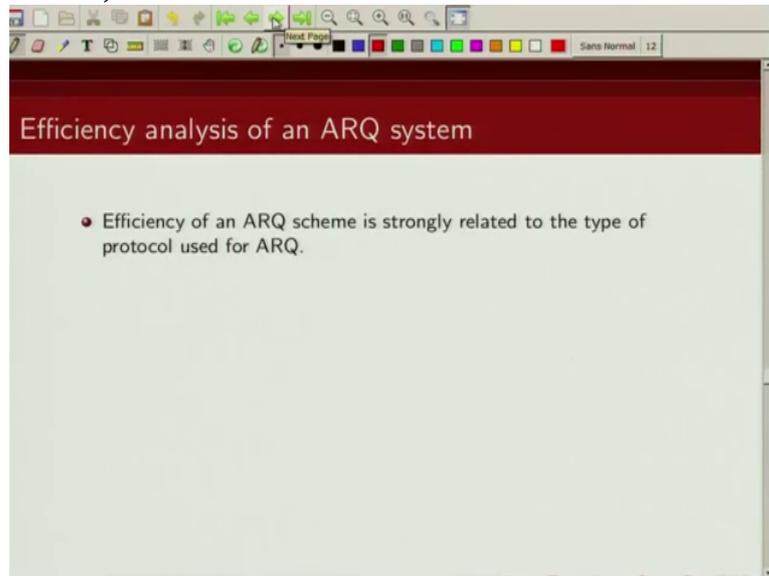
- Let P_d be the probability that a frame is received in error and correctly detected by the error detecting code.
- Let P_u be the probability that a frame is received in error and the error detecting code is unable to detect it.
- Assuming there is no limit on retransmissions, the frame error rate is given by

$$FER = P_u + P_d P_u + P_d^2 P_u + P_d^3 P_u \dots + P_u \sum_{i=0}^{\infty} P_d^i = \frac{P_u}{1 - P_d}$$

- This analysis is true for any ARQ protocol.
- Reliability of an ARQ protocol depends only on the error detecting capability of the underlying error detecting code and is independent of how many corrupted frames are subsequently retransmitted.

to summarize the reliability of A R Q protocol which we are determining on the basis of frame error rate depends only on the error detecting capability of the underlying error detecting code and it is independent of how these corrupted frames or packets are subsequently retransmitted.

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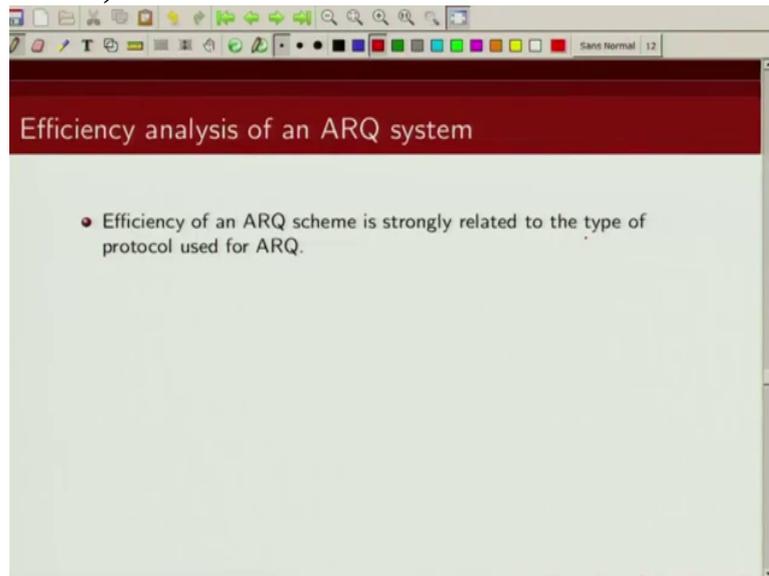
Next we are going to move into efficiency analysis of these A R Q protocols. So how are we going to measure efficiency

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of this A R Q scheme? So we are going to do that on the basis of throughput that we can achieve when we are using these A R Q Protocols. And this

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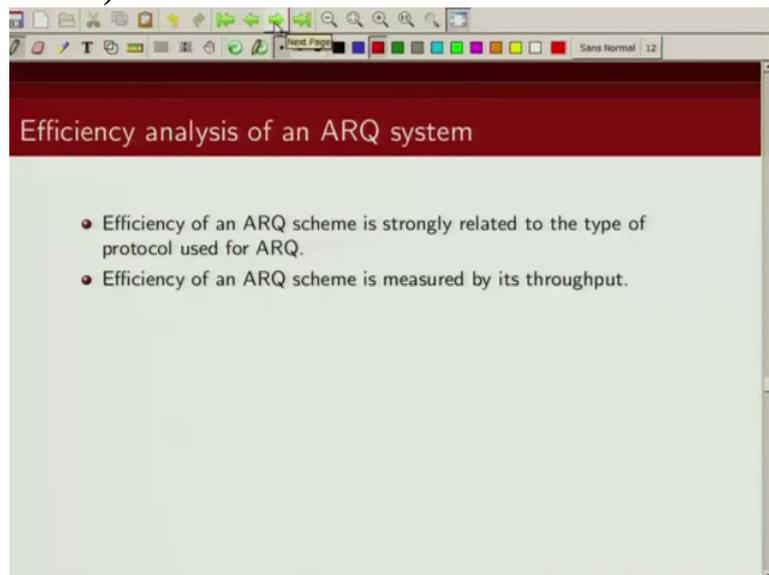
efficiency is very closely related to what sort of protocols that you are using. As you can see we saw in Stop and Wait, for a long time, idle time

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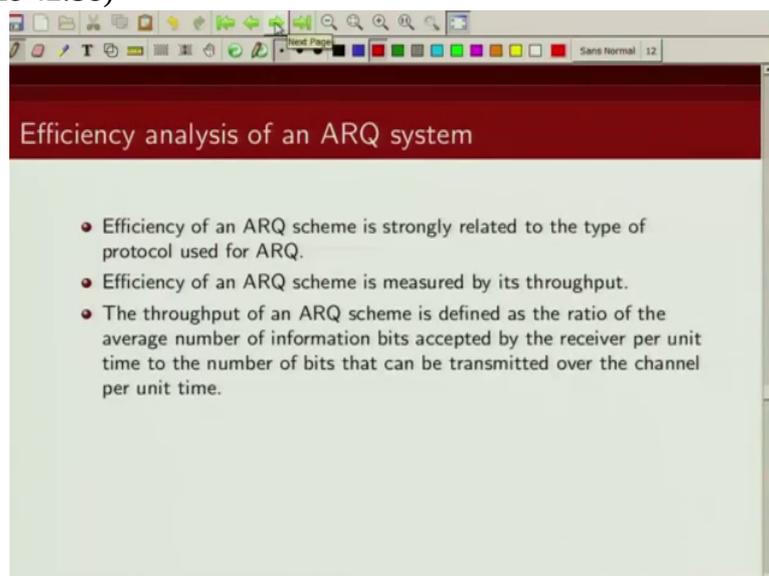
we are just waiting. We are not transmitting anything, so efficiency is very low. On the other stream, Selective Repeat has the best efficiency because we are only retransmitting those packets which are received incorrectly.

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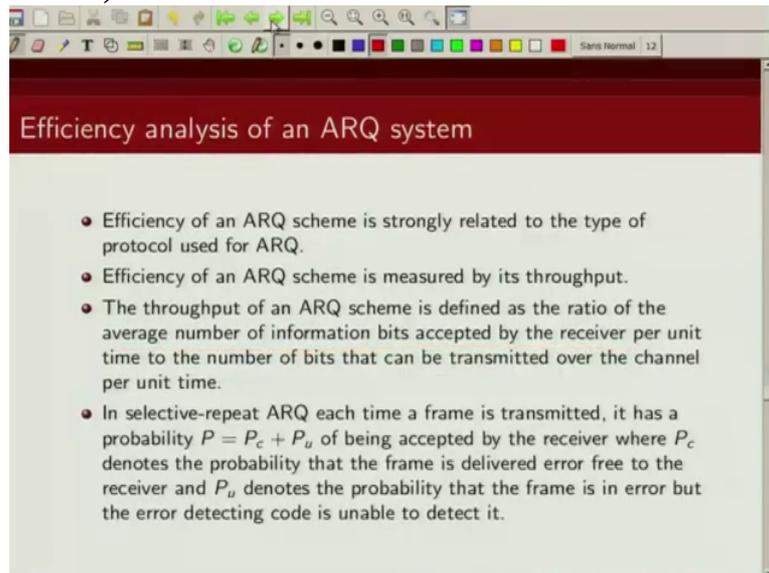
So we are measuring or we are quantifying the efficiency of an A R Q scheme using its throughput.

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So the throughput of an A R Q scheme is defined as the ratio of the average number of information bits accepted by the receiver per unit time to the number of bits that can be transmitted over a channel per unit time.

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The image shows a presentation slide with a red header and a white body. The header contains the title "Efficiency analysis of an ARQ system". The body contains a bulleted list of four points. The first point states that efficiency is related to the protocol type. The second point states that efficiency is measured by throughput. The third point defines throughput as the ratio of average information bits accepted to the number of bits transmitted. The fourth point discusses selective-repeat ARQ and its probability of being accepted, $P = P_c + P_u$, where P_c is the probability of error-free delivery and P_u is the probability of undetected errors.

Efficiency analysis of an ARQ system

- Efficiency of an ARQ scheme is strongly related to the type of protocol used for ARQ.
- Efficiency of an ARQ scheme is measured by its throughput.
- The throughput of an ARQ scheme is defined as the ratio of the average number of information bits accepted by the receiver per unit time to the number of bits that can be transmitted over the channel per unit time.
- In selective-repeat ARQ each time a frame is transmitted, it has a probability $P = P_c + P_u$ of being accepted by the receiver where P_c denotes the probability that the frame is delivered error free to the receiver and P_u denotes the probability that the frame is in error but the error detecting code is unable to detect it.

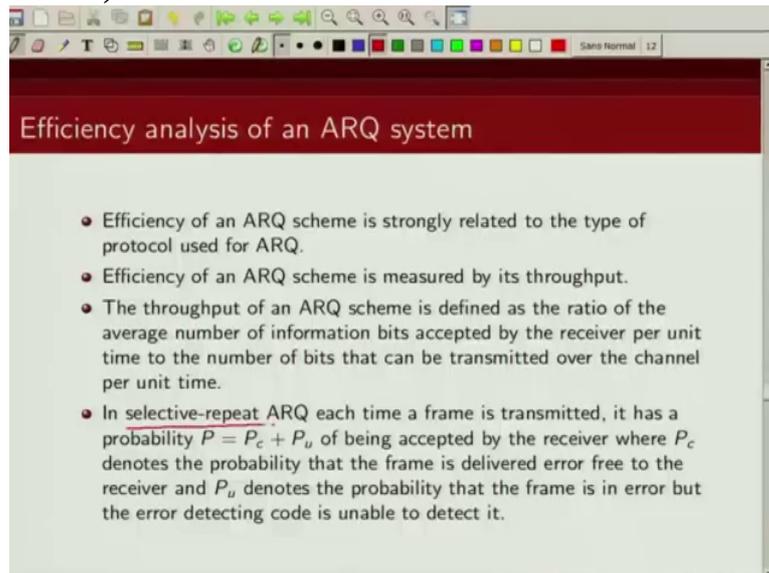
So let us compute this throughput for each of these 3 A R Q

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protocols that we talked about. So first scheme that we are going to talk about

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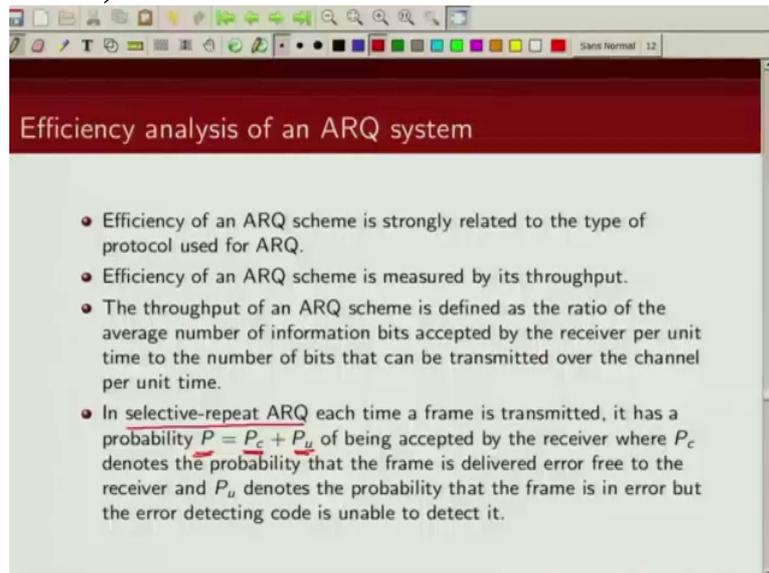
is Selective Repeat A R Q. Now in the Selective Repeat A R Q, the probability of receiving or accepting a packet is given by P where P is the probability that P is sum of two probability. This probability P sub c is basically the probability that the frame is delivered error free and the probability P u is the probability that the error detecting code failed to detect

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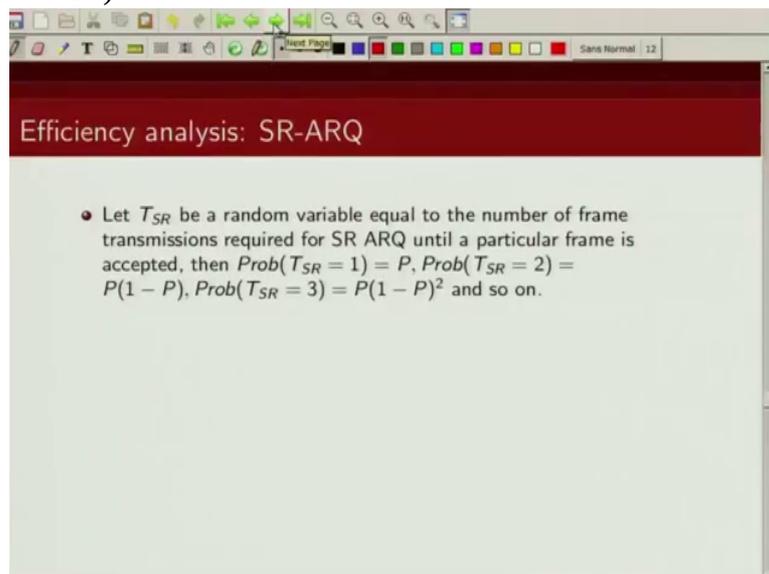
an error. So we are going to accept a packet either when it is received error free or the error detecting code fails to detect the error and thinks that it has received the packet correctly. So

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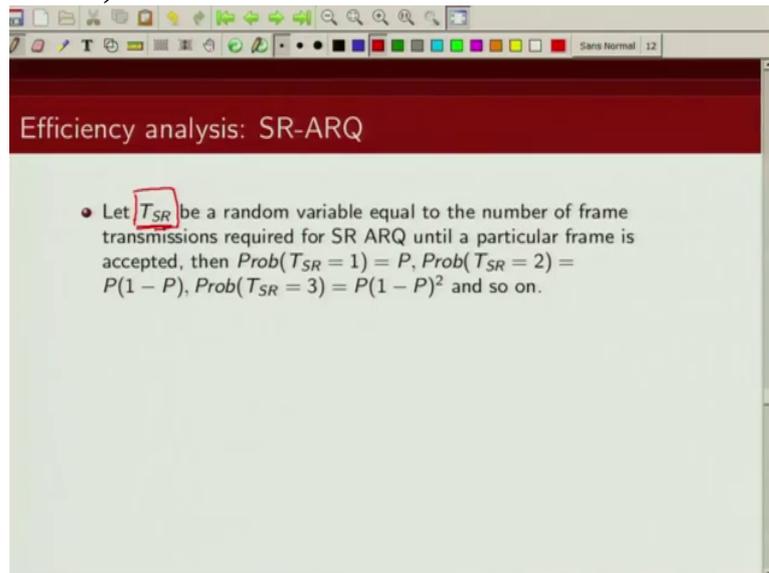
probability P is the sum of these two probability, with this probability we are going to accept, the receiver is going to accept a particular packet in S R A R Q scheme. Now

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T_{SR} is a random variable that we are defining is equal to the number of frame transmission required for a selective repeat A R Q scheme until a particular frame is accepted. So this is basically a random error because

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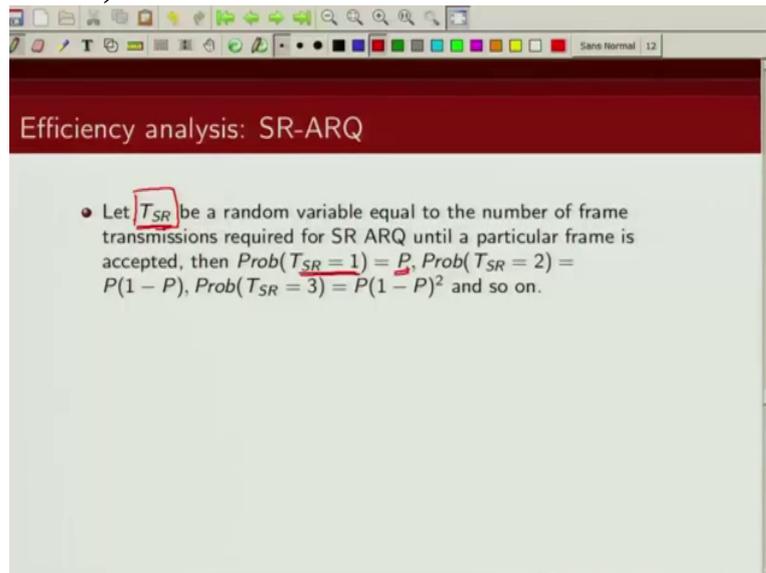
it can happen that at first instance the packet is accepted or it can happen that we need to transmit the packet twice to get the packet accepted or we could have to have transmit thrice or more, so T_{SR} is a random variable which denotes the number of frame transmission required for Selective Repeat A R Q until a particular frame is accepted. Now let us compute the probability that T_{SR} is 1. What is the probability that at first transmission itself you are going to accept the packet? Now we just saw that that probability is given by P which is sum of P sub c

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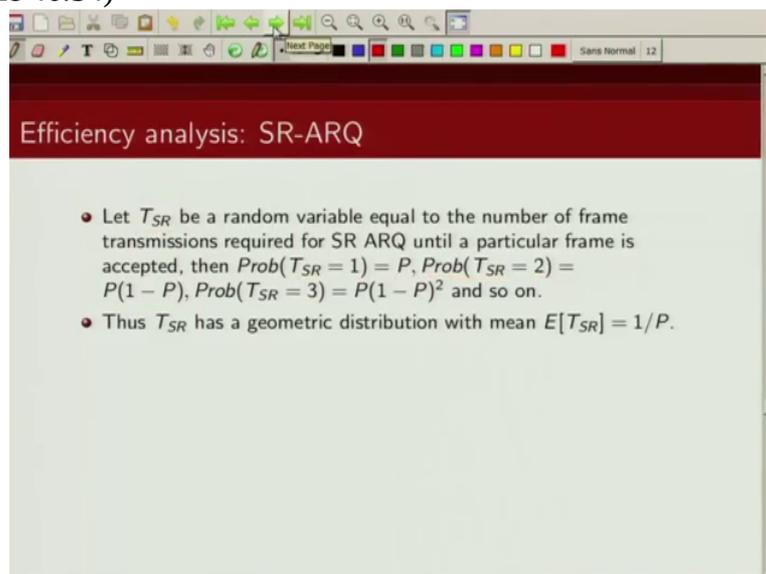
plus P sub u . So with probability P

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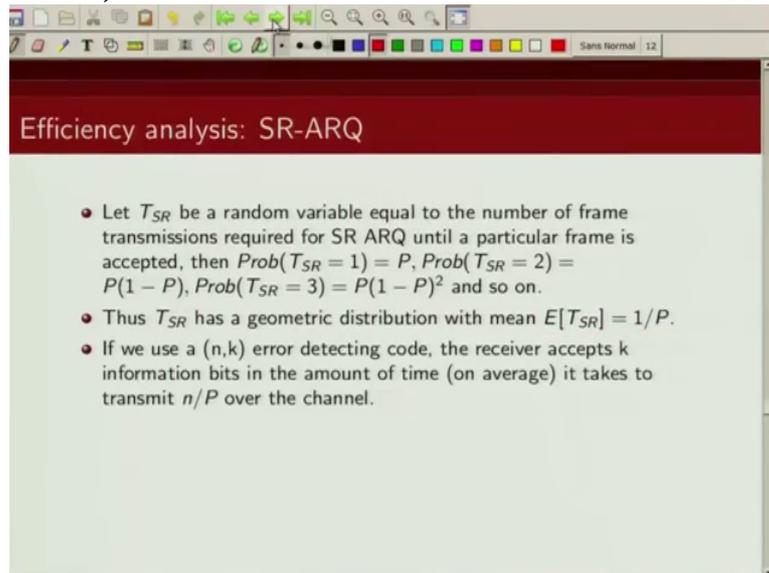
we are going to accept the packet the first transmission. Now what is the probability that we need to transmit the packet twice? So that would be given by $1 - P$ because in the first instance we did not accept the packet, that probability is given by $1 - P$ and the second instance we accepted the packet. So the probability is given by P times $1 - P$. Similarly what's the probability that we have to make 3 transmissions before a packet is accepted. Now in this case, it means that the first two transmissions we did not accept the packet, that probability is $1 - P$ square. And the third transmission we accepted it so that is P $1 - P$ square. So

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we can see that this random variable has a geometric distribution and the expected value of this is given by 1 divided by P . Now

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The image shows a presentation slide with a red header and a white body. The header contains the text "Efficiency analysis: SR-ARQ". The body contains three bullet points discussing the random variable T_{SR} and its geometric distribution, as well as the relationship between n , k , and P .

Efficiency analysis: SR-ARQ

- Let T_{SR} be a random variable equal to the number of frame transmissions required for SR ARQ until a particular frame is accepted, then $Prob(T_{SR} = 1) = P$, $Prob(T_{SR} = 2) = P(1 - P)$, $Prob(T_{SR} = 3) = P(1 - P)^2$ and so on.
- Thus T_{SR} has a geometric distribution with mean $E[T_{SR}] = 1/P$.
- If we use a (n, k) error detecting code, the receiver accepts k information bits in the amount of time (on average) it takes to transmit n/P over the channel.

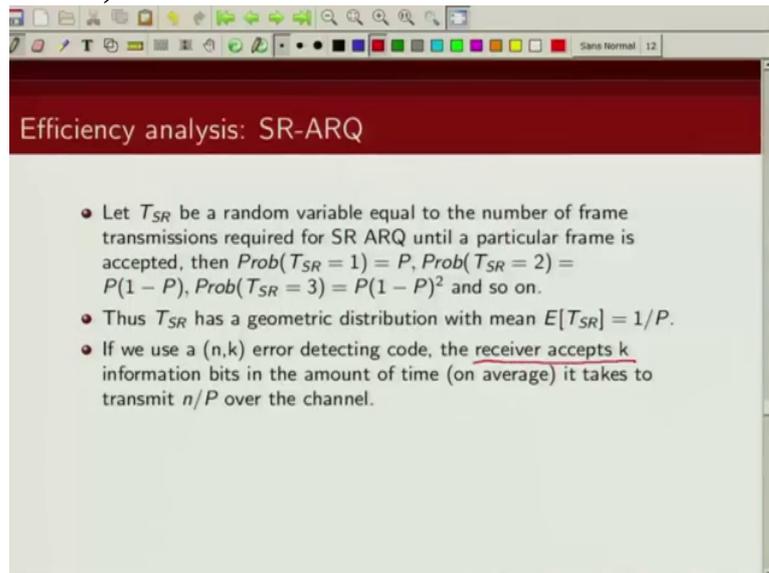
remember we are using

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n k linear block code, we have k information bits and then n minus k C R C check bits and overall block length is n . So the receiver accepts k

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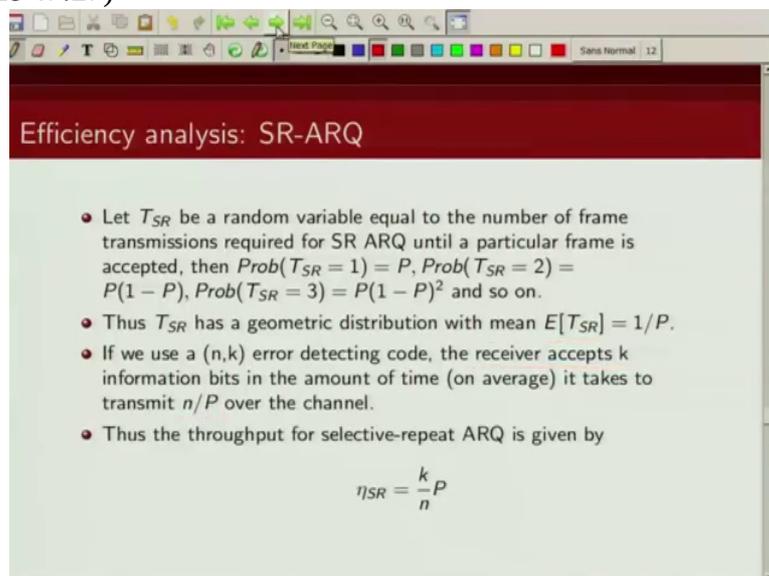


The slide is titled "Efficiency analysis: SR-ARQ" and contains the following text:

- Let T_{SR} be a random variable equal to the number of frame transmissions required for SR ARQ until a particular frame is accepted, then $Prob(T_{SR} = 1) = P$, $Prob(T_{SR} = 2) = P(1 - P)$, $Prob(T_{SR} = 3) = P(1 - P)^2$ and so on.
- Thus T_{SR} has a geometric distribution with mean $E[T_{SR}] = 1/P$.
- If we use a (n,k) error detecting code, the receiver accepts k information bits in the amount of time (on average) it takes to transmit n/P over the channel.

information bits in the amount of time it takes to transmit n into expected value of this. So receiver is accepting k information bits over the time it takes to transmit n divided by P over the communication channel. So the throughput is given by

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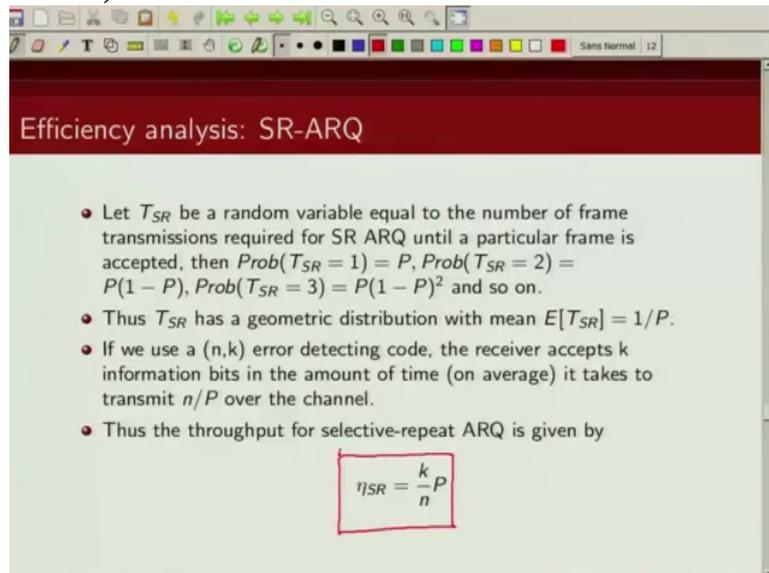
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- Thus T_{SR} has a geometric distribution with mean $E[T_{SR}] = 1/P$.
- If we use a (n,k) error detecting code, the receiver accepts k information bits in the amount of time (on average) it takes to transmit n/P over the channel.
- Thus the throughput for selective-repeat ARQ is given by

$$\eta_{SR} = \frac{kP}{n}$$

k divided by n by P which is this.

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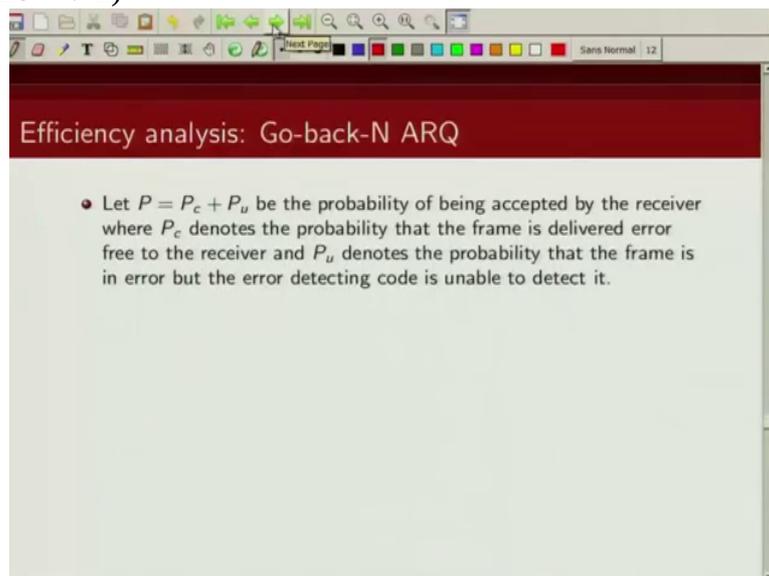
The slide is titled "Efficiency analysis: SR-ARQ" and contains the following text:

- Let T_{SR} be a random variable equal to the number of frame transmissions required for SR ARQ until a particular frame is accepted, then $Prob(T_{SR} = 1) = P$, $Prob(T_{SR} = 2) = P(1 - P)$, $Prob(T_{SR} = 3) = P(1 - P)^2$ and so on.
- Thus T_{SR} has a geometric distribution with mean $E[T_{SR}] = 1/P$.
- If we use a (n, k) error detecting code, the receiver accepts k information bits in the amount of time (on average) it takes to transmit n/P over the channel.
- Thus the throughput for selective-repeat ARQ is given by

$$\eta_{SR} = \frac{k}{n} P$$

So for a Selective Repeat A R Q scheme, the throughput is given by k by n into P .

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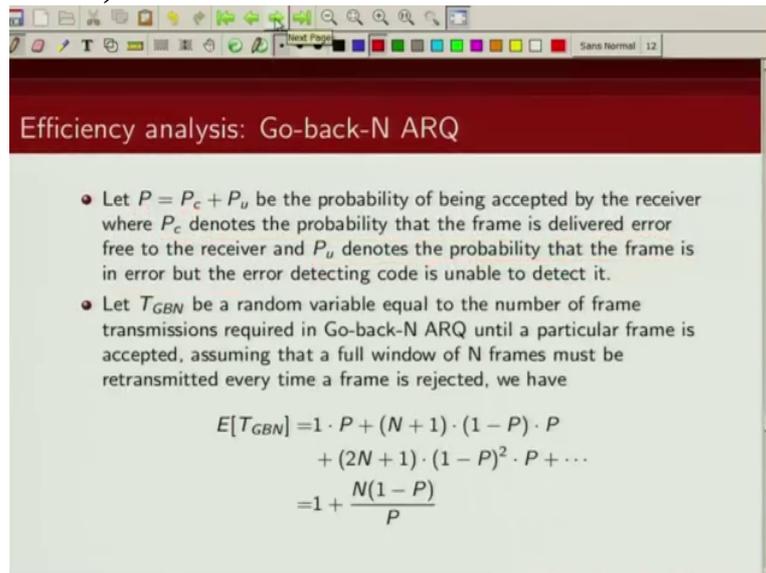


The slide is titled "Efficiency analysis: Go-back-N ARQ" and contains the following text:

- Let $P = P_c + P_u$ be the probability of being accepted by the receiver where P_c denotes the probability that the frame is delivered error free to the receiver and P_u denotes the probability that the frame is in error but the error detecting code is unable to detect it.

Now let us compute the throughput for Go Back N. Again here, P_c denotes the probability that the frame is received error free at the receiver and P_u denotes the probability that the frame is in error but the receiver is unable to detect it. So a packet will be accepted with probability P .

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The slide is titled "Efficiency analysis: Go-back-N ARQ". It contains two bullet points and a mathematical derivation. The first bullet point defines $P = P_c + P_u$ as the probability of a frame being accepted by the receiver, where P_c is the probability of a frame being delivered error-free and P_u is the probability of a frame being in error but undetected. The second bullet point defines T_{GBN} as the number of frame transmissions required until a frame is accepted, assuming a full window of N frames is retransmitted every time a frame is rejected. The derivation shows the expected value $E[T_{GBN}] = 1 \cdot P + (N+1) \cdot (1-P) \cdot P + (2N+1) \cdot (1-P)^2 \cdot P + \dots$, which simplifies to $= 1 + \frac{N(1-P)}{P}$.

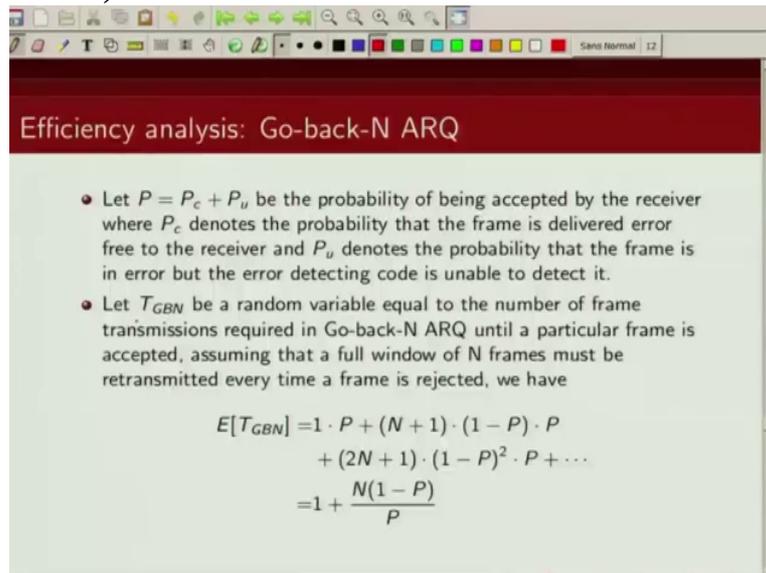
Now case of analysis, we are

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considering the worst case scenario and what is the worst case scenario? So we are assuming that the error happens in the beginning of the block. If the error happens in the beginning of the block then we need to, remember in Go Back N we backtrack and start sending, retransmit the packet from the sequence which has been received incorrectly. So the worst case scenario would be if at the start of the window we receive the first packet itself incorrectly then we would retransmit the next n bits. So

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The slide contains the following text:

- Let $P = P_c + P_u$ be the probability of being accepted by the receiver where P_c denotes the probability that the frame is delivered error free to the receiver and P_u denotes the probability that the frame is in error but the error detecting code is unable to detect it.
- Let T_{GBN} be a random variable equal to the number of frame transmissions required in Go-back-N ARQ until a particular frame is accepted, assuming that a full window of N frames must be retransmitted every time a frame is rejected, we have

$$\begin{aligned} E[T_{GBN}] &= 1 \cdot P + (N + 1) \cdot (1 - P) \cdot P \\ &\quad + (2N + 1) \cdot (1 - P)^2 \cdot P + \dots \\ &= 1 + \frac{N(1 - P)}{P} \end{aligned}$$

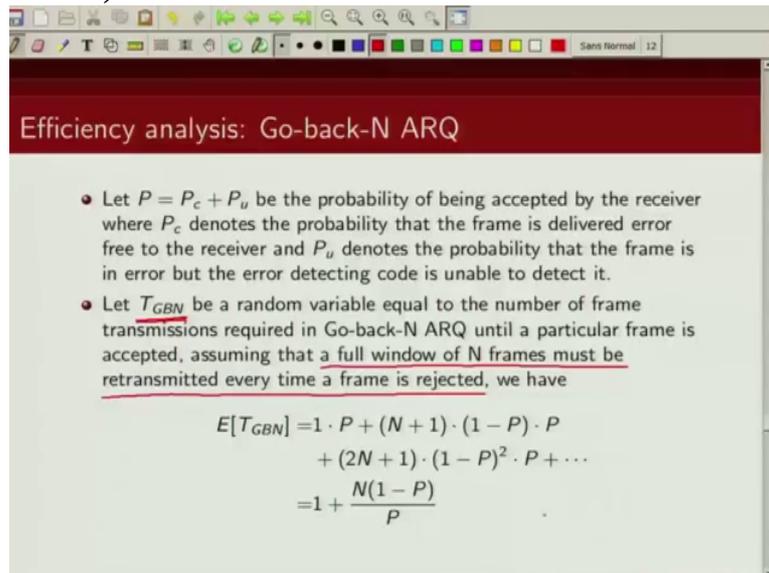
T_{GBN} denotes a random variable which is equal to number of transmissions required in Go Back N until a particular frame is accepted. Now we are considering a worst case scenario, and what is that worst case scenario, we are assuming that a full window of n frames must be retransmitted every time a frame is rejected. So we are assuming the worst case scenario, the first

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frame in this window itself is received incorrectly. Therefore we are retransmitting all the packets. Now you can see if the packet is received

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The slide is titled "Efficiency analysis: Go-back-N ARQ" and contains the following text:

- Let $P = P_c + P_u$ be the probability of being accepted by the receiver where P_c denotes the probability that the frame is delivered error free to the receiver and P_u denotes the probability that the frame is in error but the error detecting code is unable to detect it.
- Let T_{GBN} be a random variable equal to the number of frame transmissions required in Go-back-N ARQ until a particular frame is accepted, assuming that a full window of N frames must be retransmitted every time a frame is rejected, we have

$$\begin{aligned} E[T_{GBN}] &= 1 \cdot P + (N + 1) \cdot (1 - P) \cdot P \\ &\quad + (2N + 1) \cdot (1 - P)^2 \cdot P + \dots \\ &= 1 + \frac{N(1 - P)}{P} \end{aligned}$$

correctly the first instance that probability is 1 into P. Now if the packet is not received correctly first instance we are retransmitting

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because we are considering the worst case scenario, we are re-transmitting these n packets so the number so we,

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The slide contains the following text:

- Let $P = P_c + P_u$ be the probability of being accepted by the receiver where P_c denotes the probability that the frame is delivered error free to the receiver and P_u denotes the probability that the frame is in error but the error detecting code is unable to detect it.
- Let T_{GBN} be a random variable equal to the number of frame transmissions required in Go-back-N ARQ until a particular frame is accepted, assuming that a full window of N frames must be retransmitted every time a frame is rejected, we have

$$E[T_{GBN}] = 1 \cdot P + (N+1) \cdot (1-P) \cdot P + (2N+1) \cdot (1-P)^2 \cdot P + \dots$$
$$= 1 + \frac{N(1-P)}{P}$$

second time it will be received correctly n plus 1 into this is the probability that it was not received correctly in the first instance and this is the probability that the second instance it was received correctly. Similarly what is the probability that T, G B N is 3, so the first 2 transmissions, we did not receive the packets correctly but the third transmission we received correctly. That probability is given by this and number of frames that we transmitted is 2 raised to power 2 into N plus 1. So this, like this we can compute and this expected value comes out to be this quantity

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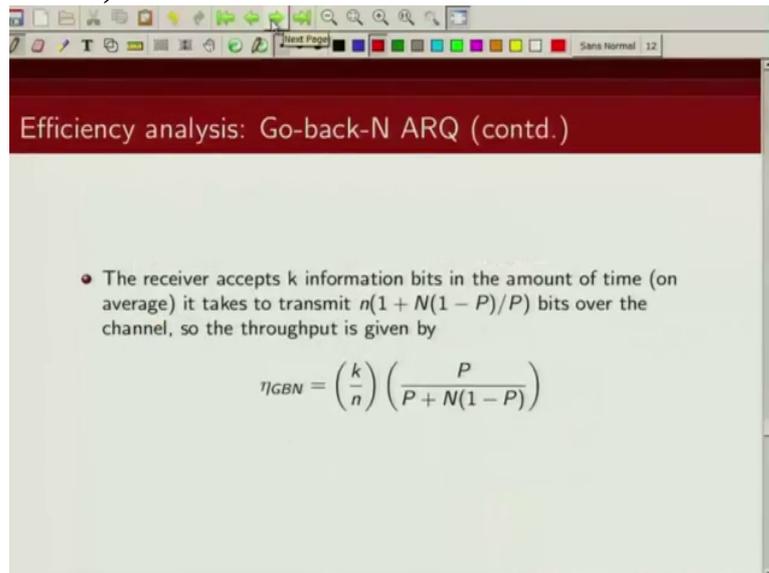
The slide contains the following text:

- Let $P = P_c + P_u$ be the probability of being accepted by the receiver where P_c denotes the probability that the frame is delivered error free to the receiver and P_u denotes the probability that the frame is in error but the error detecting code is unable to detect it.
- Let T_{GBN} be a random variable equal to the number of frame transmissions required in Go-back-N ARQ until a particular frame is accepted, assuming that a full window of N frames must be retransmitted every time a frame is rejected, we have

$$E[T_{GBN}] = 1 \cdot P + (N+1) \cdot (1-P) \cdot P + (2N+1) \cdot (1-P)^2 \cdot P + \dots$$
$$= 1 + \frac{N(1-P)}{P}$$

which is 1 plus N into 1 minus P divided by P. Now remember

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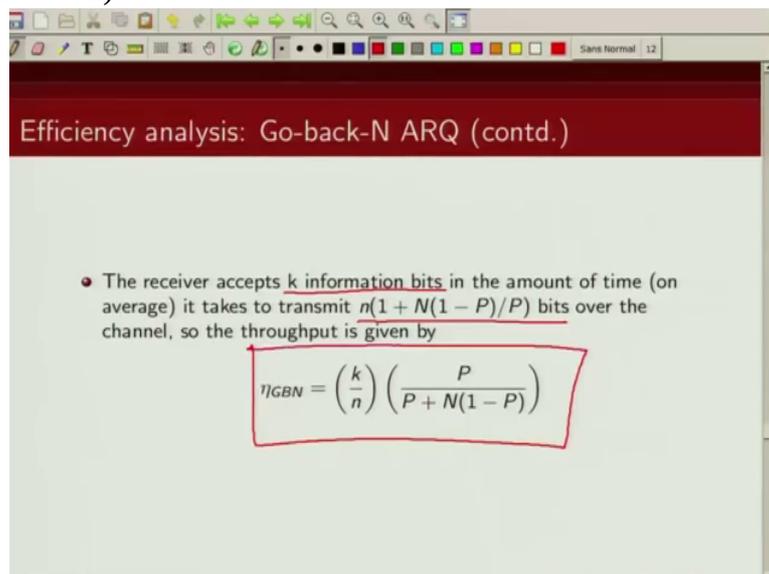
The slide contains the following text:

- The receiver accepts k information bits in the amount of time (on average) it takes to transmit $n(1 + N(1 - P)/P)$ bits over the channel, so the throughput is given by

$$\eta_{GBN} = \left(\frac{k}{n}\right) \left(\frac{P}{P + N(1 - P)}\right)$$

the receiver is accepting k information bits in this amount of time where it takes to transmit this many bits over the channel. So the throughput of Go Back N is given

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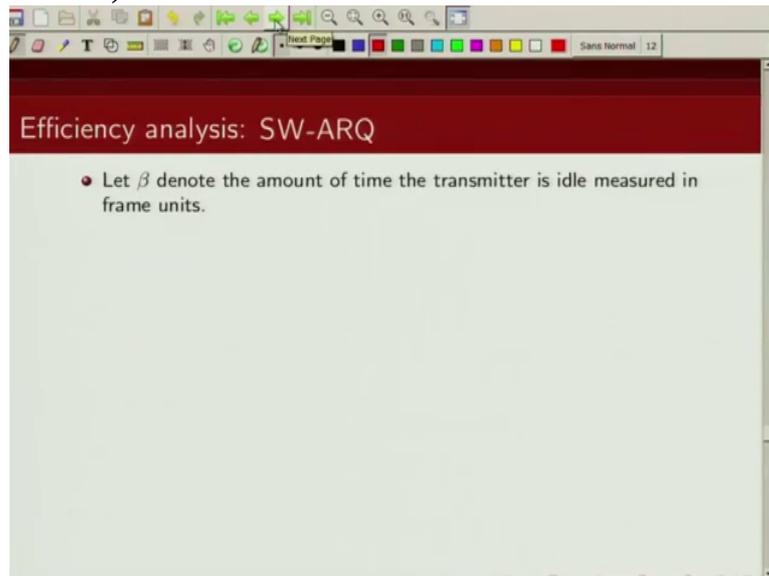
The slide contains the following text:

- The receiver accepts k information bits in the amount of time (on average) it takes to transmit $n(1 + N(1 - P)/P)$ bits over the channel, so the throughput is given by

$$\eta_{GBN} = \left(\frac{k}{n}\right) \left(\frac{P}{P + N(1 - P)}\right)$$

by this expression. Now this is, this quantity is less than what we can achieve from Selective Repeat scheme

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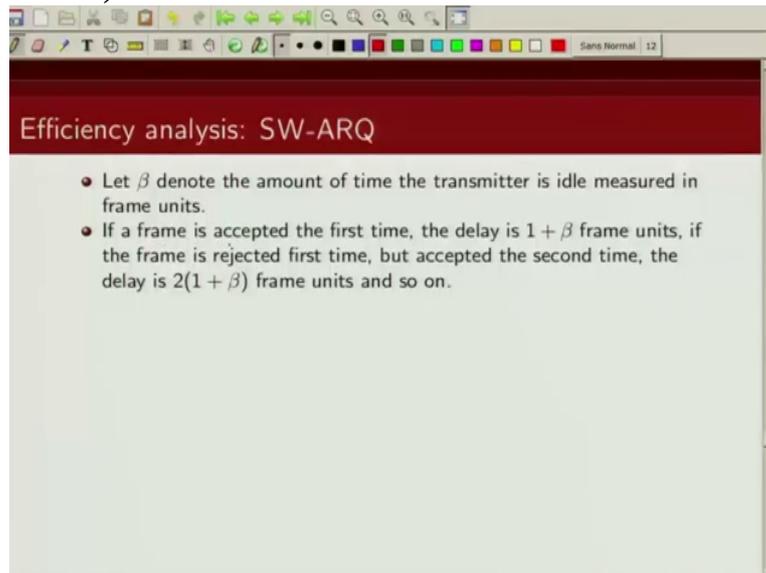
And finally we do this analysis for Stop and Wait A R Q. So let beta denotes amount of time the transmitter is idle and this idle time we are

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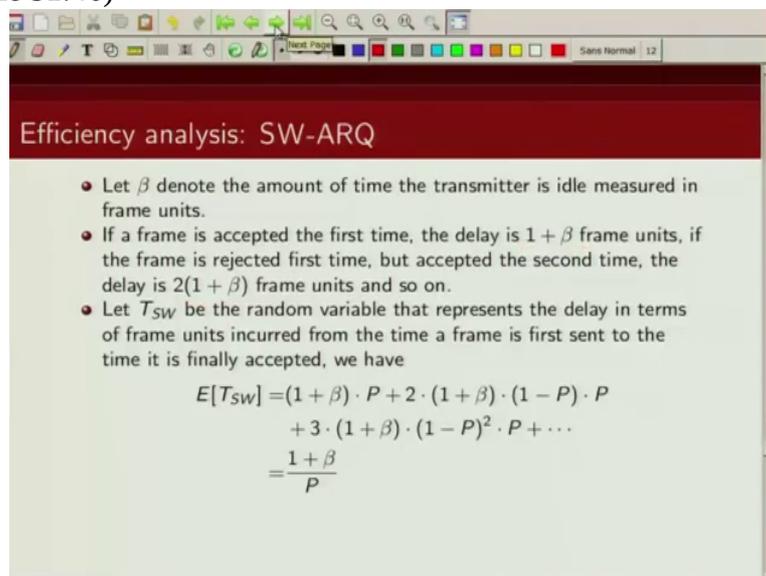
measuring in terms of this frame unit or frame length. So if a frame

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is accepted for the first time the total delay is 1 plus beta frame unit. If the frame is rejected and accepted second time, then the delay is two times 1 plus beta. If the frame is rejected second time and accepted third time the delay is three times 1 plus beta.

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So if we use this random variable T_{SW} to represent the delay in terms of the frame unit, then expected value of this is given by, so this is the delay involved when the packet is accepted for the first time and this is the probability with which the packet will be accepted, the frame will be accepted for the first time. If it is rejected the first time and second time it is accepted this is the delay and this is the probability of the frame being rejected first time but accepted second time. Similarly this is the probability that the frame has been rejected twice

but the third instance it has been accepted and there is the corresponding delay. So like that if you write it, expected value of this T S W comes out to be 1 plus beta

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Efficiency analysis: SW-ARQ

- Let β denote the amount of time the transmitter is idle measured in frame units.
- If a frame is accepted the first time, the delay is $1 + \beta$ frame units, if the frame is rejected first time, but accepted the second time, the delay is $2(1 + \beta)$ frame units and so on.
- Let T_{SW} be the random variable that represents the delay in terms of frame units incurred from the time a frame is first sent to the time it is finally accepted, we have

$$E[T_{SW}] = (1 + \beta) \cdot P + 2 \cdot (1 + \beta) \cdot (1 - P) \cdot P + 3 \cdot (1 + \beta) \cdot (1 - P)^2 \cdot P + \dots$$

$$= \frac{1 + \beta}{P}$$

divided by P.

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Efficiency analysis: SW-ARQ

- Let β denote the amount of time the transmitter is idle measured in frame units.
- If a frame is accepted the first time, the delay is $1 + \beta$ frame units, if the frame is rejected first time, but accepted the second time, the delay is $2(1 + \beta)$ frame units and so on.
- Let T_{SW} be the random variable that represents the delay in terms of frame units incurred from the time a frame is first sent to the time it is finally accepted, we have

$$E[T_{SW}] = (1 + \beta) \cdot P + 2 \cdot (1 + \beta) \cdot (1 - P) \cdot P + 3 \cdot (1 + \beta) \cdot (1 - P)^2 \cdot P + \dots$$

$$= \frac{1 + \beta}{P}$$

- So the throughput is given by

$$\eta_{SW} = \left(\frac{k}{n}\right) \left(\frac{P}{1 + \beta}\right)$$

And again so the receiver is receiving k bits

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over the time N times $1 + \beta$ by P so the throughput will be given by

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The slide is titled "Efficiency analysis: SW-ARQ" and contains the following text:

- Let β denote the amount of time the transmitter is idle measured in frame units.
- If a frame is accepted the first time, the delay is $1 + \beta$ frame units, if the frame is rejected first time, but accepted the second time, the delay is $2(1 + \beta)$ frame units and so on.
- Let T_{SW} be the random variable that represents the delay in terms of frame units incurred from the time a frame is first sent to the time it is finally accepted, we have

$$E[T_{SW}] = (1 + \beta) \cdot P + 2 \cdot (1 + \beta) \cdot (1 - P) \cdot P + 3 \cdot (1 + \beta) \cdot (1 - P)^2 \cdot P + \dots$$
$$= \frac{1 + \beta}{P}$$

- So the throughput is given by

$$\eta_{SW} = \left(\frac{k}{n} \right) \left(\frac{P}{1 + \beta} \right)$$

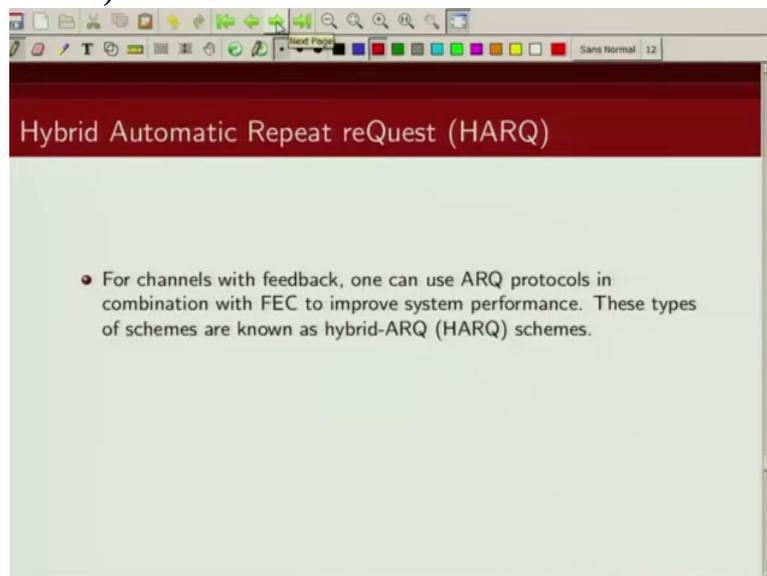
this expression. Now if we compare the efficiency of Stop and Wait, Go Back N

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and Selective Repeat A R Q, Selective Repeat A R Q will give us the best throughput followed by Go Back N and then Stop and Wait. However from the complexity point of view, Stop and Wait is the simplest, then Go Back N and then the most complex scheme is Selective Repeat A R Q.

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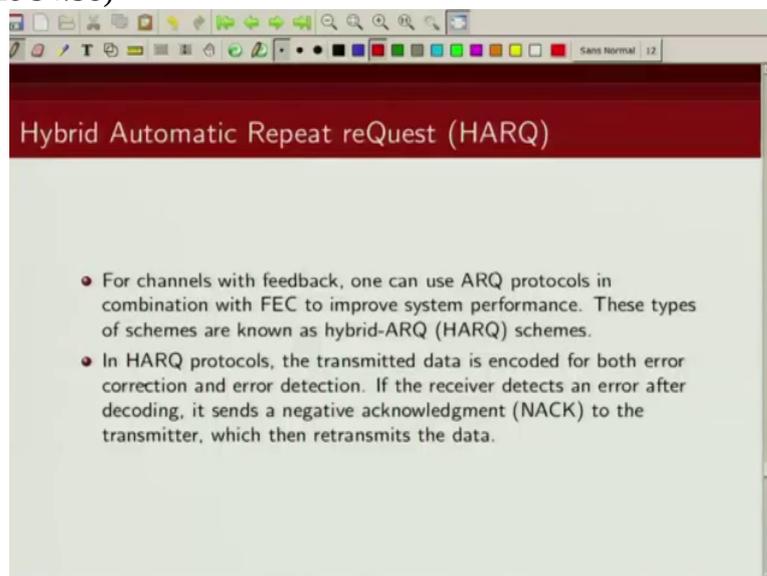
So next we are going to talk about hybrid A R Q which combines forward error correction with A R Q. So as you know that

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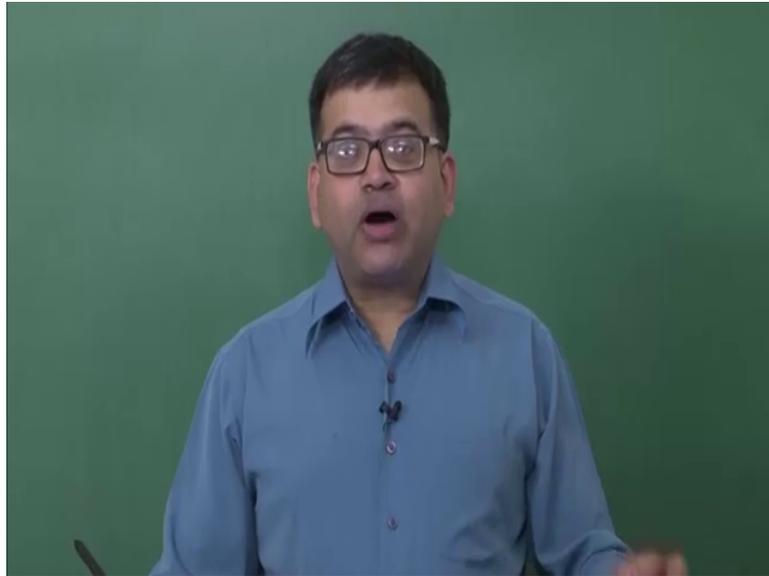
channels, one way channels use forward error correction and in two way channels we can use A R Q. In the pure A R Q protocols we send uncoded packets along with some parity bits which we use for error detection. Now in hybrid A R Q, we are using f e c coded packets for transmission, so at the receiver we will first try to correct the errors and then we will detect for errors. Now if the packets are detected to be in error, there receiver will send a NACK and another packet will be re-transmitted. So the difference between A R Q and hybrid A R Q is in pure A R Q protocols we send uncoded packets along with some parity bits which we use for error detection. So typically in pure A R Q protocol the information bits are coded using an error detecting code. Here we are encoding the packet using an error detecting as well as error correcting code.

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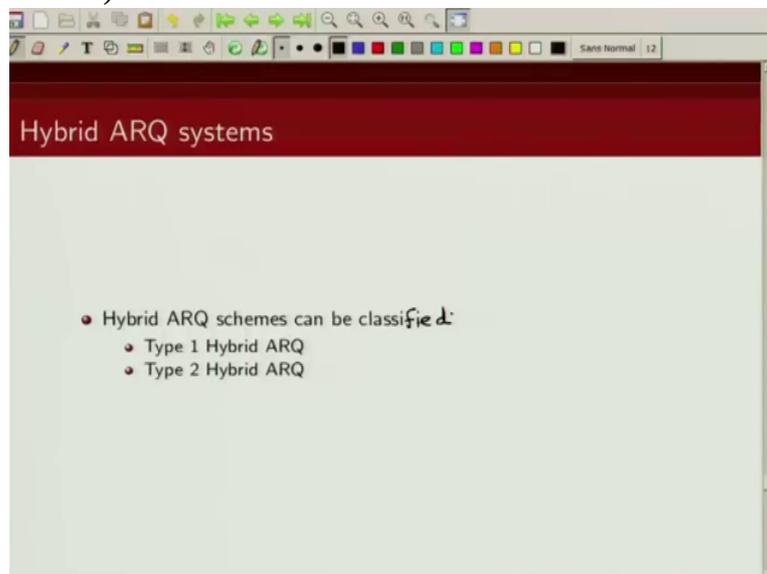
So in hybrid A R Q protocols we encode the transmitted data for both error correction as well as error detection. The receiver detects the error; after decoding it will send a negative acknowledgement and then the data will be again transmitted.

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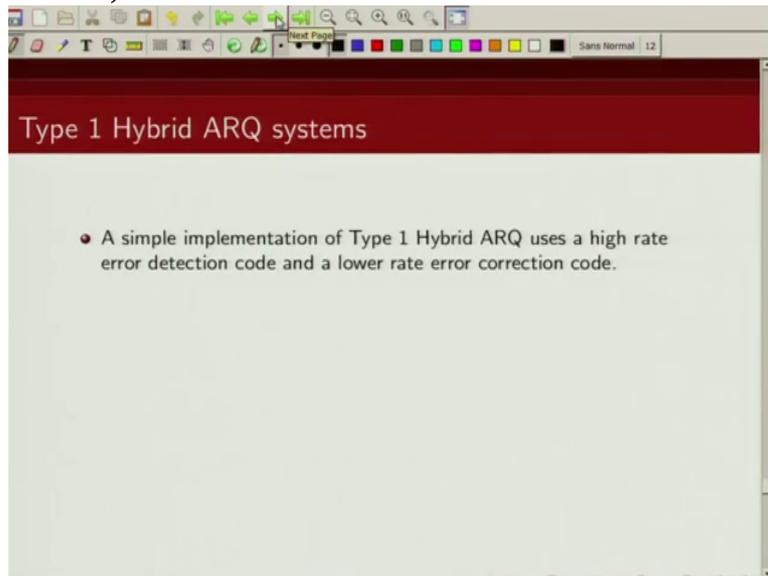
So hybrid A R Q schemes can be classified into 2 categories,

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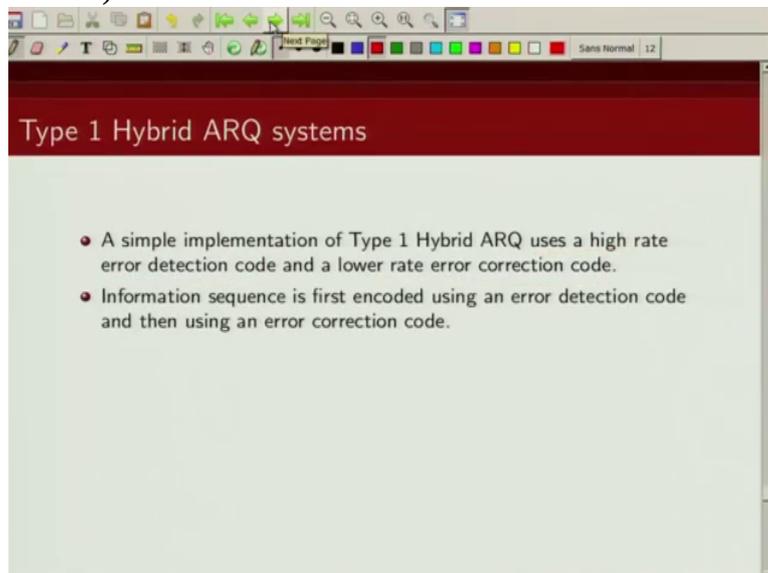
first one which is known as Type 1 hybrid A R Q scheme and second one which is known as Type 2 hybrid A R Q schemes.

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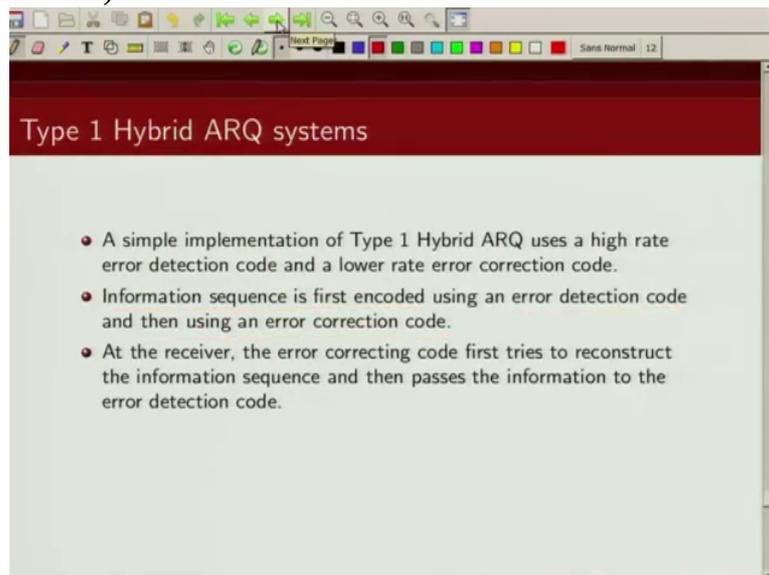
So we will first describe Type 1 hybrid A R Q scheme. So simple implementation of a Type 1 hybrid A R Q scheme is as follows. We use a very high rate error detecting code and a lower rate error correcting code. Now this high rate error detecting code could be like for example, C R C which we are going to use to detect error and this lower rate error correcting code could be convolutional code, turbo code, L D P C code

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so the information sequence is first encoded using an error detecting code and then using a error correcting code. At the receiver

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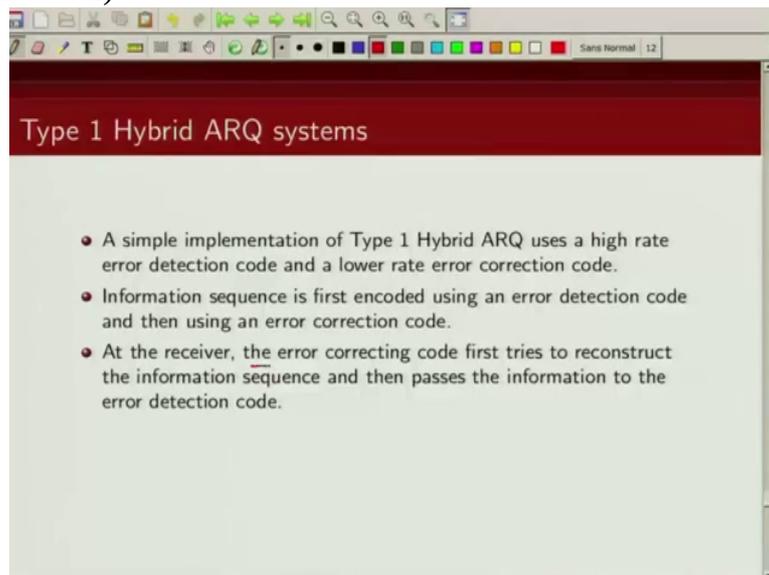
when you receive this coded packet you first

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try to decode and try to correct errors. So at the receiver

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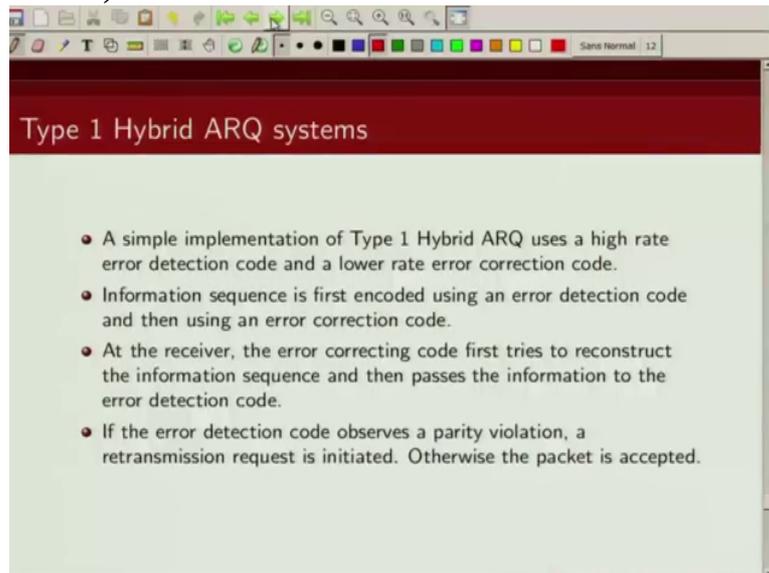
the error correcting code first tries to reconstruct the information sequence or estimate the information sequence and then it passes

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the information to the error detecting code which is going to check whether estimated information sequence is in error or not.

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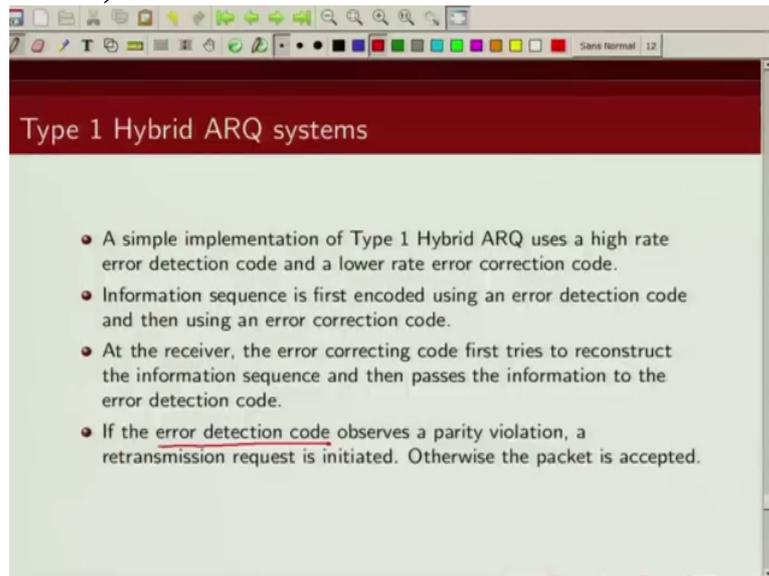
If the error detection code observes that there is a parity violation, for example

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if you are example if you are using a C R C we know that the received polynomial should be divisible by the generator polynomial of the C R C code. So if the

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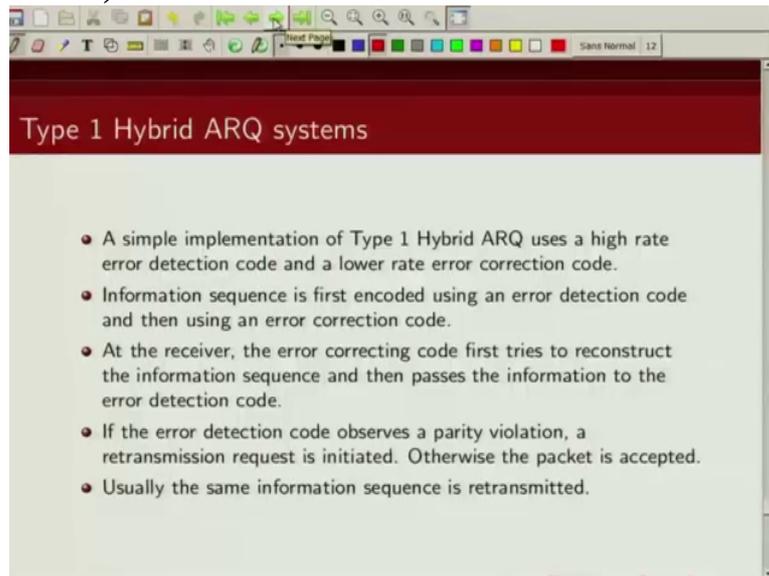
error detecting code observes the parity violation then it will ask for re-transmission. Otherwise the packet would be assumed to be

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received correctly and we will accept the packet.

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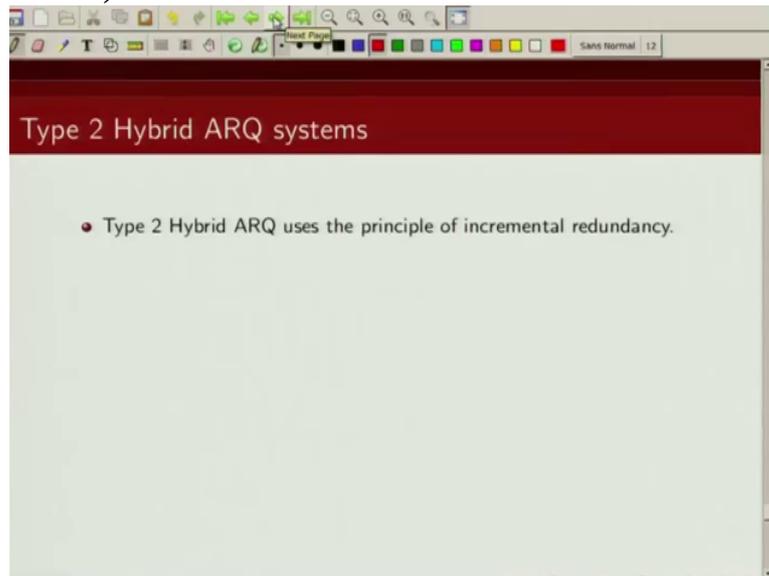
And usually in this Type 1 A R Q scheme when we retransmit we are again sending the same coded packet. So

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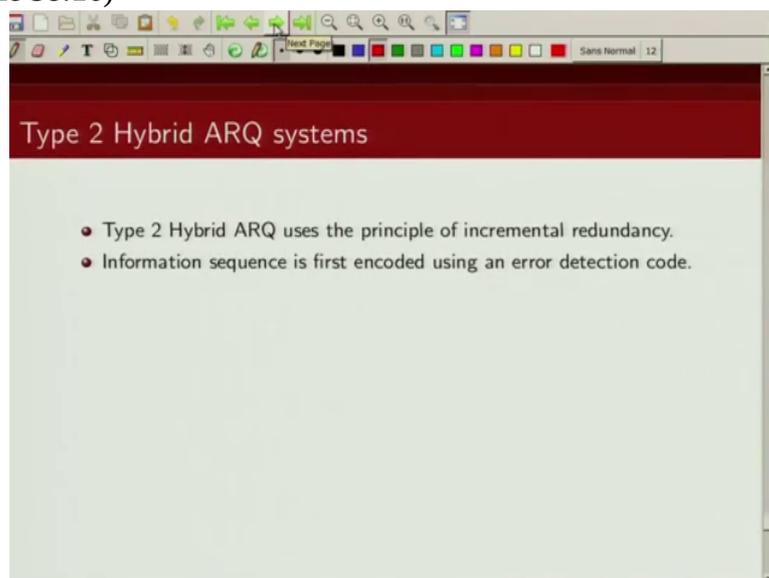
whenever the receiver is asking for retransmission, we are sending the same coded packet again. The receiver tries to correct the errors and then the information, extrinsic information sequence is passed to a error detecting code which will try to detect whether there is any error in the decoded sequence. If there is error again, receiver will set a NACK and the same coded sequence would be sent again.

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Now type 2 hybrid A R Q scheme works on the principle of incremental redundancy. So what happens here is

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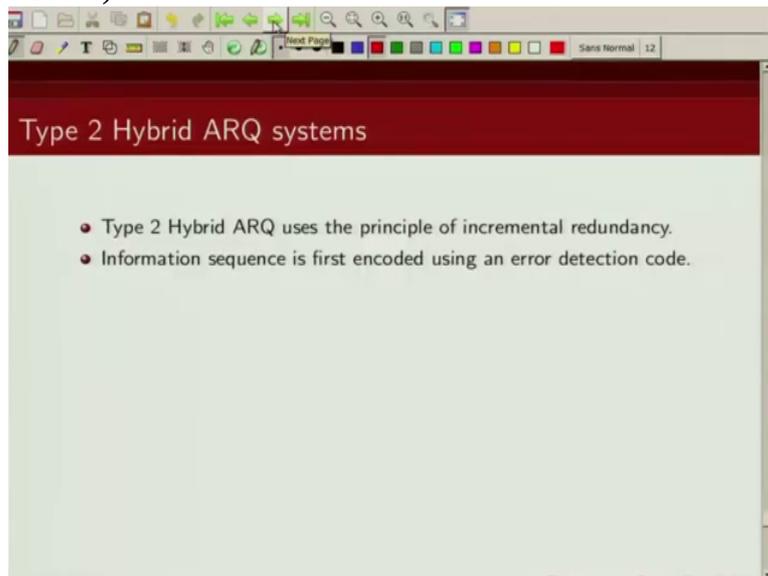
you first send information sequence coded using an error

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detecting code, alright. Now

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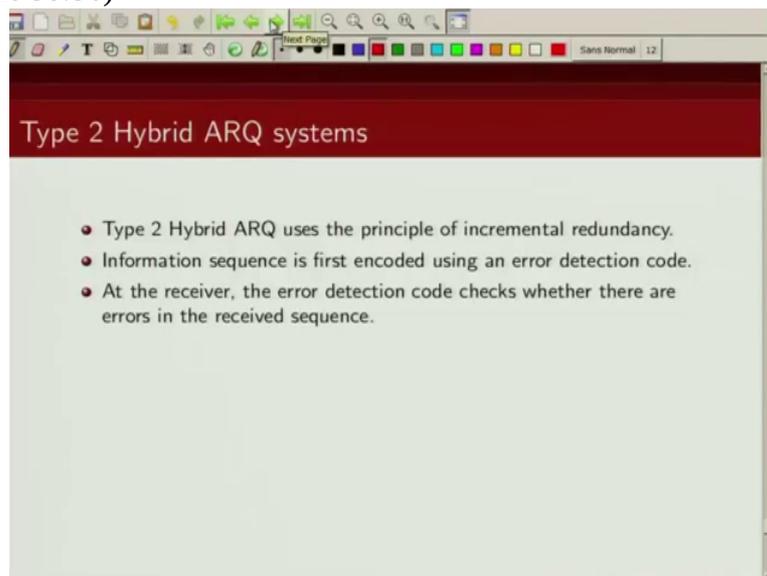
once you receive this packet the error detecting code will try to see if the

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packet has been received correctly or not. Now if the

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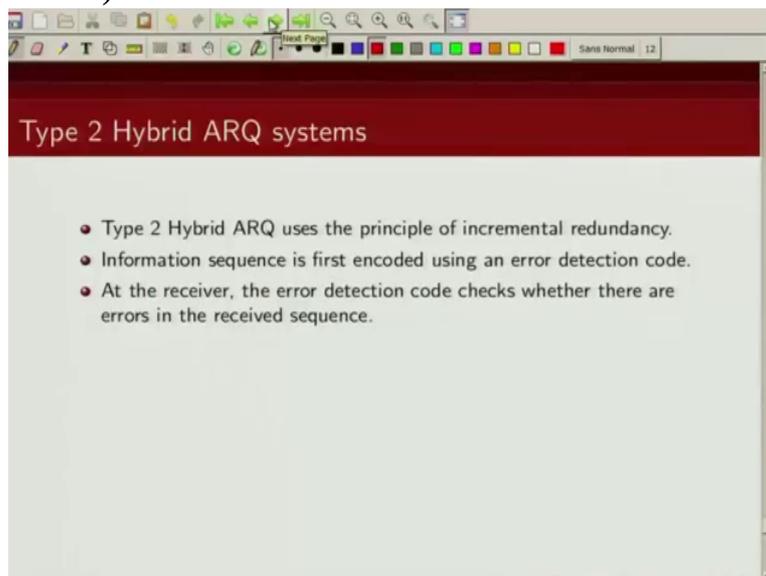
error detecting code does not find any error or it passed the parity constraint checks then the packet will be accepted

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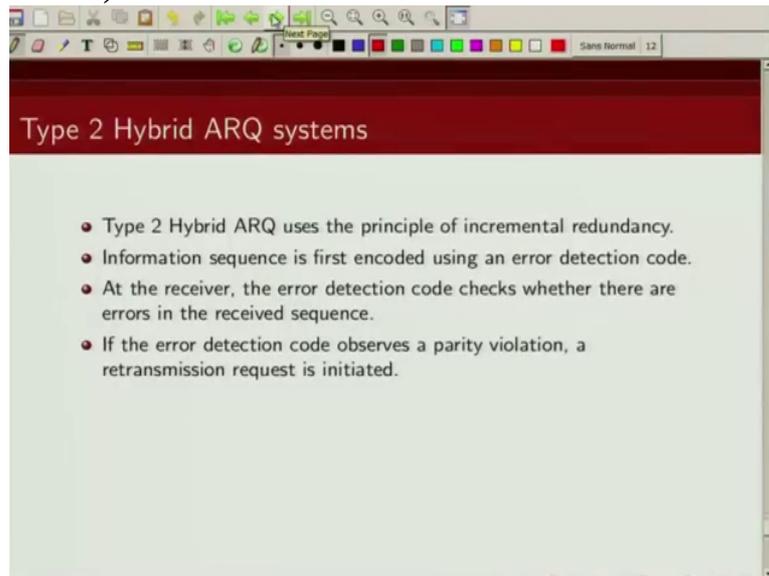
otherwise you will, the receiver is going to send a negative acknowledgement and then the transmitter is going to send a new packet. And what is there in this new packet?

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That is where

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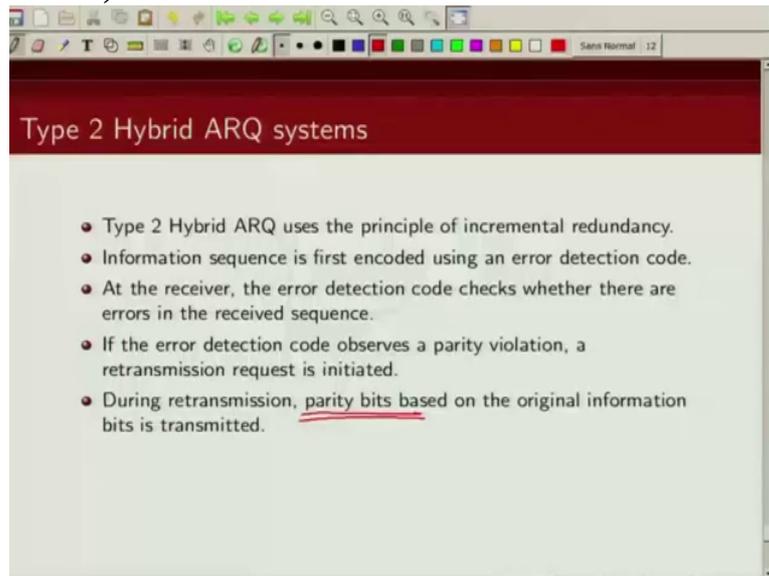
the difference lies. So the new packet that we will send will have send parity bits based on the information sequence. If you remember, during initial transmission we are sending only the information sequence coding

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using error detecting code. Now if that is not received correctly in the first retransmission we are going to send parity bits corresponding those information bits and we are encode them using error detecting code and then transmit over the communication channel. Now at the receiver you already know the information bits that you received in the first transmission. Now we are receiving a parity bit. So the receiver you can make a low, a more powerful lower rate code and decoder can now try to correct the errors.

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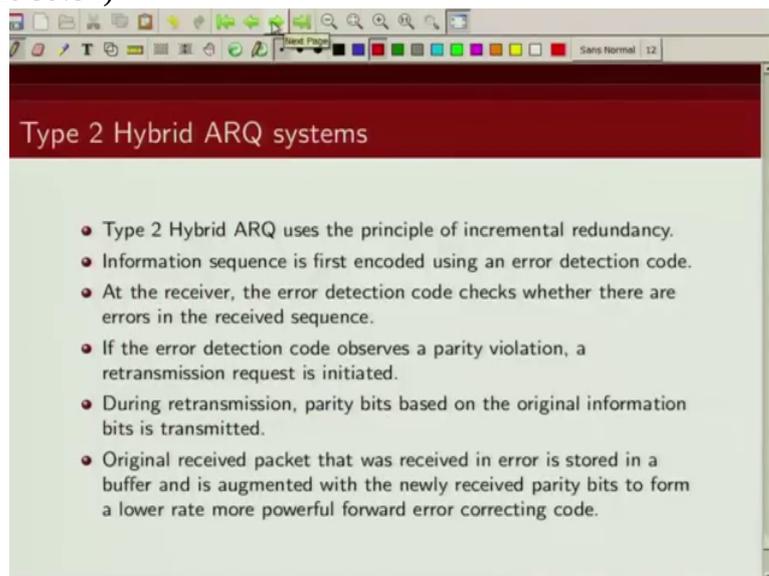


The screenshot shows a presentation slide with a dark red header containing the title "Type 2 Hybrid ARQ systems". Below the header, there is a list of five bullet points describing the system's operation. The slide is displayed in a window with a standard toolbar and a "Sans Normal | 12" font indicator.

- Type 2 Hybrid ARQ uses the principle of incremental redundancy.
- Information sequence is first encoded using an error detection code.
- At the receiver, the error detection code checks whether there are errors in the received sequence.
- If the error detection code observes a parity violation, a retransmission request is initiated.
- During retransmission, parity bits based on the original information bits is transmitted.

So

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This screenshot shows the same presentation slide as above, but with an additional sixth bullet point. The slide content is identical to the previous one, including the title and the first five bullet points.

- Type 2 Hybrid ARQ uses the principle of incremental redundancy.
- Information sequence is first encoded using an error detection code.
- At the receiver, the error detection code checks whether there are errors in the received sequence.
- If the error detection code observes a parity violation, a retransmission request is initiated.
- During retransmission, parity bits based on the original information bits is transmitted.
- Original received packet that was received in error is stored in a buffer and is augmented with the newly received parity bits to form a lower rate more powerful forward error correcting code.

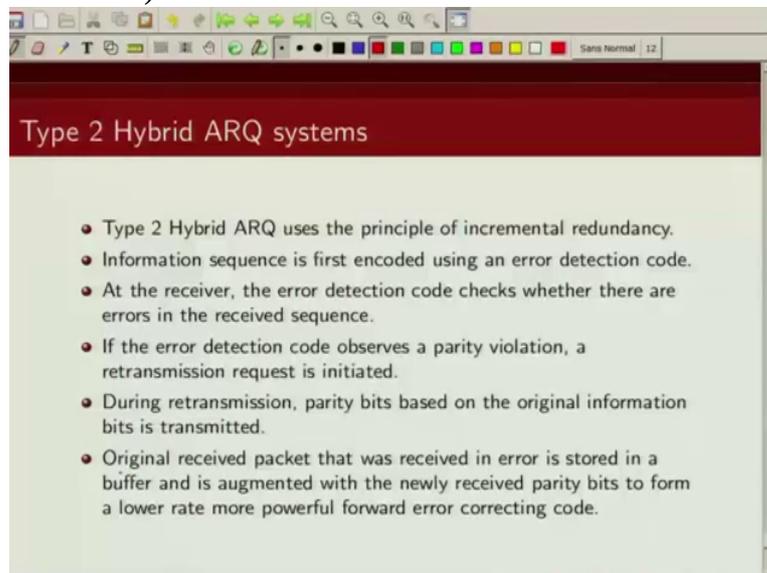
the difference here is, in Type 2 hybrid A R Q scheme each time when we retransmit, we send new set of parity bits and at the receiver we are adding these new set of parity bits to our original received sequences

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to create a more powerful lower rate code. In type 1 kind of schemes we were sending coded packets but then when we are retransmitting we are sending the same coded packet. There also if you want, you could combine those packets together to get a better performance. But the difference here is when we are retransmitting in type 2 hybrid A R Q scheme, each time instance we are sending a new set of parity bits. So

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the original received packet which was received in error is now stored in a buffer and it is augmented with newly received parity bits to form a more powerful lower rate forward error correcting code. So if you take an example, let's say I have a rate one third code, Ok. So let's say

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$R = \frac{1}{3}$

- Type 2 Hybrid ARQ uses the principle of incremental redundancy.
- Information sequence is first encoded using an error detection code.
- At the receiver, the error detection code checks whether there are errors in the received sequence.
- If the error detection code observes a parity violation, a retransmission request is initiated.
- During retransmission, parity bits based on the original information bits is transmitted.
- Original received packet that was received in error is stored in a buffer and is augmented with the newly received parity bits to form a lower rate more powerful forward error correcting code.

this is my block of information bits. Now if pass this block of information bits through a rate one third encoder, what you will get I will get 3 sets

Let's assume this is a systematic code. So this is nothing but this and this are same, these are information sequence, information sequence and these are my parity sequence. Now in Type 2 hybrid A R Q scheme you are sending these information sequence first. Now if the error detecting code finds out

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$R = \frac{1}{3}$

Information sequence
parity

- Type 2 Hybrid ARQ uses the principle of incremental redundancy.
- Information sequence is first encoded using an error detection code.
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- If the error detection code observes a parity violation, a retransmission request is initiated.
- During retransmission, parity bits based on the original information bits is transmitted.
- Original received packet that was received in error is stored in a buffer and is augmented with the newly received parity bits to form a lower rate more powerful forward error correcting code.

that these information bits are in error, in the first retransmission you are going to send these parity bits. At the receiver what you are going to do is you already have these information bits that your received at the first transmission. Now to that you add this parity bit. So now

(Refer Slide Time 01:02:13)

The slide is titled "Type 2 Hybrid ARQ systems". At the top left, there is a handwritten equation $R = \frac{1}{3}$. To its right is a diagram showing a box labeled "Information sequence" with an arrow pointing to a box labeled "parity". A bracket under the "parity" box is labeled "parity". Below the diagram, there is a list of six bullet points:

- Type 2 Hybrid ARQ uses the principle of incremental redundancy.
- Information sequence is first encoded using an error detection code.
- At the receiver, the error detection code checks whether there are errors in the received sequence.
- If the error detection code observes a parity violation, a retransmission request is initiated.
- During retransmission, parity bits based on the original information bits is transmitted.
- Original received packet that was received in error is stored in a buffer and is augmented with the newly received parity bits to form a lower rate more powerful forward error correcting code.

what's going to happen is you are now effectively creating a rate half

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The slide is titled "Type 2 Hybrid ARQ systems". At the top left, there is a handwritten equation $R = \frac{1}{3}$. To its right is a diagram showing a box labeled "Information sequence" with an arrow pointing to a box labeled "parity". A bracket under the "parity" box is labeled "parity". Below the diagram, there is a list of six bullet points:

- Type 2 Hybrid ARQ uses the principle of incremental redundancy.
- Information sequence is first encoded using an error detection code.
- At the receiver, the error detection code checks whether there are errors in the received sequence.
- If the error detection code observes a parity violation, a retransmission request is initiated.
- During retransmission, parity bits based on the original information bits is transmitted.
- Original received packet that was received in error is stored in a buffer and is augmented with the newly received parity bits to form a lower rate more powerful forward error correcting code.

code at the receiver. And now this rate half decoder will try to correct error. Now if this fails again you will ask for, the receiver will ask for retransmission. Then the transmitter is going to send you another set of parity sequence which is different from what was sent earlier. So this new set of priority sequence is now sent here. Now your code has become a rate one third code. So

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$R = \frac{1}{3}$

Information sequence

Parity

$R = \frac{1}{2}$

$R = \frac{1}{3}$

- Type 2 Hybrid ARQ uses the principle of incremental redundancy.
- Information sequence is first encoded using an error detection code.
- At the receiver, the error detection code checks whether there are errors in the received sequence.
- If the error detection code observes a parity violation, a retransmission request is initiated.
- During retransmission, parity bits based on the original information bits is transmitted.
- Original received packet that was received in error is stored in a buffer and is augmented with the newly received parity bits to form a lower rate more powerful forward error correcting code.

in Type 2 hybrid A R Q schemes what you are doing is at each retransmission

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you are sending new parity bits and at the receiver you are adding them to already received information sequence and parity sequence and making it a more powerful lower rate code. So this

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$R = \frac{1}{3}$

Information sequence

parity

$R = \frac{1}{2}$

$R = \frac{2}{3}$

- Type 2 Hybrid ARQ uses the principle of incremental redundancy.
- Information sequence is first encoded using an error detection code.
- At the receiver, the error detection code checks whether there are errors in the received sequence.
- If the error detection code observes a parity violation, a retransmission request is initiated.
- During retransmission, parity bits based on the original information bits is transmitted.
- Original received packet that was received in error is stored in a buffer and is augmented with the newly received parity bits to form a lower rate more powerful forward error correcting code.

is how a type 2 A R Q schemes work, Ok. So with this I am going to conclude my

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discussion on automatic repeat request schemes, thank you