NPTEL Video Course - Special Lecture Series - ACM India Summer School on Programming Language Analysis and Op

Subject Co-ordinator - Girish Bharambe

Co-ordinating Institute - IIT - Madras

Sub-Titles - Available / Unavailable | MP3 Audio Lectures - Available / Unavailable

Lecture 1 - Panel Discussion Q and A - Part 1
Lecture 2 - Panel Discussion Q and A - Part 2
Lecture 3 - Runtime and Linkers - Discussion - 1
Lecture 4 - Runtime and Linkers - Discussion - 2
Lecture 5 - Runtime and Linkers - Linkers
Lecture 6 - Runtime and Linkers - Program Execution - Loader
Lecture 7 - Runtime and Linkers - Static Libraries - Dynamic Linking
Lecture 8 - Code Generation and Backend - An Introduction to LLVM Backend
Lecture 9 - Code Generation and Backend - An Introduction to Loop Backend
Lecture 10 - Code Generation and Backend - Code Generation with LLVM - Part 1
Lecture 11 - Code Generation and Backend - Code Generation with LLVM - Part 2
Lecture 12 - Machine Independent Optimizations - High Level Optimizations - 1
Lecture 13 - Machine Independent optimizations - High Level Optimizations - 2
Lecture 14 - Machine Independent optimizations - High Level Optimizations - 3
Lecture 15 - Machine Independent optimizations - High Level Optimizations - 4
Lecture 16 - Machine Independent optimizations - High Level Optimizations - 5
Lecture 17 - Machine Independent optimizations - High Level Optimizations - 6
Lecture 18 - Machine Architecture and Machine Dependent Optimizations - Machine Architecture - 1
Lecture 19 - Machine Architecture and Machine Dependent Optimizations - Machine Architecture - 2
Lecture 20 - Machine Architecture and Machine Dependent Optimizations - Machine Architecture - 3
Lecture 21 - Machine Architecture and Machine Dependent Optimizations - Register Allocation - 1
Lecture 22 - Machine Architecture and Machine Dependent Optimizations - Register Allocation - 2
Lecture 23 - Machine Architecture and Machine Dependent Optimizations - Register Allocation - 3
Lecture 24 - Machine Architecture and Machine Dependent Optimizations - Instruction Scheduling - 1
Lecture 25 - Machine Architecture and Machine Dependent Optimizations - Instruction Scheduling - 2
Lecture 26 - Machine Dependent Optimizations - Dependence Analysis and Loop transformations - 1
Lecture 27 - Machine Dependent Optimizations - Dependence Analysis and Loop transformations - 2
Lecture 28 - Control-Flow Analyses and Static Single Assignment form - Control Flow Analysis - 1
Lecture 29 - Control-Flow Analyses and Static Single Assignment form - Control Flow Analysis - 2

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Lecture 30 - Control-Flow Analyses and Static Single Assignment form - Depth First Analysis of Flow Graphs
Lecture 31 - Control-Flow Analyses and Static Single Assignment form - Dominators
Lecture 32 - Control-Flow Analyses and Static Single Assignment form - Natural Loops (for Reducible Flow-Grap
Lecture 33 - Control-Flow Analyses and Static Single Assignment form - The Static Single Assignment SSA Form
Lecture 34 - Control-Flow Analyses and Static Single Assignment form - The Static Single Assignment SSA Form
Lecture 35 - Polyhedral Compilation and Loop Optimizations - Polyhedral Compilation I Part 1
Lecture 36 - Polyhedral Compilation and Loop Optimizations - Polyhedral Compilation I Part 2
Lecture 37 - Polyhedral Compilation and Loop Optimizations - Polyhedral Compilation I - Part 3
Lecture 38 - Polyhedral Compilation and Loop Optimizations - Polyhedral Compilation I - Part 4
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Lecture 41 - Polyhedral Compilation and Loop Optimizations - Polyhedral Compilation I - Part 7
Lecture 42 - Polyhedral Compilation and Loop Optimizations - Affine Control Loops
Lecture 43 - Polyhedral Compilation and Loop Optimizations - Siplifying Reductions Revised - 1
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Lecture 45 - Polyhedral Compilation and Loop Optimizations - Siplifying Reductions Revised - 3
Lecture 46 - Polyhedral Compilation and Loop Optimizations - Classical Optimization
Lecture 47 - Polyhedral Compilation and Loop Optimizations - What is Program Analysis
Lecture 48 - Polyhedral Compilation and Loop Optimizations - Live Variable Analysis - 1
Lecture 49 - Polyhedral Compilation and Loop Optimizations - Live Variable Analysis - 2
Lecture 50 - Polyhedral Compilation and Loop Optimizations - Available Expression Analysis
Lecture 51 - Polyhedral Compilation and Loop Optimizations - Common Features of Bit Vector Frameworks
Lecture 52 - Polyhedral Compilation and Loop Optimizations - The Birth of a Compiler - 1
Lecture 53 - Polyhedral Compilation and Loop Optimizations - The Birth of a Compiler - 2
Lecture 54 - Polyhedral Compilation and Loop Optimizations - The Structure of Modern Compiler Modern Challeng
Lecture 55 - Polyhedral Compilation and Loop Optimizations - Conclusion
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