

NPTEL Video Lecture Topic List - Created by LinuXpert Systems, Chennai

NPTEL Video Course - Electronics and Communication Engineering - VLSI Data Conversion Circuits

Subject Co-ordinator - Dr. Shanthi Pavan

Co-ordinating Institute - IIT - Madras

Sub-Titles - Available / Unavailable | MP3 Audio Lectures - Available / Unavailable

Lecture 1 - Introduction to Data Conversion
Lecture 2 - Sampling-1
Lecture 3 - Sampling-2
Lecture 4 - Nonidealities in Samples
Lecture 5 - Noise due to Sampling
Lecture 6 - Distortion in a Sampling Switch
Lecture 7 - Gate Boosted Switches-1
Lecture 8 - Gate Boosted Switches-2
Lecture 9 - Charge Injection
Lecture 10 - S/H Characterization-1
Lecture 11 - S/H Characterization-2
Lecture 12 - FFTs and Leakage
Lecture 13 - Spectral Windows-1
Lecture 14 - Spectral Windows-2
Lecture 15 - ADC/DAC Definition
Lecture 16 - Quantization Noise-1
Lecture 17 - Quantization Noise-2
Lecture 18 - Over Sampling and Noise Shaping
Lecture 19 - Delta-Sigma Modulation-1
Lecture 20 - Delta-Sigma Modulation-2
Lecture 21 - Linearized Analysis
Lecture 22 - Stability of Delta Sigma Modulators
Lecture 23 - High Order DSMs
Lecture 24 - NTF Design and Tradeoffs
Lecture 25 - Single bit Modulators
Lecture 26 - Loop Filter Architectures
Lecture 27 - Continuous-time Delta Sigma Modulation
Lecture 28 - Implicit Anti-aliasing
Lecture 29 - Modulators with NRZ and Impulsive DACs

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- Lecture 30 - High Order CTDSMs
- Lecture 31 - CTDm Design
- Lecture 32 - Excess Loop Delay (ELD)
- Lecture 33 - ELD Compensation
- Lecture 34 - Effect of Clock Jitter on CTDSMs-1
- Lecture 35 - Effect of Clock Jitter on CTDSMs-2
- Lecture 36 - Dynamic Range Scaling
- Lecture 37 - Simulation of CTDSMs
- Lecture 38 - Integrator Design-1
- Lecture 39 - Integrator Design-2
- Lecture 40 - Flash ADC Design
- Lecture 41 - Latches and Metastability
- Lecture 42 - Offset in a Latch-1
- Lecture 43 - Offset in a Latch-2 Auto Zeroing
- Lecture 44 - Auto Zeroing-2
- Lecture 45 - Auto Zeroing-3
- Lecture 46 - Auto Zeroing in flash ADCs
- Lecture 47 - Flash ADCs Case Study
- Lecture 48 - Flash ADC Case Study
- Lecture 49 - Flash ADC in a Delta Sigma Loop
- Lecture 50 - DAC Basics
- Lecture 51 - Binary and Thermometer DACs
- Lecture 52 - Segmented DACs
- Lecture 53 - Optimal DAC Segmentation
- Lecture 54 - DAC Nonlinearities
- Lecture 55 - Current Steering DACs-1
- Lecture 56 - Current Steering DACs-2
- Lecture 57 - DAC Mismatches in DSMS
- Lecture 58 - Calibration and Randomization
- Lecture 59 - Dynamic Element Matching-1
- Lecture 60 - Dynamic Element Matching-2