NPTEL Video Course - Electrical Engineering - NOC:VLSI Signal Processing

Subject Co-ordinator - Prof. Mrityunjay Chakraborty

Co-ordinating Institute - IIT - Kharagpur

Sub-Titles - Available / Unavailable | MP3 Audio Lectures - Available / Unavailable

Lecture 30 - Hardware Efficient 2 - Parallel FIR Filters
Lecture 31 - Hardware Efficient 3 - Parallel FIR Filters
Lecture 32 - Introduction to First Level Architectures
Lecture 33 - 2's Complement Number Systems
Lecture 34 - Multiplication of Two Binary Numbers
Lecture 35 - Carry Ripple and Carry Save Array
Lecture 36 - Bit Serial Multipliers
Lecture 37 - Bit Serial Digital Filters
Lecture 38 - Baugh Wooley Multiplier
Lecture 39 - Distributed Arithmetic